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Evaluation Report No. TRPVP11144/25E/002

Commission testing for Power Conversion System

Applicant: **Jiangsu Weiheng Intelligent Technology Co., Ltd.**
Sheng Xiang, Yaxi Community, Luoshe Town, Huishan District, 214000
Wuxi, Jiangsu Province, PEOPLE'S REPUBLIC OF CHINA

File No.: PVP11144/25E-002

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Evaluation Report

File No.: PVP11144/25E-002

Report No.: TRPVP11144/25E/002

Applicant	Jiangsu Weiheng Intelligent Technology Co., Ltd. Sheng Xiang, Yaxi Community, Luoshe Town, Huishan District, 214000 Wuxi, Jiangsu Province, PEOPLE'S REPUBLIC OF CHINA
Manufacturer	Jiangsu Weiheng Intelligent Technology Co., Ltd. Sheng Xiang, Yaxi Community, Luoshe Town, Huishan District, 214000 Wuxi, Jiangsu Province, PEOPLE'S REPUBLIC OF CHINA
Factory	Jiangsu Weiheng Intelligent Technology Co., Ltd. Sheng Xiang, Yaxi Community, Luoshe Town, Huishan District, 214000 Wuxi, Jiangsu Province, PEOPLE'S REPUBLIC OF CHINA
Order No.	HZHZPVP11144/25E
Date of Application [YYYY-MM-DD]	2025-12-30
Product(s)	Power Conversion System
Model type(s)	TAA-105kW-01, TAA-105kW-11, TAA-125kW-01, TAA-125kW-11
Type of examination	Commission testing only
Certification program:	BOS-P-01 Rev. 01
Certification fundamental(s)	EN 50549-2:2019+A1:2023 EN 50549-10:2022
Testing Period [YYYY-MM-DD] start/end	2026-01-06/2026-03-17
Testing Laboratory	Dongguan BALUN Testing Technology Co., Ltd. Room 104, 204, 205, Building 1, No. 6, Industrial South Road, Songshan Lake District, Dongguan, Guangdong, China
Annex(es)	Annex 1: Test report no. BL-DG2630055-202 (295 pages)

Test results listed in this test report refer exclusively to the mentioned test sample.

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The submitted test samples as described in the reports hereunder are in compliance with the requirements / are tested according to the requirements:

EN 50549-2:2019+A1:2023 "Requirements for generating plants to be connected in parallel with distribution networks - Part 2: Connection to a MV distribution network — Generating plants up to and including Type B";
EN 50549-10:2022 "Requirements for generating plants to be connected in parallel with distribution networks - Part 10: Tests for conformity assessment of generating units".



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TESTING
CNAS L14701

TEST REPORT

Application number: PVP11144/25E-002

Applicant: Jiangsu Weiheng Intelligent Technology Co., Ltd.
Sheng Xiang, Yaxi Community, Luoshe Town,
Huishan District, 214000 Wuxi, Jiangsu Province,
PEOPLE'S REPUBLIC OF CHINA

Address:

Equipment Type: Power Conversion System

Model Name Under Test: TAA-125kW-11

Model Name: TAA-105kW-01, TAA-105kW-11,
TAA-125kW-01, TAA-125kW-11

Brand Name: 

Ratings: See copy of marking label and model list.

Test Standard: **EN 50549-2:2019+A1:2023**
EN 50549-10:2022

Sample Arrival Date: Jan. 05, 2026

Test Date: Jan. 06, 2026 to Mar. 17, 2026

Date of Issue: Mar. 23, 2026

ISSUED BY:

Dongguan BALUN Testing Technology Co., Ltd.

Tested by: Leo Sun

Leo Sun

Checked by: Jin Huang

Jin Huang

Approved by: Tao Zheng

Tao Zheng



Revision History

Version	Issue Date	Revisions Content
<u>Rev. 01</u>	<u>Mar. 23, 2026</u>	<u>Initial Issue</u>

-Note:

For reports with CNAS mark, the ownership of accreditation is under "BALUN". "TÜV NORD" is not owing the accreditation.

Summary of testing:

All the tests results confirmed to the requirements of the standard.

Tests performed (name of test and test clause):

5.2 Normal operating range
 5.3 Immunity to disturbances
 5.4 Active response to frequency deviation
 5.5 Power specification and response to voltage variations
 5.6 Voltage related active power reduction - P(U)
 5.7 EMC and power quality
 5.8 Interface protection
 5.9 Connection and reconnection
 5.10 Active power reduction on set point
 5.11 Remote information exchange
 5.12 Requirements regarding single fault tolerance of interface protection system and interface switch

Testing location:

Dongguan BALUN Testing Technology Co., Ltd.
 Room 104, 204, 205, Building 1, No. 6, Industrial South Road, Songshan Lake District, Dongguan, Guangdong, China

☒ **The product fulfils the requirements of EN 50549-2: 2019+A1:2023, EN 50549-10:2022**

Copy of marking plate:

The following nameplates are based on actual shipment and the parameter information will not change.

**Power Conversion System**

Model:	TAA-105kW-01
S/N:	
DC Terminal	
Operating Range	615-950V*
Max. Voltage	950V
Max. Current	171A
AC Terminal	
Nominal Voltage	230V / 400V, 3W+N+PE / 3W+PE
Nominal Operating Frequency	50/60Hz
Rated Power	105kW
Max. Apparent Power	115.5kVA
Max. Current	167A
Power Factor Range	1(lagging)-1(leading)
General	
Protective Class	I
Operating Temperature Range	-30 - +55℃
Enclosure	IP20
Overvoltage Category	II(DC),III(AC)
Altitude	2000m
*When the system wiring is 3P+N+PE, the DC full load operating voltage is 680-950V.	



Jiangsu Weiheng Intelligent Technology
Co., Ltd.

Made in China

Sheng Xiang, Yaxi Community, Luoshe Town, Huishan District, 214000, Wuxi City,
Jiangsu Province, P.R. China

**Power Conversion System**

Model:	TAA-105kW-11
S/N:	
DC Terminal	
Operating Range	615-950V*
Max. Voltage	950V
Max. Current	171A
AC Terminal	
Nominal Voltage	230V / 400V, 3W+N+PE / 3W+PE
Nominal Operating Frequency	50/60Hz
Rated Power	105kW
Max. Apparent Power	115.5kVA
Max. Current	167A
Power Factor Range	1(lagging)-1(leading)
General	
Protective Class	I
Operating Temperature Range	-30 - +55℃
Enclosure	IP20
Overvoltage Category	II(DC),III(AC)
Altitude	2000m
*When the system wiring is 3P+N+PE, the DC full load operating voltage is 680-950V.	



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Power Conversion System

Model:	TAA-125kW-01
S/N:	

DC Terminal	
Operating Range	615-950V*
Max. Voltage	950V
Max. Current	203A

AC Terminal	
Nominal Voltage	230V / 400V, 3W+N+PE / 3W+PE
Nominal Operating Frequency	50/60Hz
Rated Power	125kW
Max. Apparent Power	138kVA
Max. Current	200A
Power Factor Range	1(lagging)-1(leading)

General	
Protective Class	I
Operating Temperature Range	-30 - +55 °C
Enclosure	IP20
Overvoltage Category	II(DC),III(AC)
Altitude	2000m
*When the system wiring is 3P+N+PE,the DC full load operating voltage is 680-950V.	



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Power Conversion System

Model:	TAA-125kW-11
S/N:	

DC Terminal	
Operating Range	615-950V*
Max. Voltage	950V
Max. Current	203A

AC Terminal	
Nominal Voltage	230V / 400V, 3W+N+PE / 3W+PE
Nominal Operating Frequency	50/60Hz
Rated Power	125kW
Max. Apparent Power	138kVA
Max. Current	200A
Power Factor Range	1(lagging)-1(leading)

General	
Protective Class	I
Operating Temperature Range	-30 - +55 °C
Enclosure	IP20
Overvoltage Category	II(DC),III(AC)
Altitude	2000m
*When the system wiring is 3P+N+PE,the DC full load operating voltage is 680-950V.	



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Note: Fixed PF mode conducted under the PF from 0.8 lagging to 0.8 leading; All other reactive power mode conducted from $Q_{max.} +75000\text{Var}$ to $Q_{min.} -75000\text{Var}$.

Test item particulars.....:	
Equipment mobility	Permanent connection
Operating condition.....:	Continuous
Class of equipment.....:	Class I
Mass of equipment (kg)	See model list.
Possible test case verdicts:	
- test case does not apply to the test object.....:	N/A
- test object does meet the requirement.....:	P (Pass)
- test object was not evaluated for the requirement:	N/E
- test object does not meet the requirement.....:	F (Fail)

General remarks:	
"(See Enclosure #)" refers to additional information appended to the report. "(See appended table)" refers to a table appended to the report. Throughout this report a ☐ comma / ☒ point is used as the decimal separator.	
The application for obtaining a CB Test Certificate includes more than one factory location and a declaration from the Manufacturer stating that the sample(s) submitted for evaluation is (are) representative of the products from each factory has been provided :	☐ Yes ☒ Not applicable
When differences exist; they shall be identified in the General product information section.	
Name and address of Manufacturer (ies)..... :	Jiangsu Weiheng Intelligent Technology Co., Ltd. Sheng Xiang, Yaxi Community, Luoshe Town, Huishan District, 214000 Wuxi, Jiangsu Province, PEOPLE'S REPUBLIC OF CHINA
Name and address of factory (ies)	Jiangsu Weiheng Intelligent Technology Co., Ltd. Sheng Xiang, Yaxi Community, Luoshe Town, Huishan District, 214000 Wuxi, Jiangsu Province, PEOPLE'S REPUBLIC OF CHINA

General product information:

The unit under test (EUT) is power conversion system. During inverter, which convert the variable DC power generated from the Batteries to the stable utility AC power which can be fed into the commercial electrical grid or off-grid load. When charging, the grid converts the alternating current into direct current into the battery through the Power Conversion System.

The maximum ambient temperature allowed by the manufacturer's specifications is 55°C (derating at 45°C). The unit is an AC/DC convert module, additional automatic disconnection protection devices should be installed in final whole system according to the local requirement.

Differences of the models:

The engineering samples of Power Conversion System products:

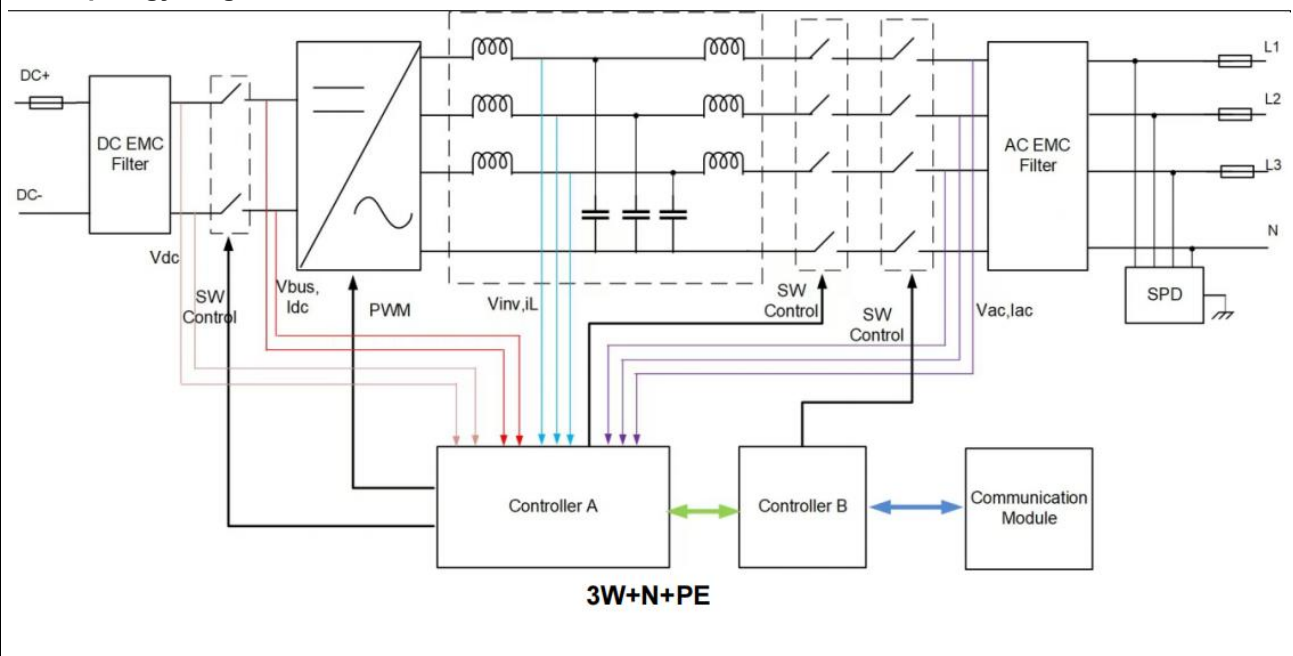
The models TAA-125kW-01, TAA-125kW-11, TAA-105kW-01, TAA-105kW-11 are same as the hardware and software, expect the output voltage is different due to be adjusted by software. The difference between having the suffix '-11' and having the suffix '-01' is the difference in the way the wires come out and the terminals on the DC side. Among them, having the suffix '-01' adopts the front outlet mode, while having the suffix '-11' adopts the rear outlet mode.

If no specification, all the tests are conducted on model TAA-125kW-11.

Software Version: V100B000D000

Hardware Version: A00

The topology diagram:



Model list:

Model or Type designation	TAA-105kW-01 TAA-105kW-11	TAA-125kW-01 TAA-125kW-11
DC terminal		
Voltage Range [Vd.c.]	615-950(3W+PE)/650-950(3W+N+PE)	
Full load voltage Range [Vd.c.]	615-950(3W+PE)/680-950(3W+N+PE)	
Max. Current [Ad.c.]	171	203
Overvoltage Category (OVC)	II	
AC terminal		
Rated Voltage [Va.c.]	230V / 400V, 3W+N+PE / 3W+PE	
Rated Frequency [Hz]	50Hz/60Hz	
Rated Charge/Discharge Power [kW]	105	125
Max. Charge/Discharge Apparent Power [kVA]	115.5	138
Max. Charge/Discharge Apparent Current [Aa.c.]	167	200
Power Factor cosφ [λ]	0.8 lagging – 0.8 leading	
Overvoltage Category (OVC)	III	
System		
Type of converter	Non-Isolated	
Protective Class	I	
Enclosure Protection (IP)	IP20	
Pollution degree	External PD3, Internal PD2	
Operating Temperature Range [°C]	-30 ~ 55	
Size W*H*D [mm]	For model TAA-105kW-01 and TAA-125kW-01: 520*232*725 For model TAA-105kW-11 and TAA-125kW-11: 520*232*785	
Weight [kg]	For model TAA-105kW-01 and TAA-125kW-01: 69 For model TAA-105kW-11 and TAA-125kW-11: 74.5	

EN 50549-2:2019 & EN 50549-10:2022			
Clause(s) / subclause(s) of EN 50549-2:2019	Clause(s) / subclause(s) of EN 50549-10:2022	Remark	Verdict
4.4.2 Operating frequency range	5.2.1 Frequency operating range		P
4.4.3 Minimal requirement for active power delivery at underfrequency	5.2.1 Frequency operating range		P
4.4.4 Continuous Operating voltage range	5.2.2 Voltage operating range		P
4.5.2 Rate of change of frequency (ROCOF) immunity	5.3.1 Immunity to disturbances - Rate of change of frequency (ROCOF)		P
4.5.3.2 Generating plant with non-synchronous generating technology	5.3.3 Immunity to disturbances - Fault ride through, over-voltage (OVRT) and under-voltage (UVRT)		P
4.5.3.3 Generating plant with synchronous generating technology	5.3.3 Immunity to disturbances - Fault ride through, over-voltage (OVRT) and under-voltage (UVRT)		N/A
4.5.4 Over-voltage ride through (OVRT)	5.3.3 Immunity to disturbances - Fault ride through, over-voltage (OVRT) and under-voltage (UVRT)		P
4.6.1 Power response to over-frequency	5.4.3.2 Power response to over-frequencies		P
4.6.2 Power response to under-frequency	5.4.3.3 Power response to under-frequencies		P
4.7.2.2 Voltage support by reactive power, Capabilities	5.5.1 Power capabilities assessment		P
4.7.2.3 Voltage support by reactive power, control modes	5.5.2 Voltage support by reactive power - test to determine the reactive power control modes		P
4.7.2.3.2 Setpoint control modes	5.5.2.3 Verification procedure for Set point control		P
4.7.2.3.3 Voltage related control mode	5.5.2.5 Verification procedure for power related control modes for reactive power		P
4.7.2.3.4 Power related control mode	5.5.2.5 Verification procedure for power related control modes for reactive power		P
4.7.4.2.2 Zero current mode for converter connected generating technology	5.5.2.5 Verification procedure for power related control modes for reactive power		P

4.9.3 Requirements on voltage and frequency protection	5.8.3 Verification procedure for generating plants to be connected to a LV distribution network with Interface protection as internal device. 5.8.4 Verification procedure for generating plants to be connected to a LV distribution network with Interface protection as dedicated device		P
4.9.4.2 4.9.4 Means to detect island situation	5.8.6 Islanding detection		P
4.10.2 Automatic reconnection after tripping	5.9.3 Automatic reconnection after tripping		P
4.10.3 Starting to generate electrical power	5.9.4 Starting to generate electrical power		P
4.11.1 Ceasing active power	5.10 Active power reduction on set point		P
4.11.2 Reduction of active power on set point	5.10 Active power reduction on set point		P
4.12 Remote information exchange	5.11 Remote information exchange		P
4.13 Requirements regarding single fault tolerance of interface protection system and interface switch	5.12 Requirements regarding single fault tolerance of interface protection system and interface switch		P

General remarks:

The uncertainty of the measurement was not given in test report. Please see below information of measurement uncertainty:

- - Voltage

Type A uncertainty: $u(X_i)=0.012$ combined standard uncertainty: $U_c (\%) = 0.29$

Expansion uncertainty: $U (\%) = 0.58$ (factor $k=2$)

- - Current

Type A uncertainty: $u(X_i)=0.011$ combined standard uncertainty: $U_c (\%) = 0.49$

Expansion uncertainty: $U (\%) = 0.98$ (factor $k=2$)

- - Power

Type A uncertainty: $u(X_i)=0.017$ combined standard uncertainty: $U_c (\%) = 0.29$

Expansion uncertainty: $U (\%) = 0.59$ (factor $k=2$)

- - harmonics

Error in Measurement: 0.04%

- - flicker

Error in Measurement: 0.04%

- - current transducer

Expansion uncertainty:

DC current $U_{rel} = 0.7\%$ (factor $k=2$) AC current $U_{rel} = 0.7\%$ (factor $k=2$)

combined standard uncertainty: $U_c (\%) = 0.49$

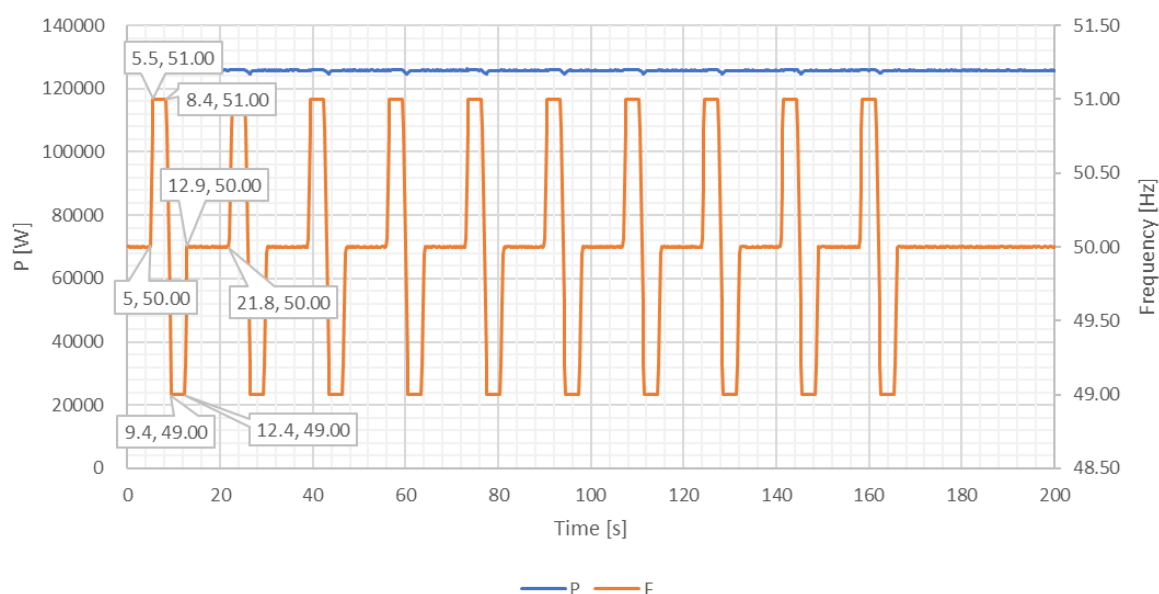
EN 50549-10:2022		
Clause	Test items	Remark
5.2	Normal operating range	
5.2.1	Frequency operating range	P
5.2.2	Voltage operating range	P

5.2.1 5.2.2		Frequency operating range Voltage operating range			P
Setting values	Over-voltage [V]:		253.0		
	Under-voltage [V]:		207.0		
	Over-frequency [Hz]:		51.5		
	Under-frequency [Hz]:		47.5		
- Test 1: U = 230.0 V; f = 47.5 Hz; P = 1.00 S_n; cosφ = 1; - Test 2: U = 230.0 V; f = 48.5 Hz; P = 1.00 S_n; cosφ = 1; - Test 3: U = 230.0 V; f = 51.5 Hz; P = 1.00 S_n; cosφ = 1; - Test 4: U = 230.0 V; f = 47.0 Hz; P = 1.00 S_n; cosφ = 1; - Test 5: U = 230.0 V; f = 52.0 Hz; P = 1.00 S_n; cosφ = 1; - Test 6-1: U = 207.0 V; f = 50.0 Hz; P = 1.00 S_n; cosφ = 1; Test 6-2: U = 230.0 V; f = 50.0 Hz; P = 1.00 S_n; cosφ = 1; Test 6-3: U = 253.0 V; f = 50.0 Hz; P = 1.00 S_n; cosφ = 1; Test 6-4: U = 230.0 V; f = 50.0 Hz; P = 1.00 S_n; cosφ = 1;					
Test :					
Test sequence	Voltage [V]	Frequency [Hz]	Output power [W]	Cos φ	
1	230.1	47.50	125012	0.9999	
2	230.4	48.50	125041	0.9999	
3	230.7	51.50	125167	0.9999	
4	230.3	47.00	124985	0.9999	
5	230.8	52.00	125199	0.9998	
6	206.9	50.00	124025	0.9999	
	230.1	50.00	125003	0.9999	
	253.1	50.00	124770	0.9999	
	230.2	50.00	124995	0.9999	
Note:					
Operating points test 1, test 2, test 3, test 4, test 5 and test 6 must be kept for at least 30 minutes.					
During the tests the interface protection and activating active power response to over/under frequency and voltage shall be disabled.					
Operation at reduced power is allowed during test 6, equal to the maximum power that can be supplied on reaching the maximum output current limit (P ≥ 0.85 S _n).					
The power supplied by the primary energy source shall be kept stable with a maximum error of 2 % x S _n					

EN 50549-10:2022		
Clause	Test items	Remark
5.3	Immunity to disturbances	
5.3.1	Rate of change of frequency (ROCOF)	P
5.3.2	Phase jump	P
5.3.3	Fault ride through, over-voltage (OVRT) and under-voltage (UVRT)	P

5.3.1 Rate of change of frequency (RoCoF)

P



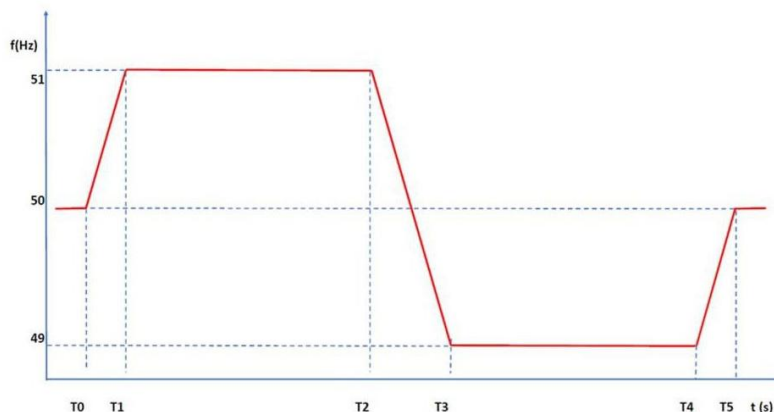
	Start Frequency	Change	End Frequency	Confirm no trip
T0	50Hz	Hold 3s	50Hz	No trip
T0 to T1	50Hz	+2Hz/sec	51Hz	No trip
T1 to T2	51Hz	Hold 3s	51Hz	No trip
T2 to T3	51Hz	-2Hz/sec	49Hz	No trip
T3 to T4	49Hz	Hold 3s	49Hz	No trip
T4 to T5	49Hz	+2Hz/sec	50Hz	No trip
T5	50Hz	Hold 6s	50Hz	No trip

Note:

The test shall be conducted on a grid simulator able to control the frequency within the required range and with the required dynamic. Initial status of the generating unit before the test shall be as follows:

The generating unit, connected to a grid simulator, shall be operating between 100 % and 80 % of the nominal power with an active factor equal to 1.

Then the test shall be conducted following the sequence represented in the Figure 4 below.



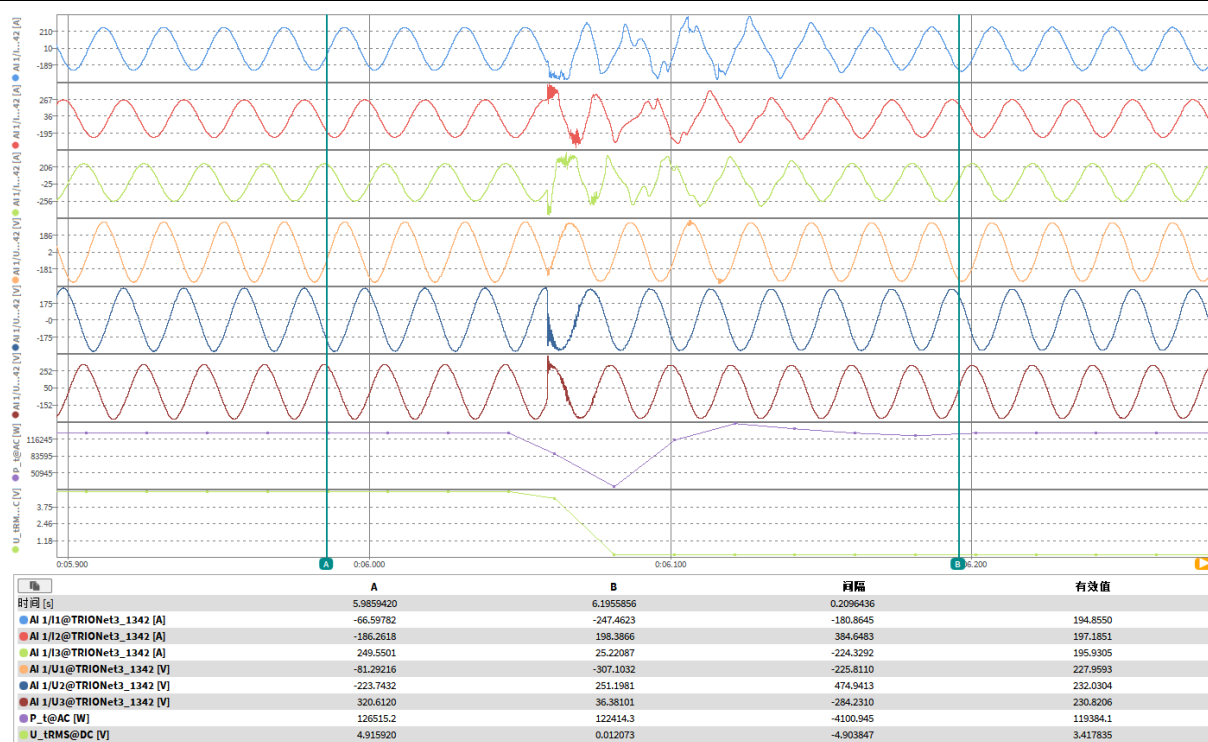
During the Overfrequency and Underfrequency verification, once the higher, respectively, lower values of frequency have been reached, the frequency shall be maintained for a time period equal or longer than 3 s which normally corresponds to the time requested by the system to stabilize during a real event.

During the test, the frequency, RMS voltage, RMS current and active and reactive power measured at the output terminals of the generating unit shall be recorded as 10 cycle average values.

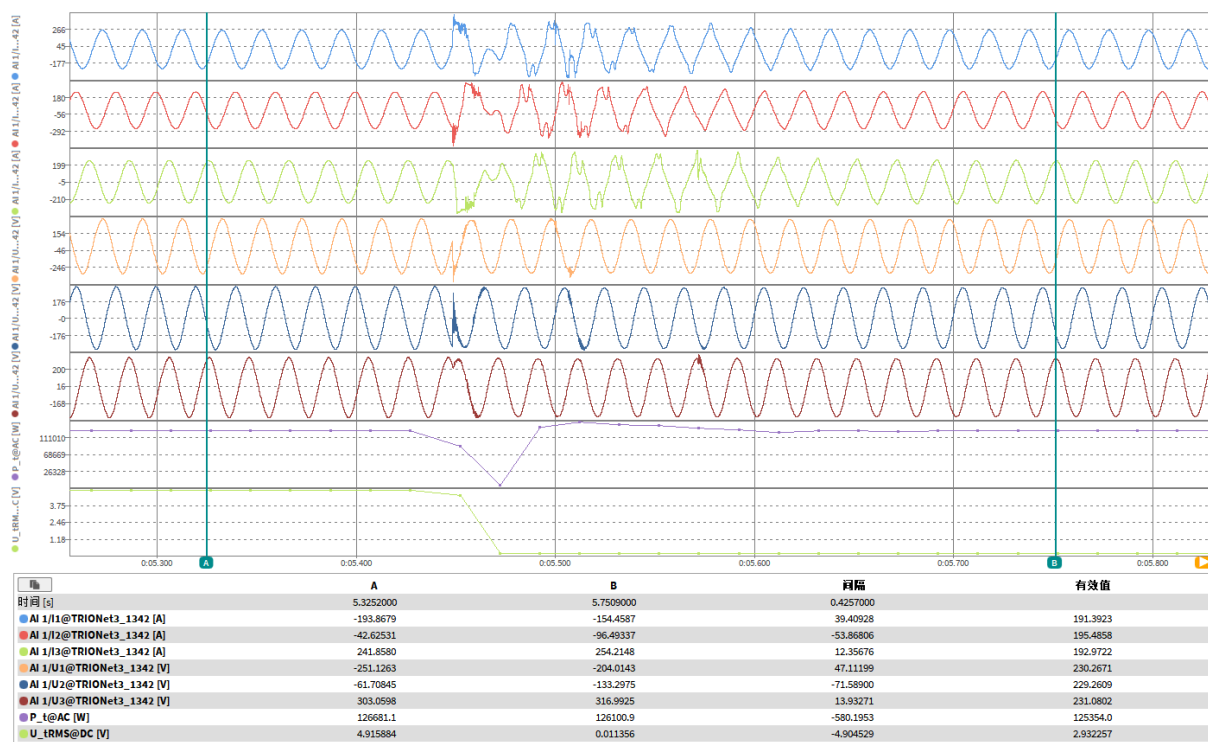
During the tests the interface protection and activating active power response to over/under frequency and voltage shall be disabled.

5.3.2 Phase jump				P
Test results				
Test 1:				
P _{EMAX} in %	Phase shift angle		Result an	
100% ±5%	90° to 0°	L1-N	Unit normal use, no damage, no hazard.	
		L2-N	Unit normal use, no damage, no hazard.	
		L3-N	Unit normal use, no damage, no hazard.	
		L1-L2	Unit normal use, no damage, no hazard.	
		L2-L3	Unit normal use, no damage, no hazard.	
		L3-L1	Unit normal use, no damage, no hazard.	
		L1+L2+L3	Unit normal use, no damage, no hazard.	
Test 2:				
P _{EMAX} in %	Phase shift angle		Result	
100% ±5%	0° to 90°	L1-N	Unit normal use, no damage, no hazard.	
		L2-N	Unit normal use, no damage, no hazard.	
		L3-N	Unit normal use, no damage, no hazard.	
		L1-L2	Unit normal use, no damage, no hazard.	
		L2-L3	Unit normal use, no damage, no hazard.	
		L3-L1	Unit normal use, no damage, no hazard.	
		L1+L2+L3	Unit normal use, no damage, no hazard.	
Test 3:				
P _{EMAX} in %	Phase shift angle		Result	
100% ±5%	180° to 0°	L1-N	Unit shut down, no damage, no hazard.	
		L2-N	Unit shut down, no damage, no hazard.	
		L3-N	Unit shut down, no damage, no hazard.	
		L1-L2	Unit normal use, no damage, no hazard.	
		L2-L3	Unit shut down, no damage, no hazard.	
		L3-L1	Unit shut down, no damage, no hazard.	
		L1+L2+L3	Unit shut down, no damage, no hazard.	
Test 4:				
P _{EMAX} in %	Phase shift angle		Result	
100% ±5%	0° to 180°	L1-N	Unit shut down, no damage, no hazard.	
		L2-N	Unit normal use, no damage, no hazard.	
		L3-N	Unit normal use, no damage, no hazard.	
		L1-L2	Unit shut down, no damage, no hazard.	
		L2-L3	Unit normal use, no damage, no hazard.	
		L3-L1	Unit shut down, no damage, no hazard.	
		L1+L2+L3	Unit shut down, no damage, no hazard.	
Limit	The generation must be no damage			

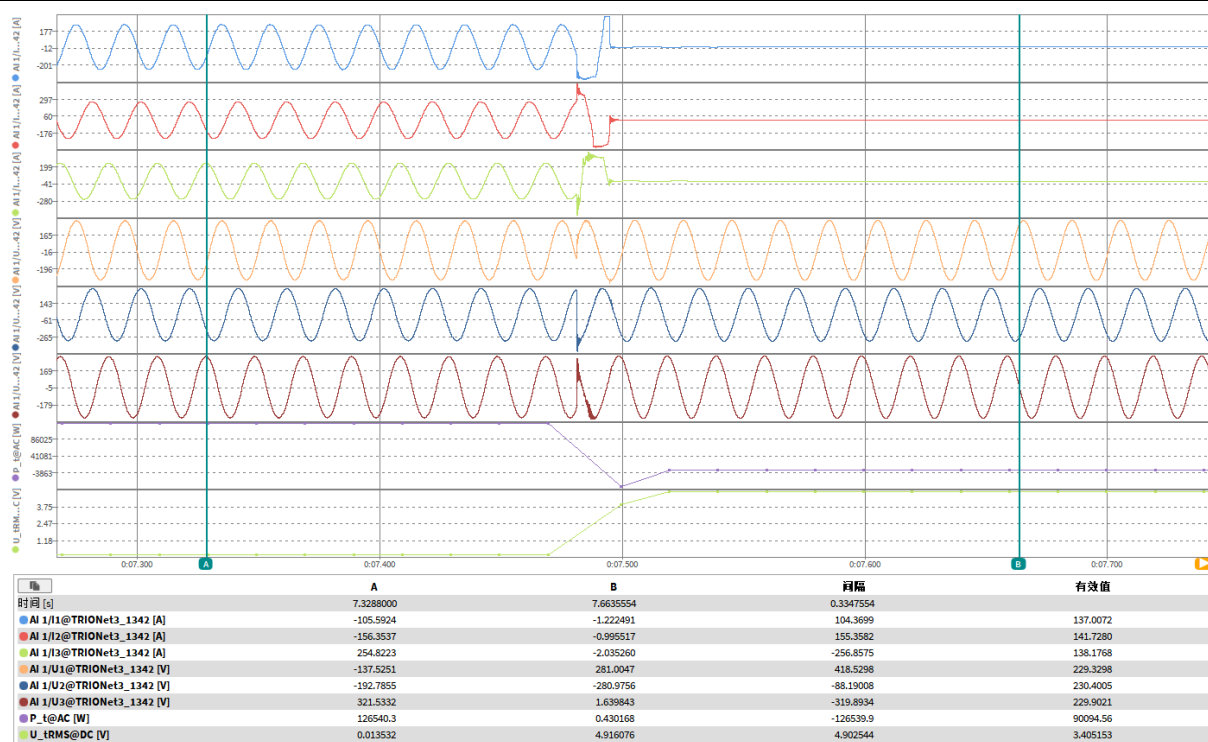
Test 1: 90° to 0°



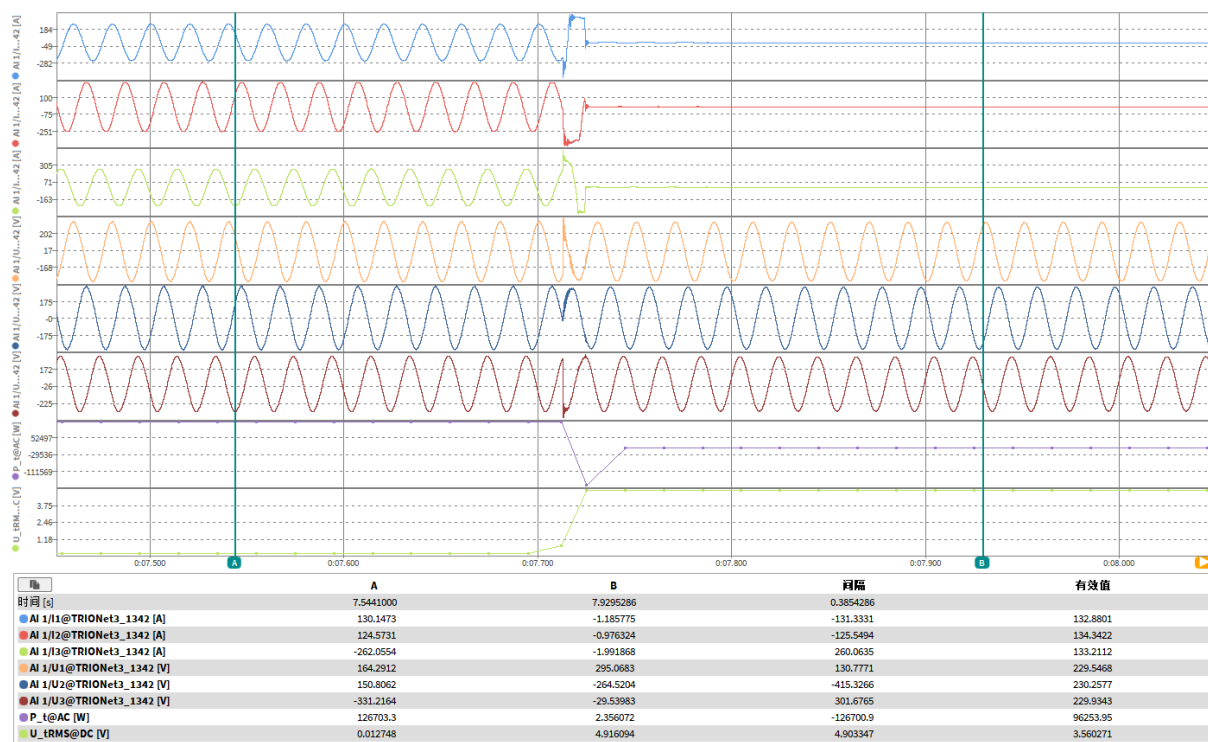
Test 2: 0° to 90°



Test 3: 180° to 0°



Test 4: 0° to 180°



5.3.3 Fault ride through, over-voltage (OVRT) and under-voltage (UVRT)

P

General:

To define the effect of the generating unit on the voltage magnitude, the short circuit power of the test setup shall be in the range of 2 times to 40 times the nominal power of the generating unit. The X/R ratio of the test impedance shall be at least 3.

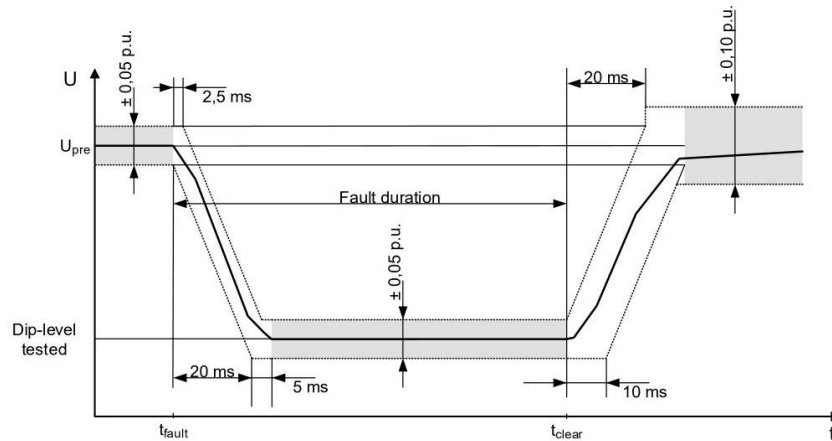


Figure 5 - Tolerances of the positive sequence voltage for the undervoltage event

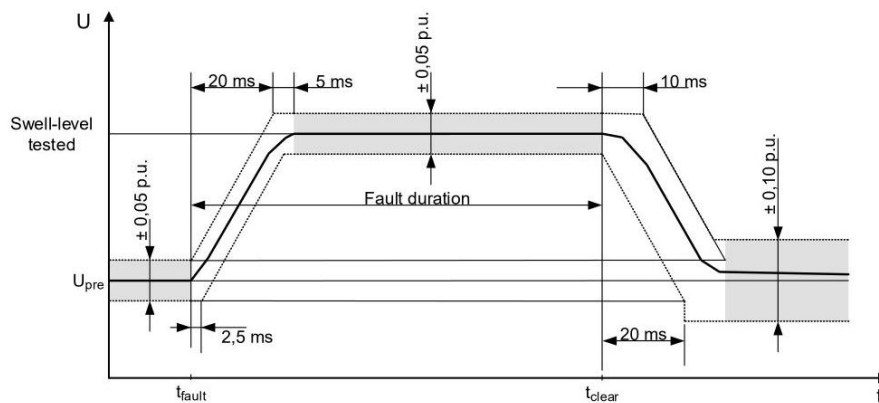


Figure 6 - Tolerances of the positive sequence voltage for the overvoltage event

Each test according to Table 4 and Table 5 represents one point in the voltage-time diagram to be verified e.g. Figure 6, 7, 8 of EN 50549-1:2019 and EN 50549-2:2019. If the voltage-time diagram includes vertical sections as Figure 7 and 8 of EN 50549-1:2019 and EN 50549-2:2019, the voltage level and duration according to Table 4 and Table 5 shall be chosen in a way that the most severe voltage level at this duration shall be tested. For each vertical section at least one test shall be included. If necessary additional tests shall be added to Table 4 and Table 5.

If the test is intended to provide evidence of controlled short circuit current contribution, at least the voltage levels for undervoltage according to Table 4 and at least the voltage levels for overvoltage according to Table 5 shall be tested, with each level tested for (at least) a symmetric event and an asymmetric event (simulated two- phase fault).

Table 4 specifies magnitudes for the undervoltage testing. Voltage levels refer to no-load-tests. U_{min} is the minimum voltage the generating unit can ride through, $UVRC$ is the under voltage range capability calculated as $U_{UVRC} = U_n - U_{min}$ (see Figure 7).

Table 5 specifies magnitudes for the overvoltage testing. Voltage levels refer to no-load-tests. U_{max} is the maximum voltage the generating unit can ride through, $UOVRC$ is the over voltage range capability

calculated as $U_{OVRC} = U_{max} - U_n$ (see Figure 7)

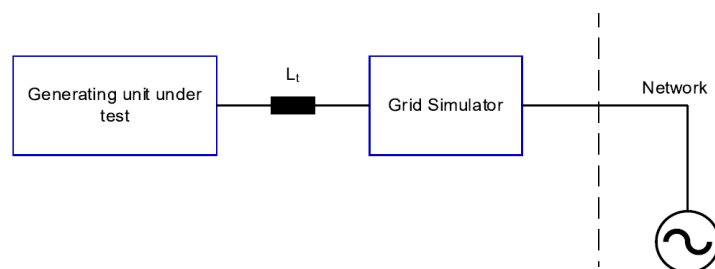


Figure A.3 — Example test setup with AC grid simulator

Regarding phase relationship in the test event:

- 1) Symmetrical three-phase fault (Table 4, tests VD1, 2, 3, 4, 5), see Figure A.4.

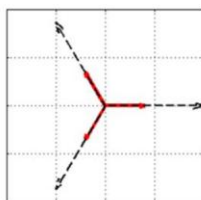


Figure A.4 — Vectorial representation of a symmetrical three-phase fault

- 2) asymmetric two-phase fault (Table 4, Tests VD 6, 7, 8, 9, 10), see Figure A.5.

A fault in the medium voltage or high voltage grid is causing a variation in LV in voltage amplitude as well as phase relationship (the case considered envisages a Dy transformer to connect the generator to the MV line or secondary station).

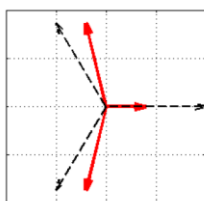


Figure A.5 — Vectorial representation of an asymmetric two-phase fault

During the asymmetric two-phase fault, the residual amplitude of the 3 voltages and phase shifts shall conform to the values given in the following Table A.1 and Table A.2.

Graph of LVRT test one (Type A+B)										
List of tests	Volta ge dip to (p.u.)	Limit for fault duratio n [ms]	Set Q [Q/P _n]	K- factor	Duration fault [ms]			Recovery time after fault		Result
					Duration [ms]	Postive reaictive current(A) at 60ms	Negative reactive current(A) at 60ms	Active curren t [ms]	Reacti ve curren t [ms]	
symmetrical Three phase fault - UVRT										
VD1.0 (P = 0)	0.03	≥ 260	--	--	359	--	--	--	--	--
VD1.1 (P = 1)	0.03	≥ 260	0	2	360	199.274	2.309	127	27	P
VD1.2 (P = 0.2)	0.03	≥ 260	0	2	360	193.061	1.669	110	27	P
VD2.0 (P = 0)	0.25	≥950	--	--	1051	--	--	--	--	--
VD2.1.a (P = 1)	0.25	≥950	0	2	1050	198.552	-0.501	127	29	P
VD2.1.b (P = 1)	0.25	≥950	Max. Ind	ZCM (zero current mode)	1050	-0.633	-0.226	163	9044	P
VD2.2 (P = 0.2)	0.25	≥950	0	2	1049	194.853	-1.651	109	28	P
VD3.0 (P = 1)	0.5	≥1800	--	--	1907	--	--	--	--	--
VD3.1 (P = 1)	0.5	≥1800	0	2	1899	200.307	-1.398	129	21	P
VD3.2.a (P = 0.2)	0.5	≥1800	0	2	1907	196.517	-0.670	116	24	P
VD3.2.b (P = 0.2)	0.5	≥1800	Max. Ind	2	1899	77.511	-1.234	81	9001	P
VD4.0 (P = 1)	0.75	≥2700	--	--	2800	--	--	--	--	--
VD4.1 (P = 1)	0.75	≥2700	0	2	2798	106.637	1.778	133	22	P
VD4.2.a (P = 0.2)	0.75	≥2700	Max. Ind	2	2803	18.682	-0.089	76	9021	P

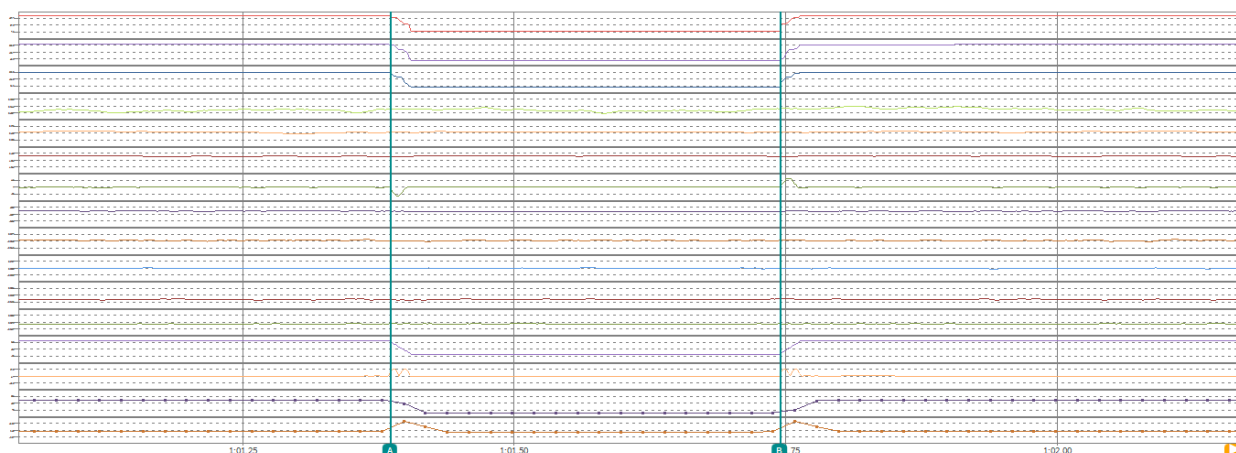
Graph of LVRT test one (Type A+B)										
List of tests	Voltage dip to (p.u.)	Limit for fault duration [ms]	Set Q [Q/P _n]	K-factor	Duration fault [ms]			Recovery time after fault		Result
					Duration [ms]	Positive reactive current(A) at 60ms	Negative reactive current(A) at 60ms	Active current [ms]	Reactive current [ms]	
VD4.2.b (P = 0.2)	0.75	≥2700	Max. cap	2	2801	194.789	1.061	120	9017	P
VD4.2.c (P = 0.2)	0.75	≥2700	0	4	2800	201.040	1.563	113	307	P
VD5.0 (P = 1)	0.9	≥60020	--	--	60175	--	--	--	--	--
VD5.1 (P = 1)	0.9	≥60020	0	2	60122	--	--	63	--	P
VD5.2 (P = 0.2)	0.9	≥60020	0	2	60121	--	--	69	--	P
Asymmetrical Two phase fault - UVRT										
VD6.0 (P = 0)	0.03	≥ 260	--	--	279	--	--	--	--	--
VD6.1 (P = 1)	0.03	≥ 260	0	2	362	97.874	-106.444	135	28	P
VD6.2 (P = 0.2)	0.03	≥ 260	0	2	359	99.551	-104.178	116	30	P
VD7.0 (P = 0)	0.25	≥950	--	--	1051	--	--	--	--	--
VD7.1 (P = 1)	0.25	≥950	0	2	1051	101.361	-98.851	187	31	P
VD7.2 (P = 0.2)	0.25	≥950	0	2	1050	100.637	-99.340	124	30	P
VD8.0 (P = 1)	0.5	≥1800	--	--	1900	--	--	--	--	--
VD8.1 (P = 1)	0.5	≥1800	0	2	1901	103.612	-94.260	135	28	P
VD8.2 (P = 0.2)	0.5	≥1800	0	2	1899	103.306	-95.646	123	30	P

Graph of LVRT test one (Type A+B)										
List of tests	Voltage dip to (p.u.)	Limit for fault duration [ms]	Set Q [Q/P _n]	K-factor	Duration fault [ms]			Recovery time after fault		Result
					Duration [ms]	Positive reactive current(A) at 60ms	Negative reactive current(A) at 60ms	Active current [ms]	Reactive current [ms]	
VD9.0 (P = 1)	0.75	≥2700	--	--	2804	--	--	--	--	--
VD9.1 (P = 1)	0.75	≥2700	0	2	2804	55.990	-47.850	137	32	P
VD9.2.a (P = 0.2)	0.75	≥2700	0	2	2800	53.492	-49.055	137	30	P
VD9.2.b (P = 0.2)	0.75	≥2700	0	4	2799	108.134	-99.243	137	31	P
VD10.0 (P = 1)	0.9	≥60020	--	--	60131	--	--	--	--	--
VD10.1 (P = 1)	0.9	≥60020	0	2	60119	--	--	55	--	P
VD10.2 (P = 0.2)	0.9	≥60020	0	2	60121	--	--	67	--	P
Symmetrical Three phase fault - OVRT										
VS1.0 (P = 0)	1.25	≥ 120	--	--	221	--	--	--	--	--
VS1.1 (P = 1)	1.25	≥ 120	0	2	220	-94.870	1.880	116	25	P
VS1.2 (P = 0.2)	1.25	≥ 120	0	2	220	-99.797	2.983	99	25	P
VS2.1.0 (P = 0)	1.20	≥ 5020	--	--	5119	--	--	--	--	--
VS2.1.1.a (P = 1)	1.20	≥ 5020	0	2	5029	-74.756	1.027	224	19	P
VS2.1.1.b (P = 1)	1.20	≥ 5020	Max. Ind	2	5116	-195.373	1.561	170	9055	P
VS2.1.2 (P = 0.2)	1.20	≥ 5020	0	2	5121	-76.79	0.12	111	20	P

Graph of LVRT test one (Type A+B)										
List of tests	Voltage dip to (p.u.)	Limit for fault duration [ms]	Set Q [Q/P _n]	K-factor	Duration fault [ms]			Recovery time after fault		Result
					Duration [ms]	Positive reactive current(A) at 60ms	Negative reactive current(A) at 60ms	Active current [ms]	Reactive current [ms]	
VS2.2.0 (P = 0)	1.15	≥60020	--	--	60122	--	--	--	--	--
VS2.2.1 (P = 1)	1.15	≥60020	0	2	60120	-51.39	1.12	123	33	P
VS2.2.2 (P = 0.2)	1.15	≥60020	0	2	60121	-55.67	0.66	110	29	P
Asymmetrical Two phase fault - OVRT										
VS3.0 (P = 0)	1.25	≥ 120	--	--	219	--	--	--	--	--
VS3.1 (P = 1)	1.25	≥ 120	0	2	223	-44.716	-52.171	134	42	P
VS3.2 (P = 0.2)	1.25	≥ 120	0	2	221	-48.274	-52.398	122	18	P
VS4.1.0 (P = 0)	1.20	≥ 5020	--	--	5119	--	--	--	--	--
VS4.1.1 (P = 1)	1.20	≥ 5020	0	2	5121	-30.234	-45.092	159	37	P
VS4.1.2 (P = 0.2)	1.20	≥ 5020	0	2	5120	-35.554	-46.168	129	41	P
VS4.2.0 (P = 0)	1.15	≥60020	--	--	60122	--	--	--	--	--
VS4.2.1 (P = 1)	1.15	≥60020	0	2	60119	-21.600	-31.319	130	20	P
VS4.2.2 (P = 0.2)	1.15	≥60020	0	2	60121	-28.110	-34.104	127	20	P

Test - VD1.0

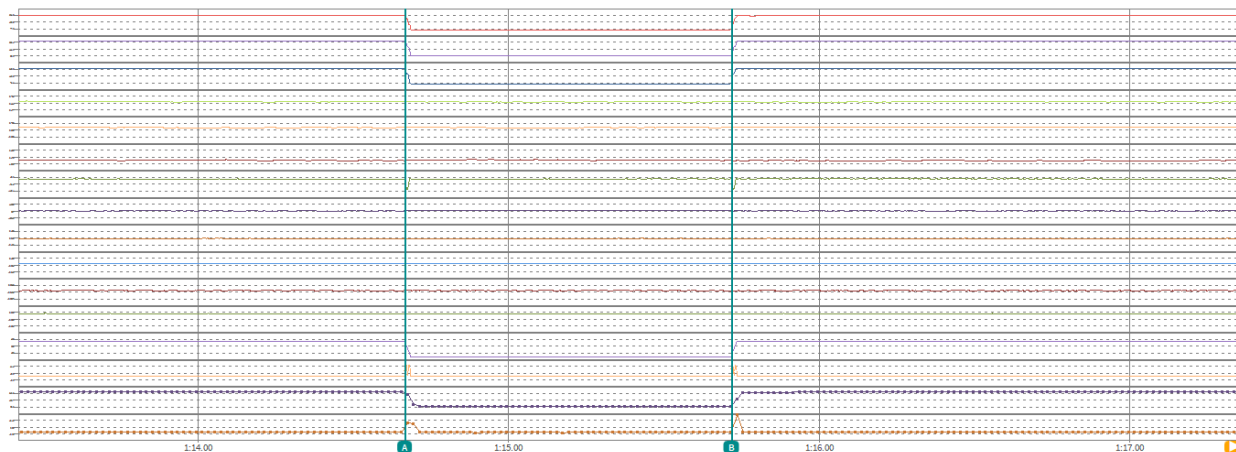
Duration fault time



时间 [s]	A	B	间隔	有效值
1:01.3863	1:01.7450	0.3587		
U1_rms_rc@AC [V]	230.0587	6.854591	-223.2041	40.02799
U2_rms_rc@AC [V]	229.9682	6.873887	-223.0943	42.74545
U3_rms_rc@AC [V]	229.9180	6.794465	-223.1236	37.48965
I1_rms_rc@AC [A]	0.485359	0.489830	4.471e-3	0.479506
I2_rms_rc@AC [A]	0.441550	0.448337	6.787e-3	0.450894
I3_rms_rc@AC [A]	1.996819	1.989185	-7.634e-3	2.012302
P_t_rc@AC [W]	-0.574475	0.916313	1.490789	20.55702
Q_t_rc@AC [var]	672.3078	19.93379	-652.3740	110.9779
I_fund_Q_SYM+_rc@AC [A]	6.729e-4	9.401e-4	2.672e-4	2.042e-3
I_fund_Q_SYM-_rc@AC [A]	-4.422e-3	-6.206e-4	3.801e-3	2.526e-3
I_fund_P_SYM+_rc@AC [A]	-6.141e-4	4.019e-3	4.633e-3	2.252e-3
I_fund_P_SYM-_rc@AC [A]	-2.440e-4	2.564e-3	2.808e-3	2.737e-3
U_fund_SYM+_rc@AC [V]	398.3321	11.79817	-386.5339	58.55221
U_fund_SYM-_rc@AC [V]	0.496997	0.047760	-0.449237	10.35392
U+ [V]	230.0510	6.774820	-223.2762	66.31571
U- [V]	0.508734	0.208751	-0.299983	4.011146

Test - VD2.0

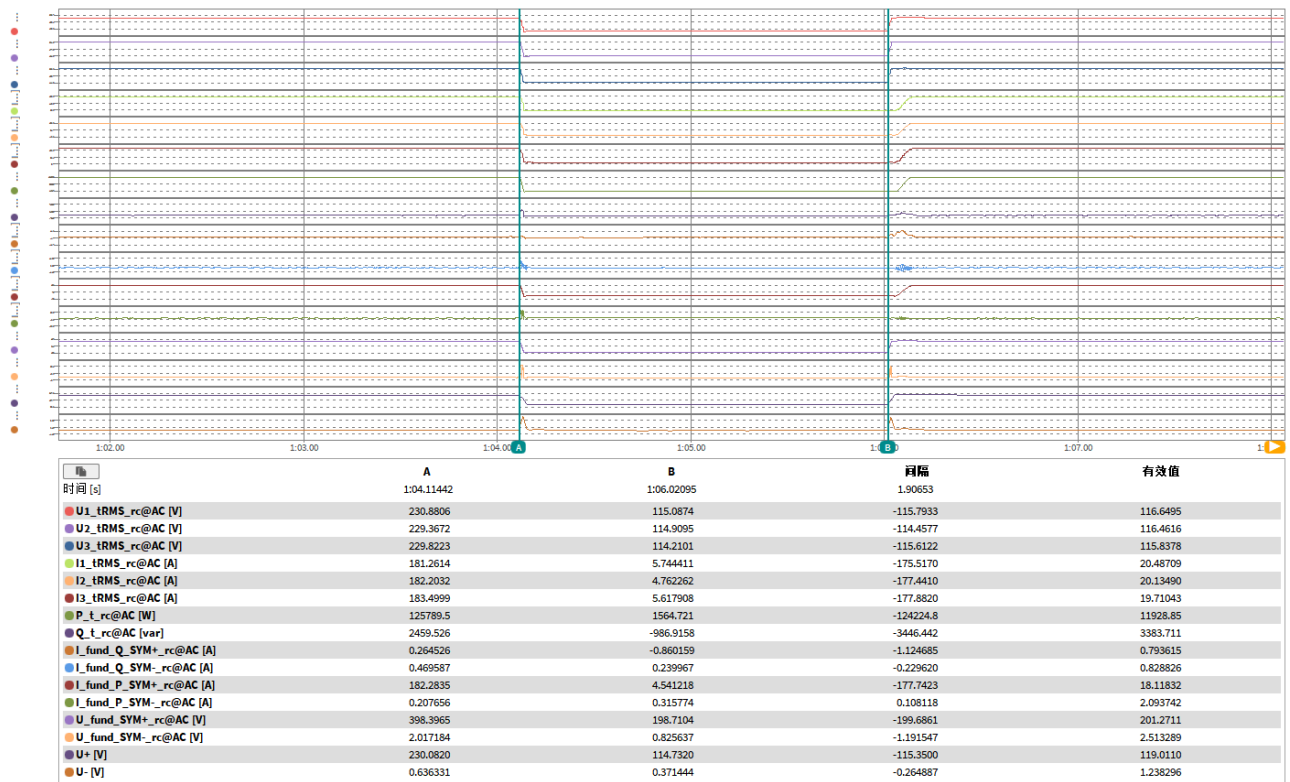
Duration fault time



时间 [s]	A	B	间隔	有效值
1:14.66814	1:15.71902	1.05088		
U1_rms_rc@AC [V]	229.8473	57.17825	-172.6690	61.94251
U2_rms_rc@AC [V]	229.6883	57.45596	-172.2323	62.53096
U3_rms_rc@AC [V]	230.5212	57.53783	-172.9833	61.39456
I1_rms_rc@AC [A]	0.468202	0.470498	2.296e-3	0.465323
I2_rms_rc@AC [A]	0.429317	0.441164	0.011847	0.4316421
I3_rms_rc@AC [A]	2.017953	2.019765	1.812e-3	2.036744
P_t_rc@AC [W]	-0.724717	1.832750	2.557467	9.772290
Q_t_rc@AC [var]	671.4045	-168.4527	-839.8573	179.5056
I_fund_Q_SYM+_rc@AC [A]	1.540e-4	-1.941e-3	-2.095e-3	2.325e-3
I_fund_Q_SYM-_rc@AC [A]	-3.115e-3	-3.420e-4	2.773e-3	2.485e-3
I_fund_P_SYM+_rc@AC [A]	7.180e-4	-3.111e-3	-3.829e-3	2.078e-3
I_fund_P_SYM-_rc@AC [A]	-1.575e-3	-8.213e-4	7.538e-4	2.226e-3
U_fund_SYM+_rc@AC [V]	398.3778	99.38510	-298.9927	105.9729
U_fund_SYM-_rc@AC [V]	0.396578	0.382506	-0.014072	4.678620
U+ [V]	230.0383	57.37086	-172.6674	70.53176
U- [V]	0.649518	0.122257	-0.527261	1.753499

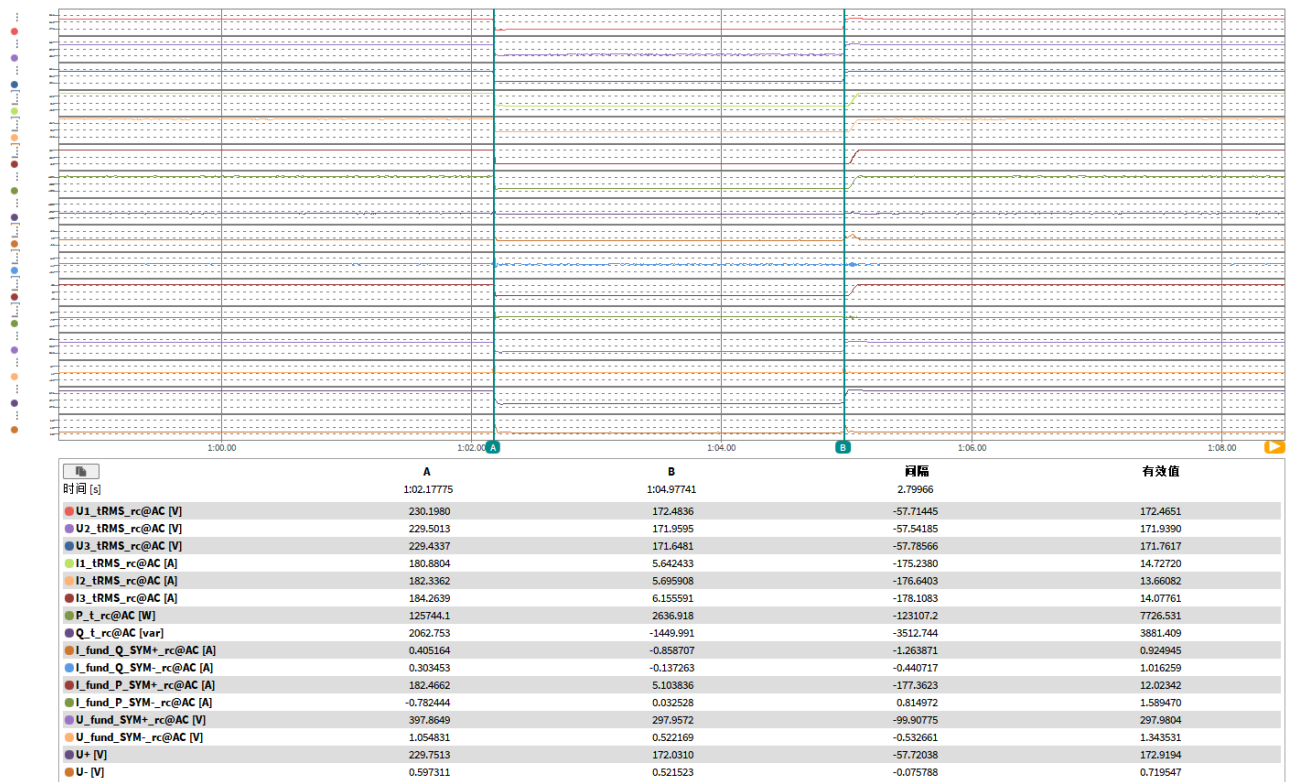
Test – VD3.0

Duration fault time



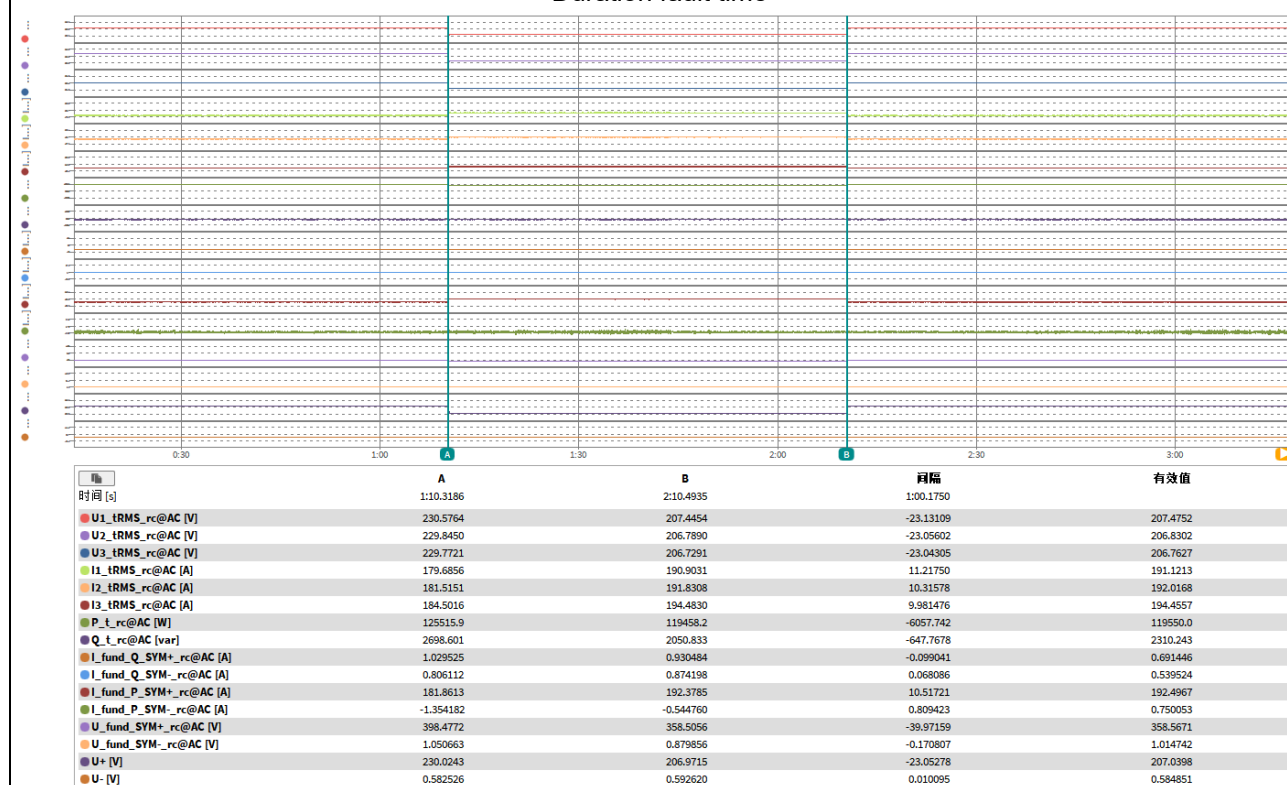
Test – VD4.0

Duration fault time



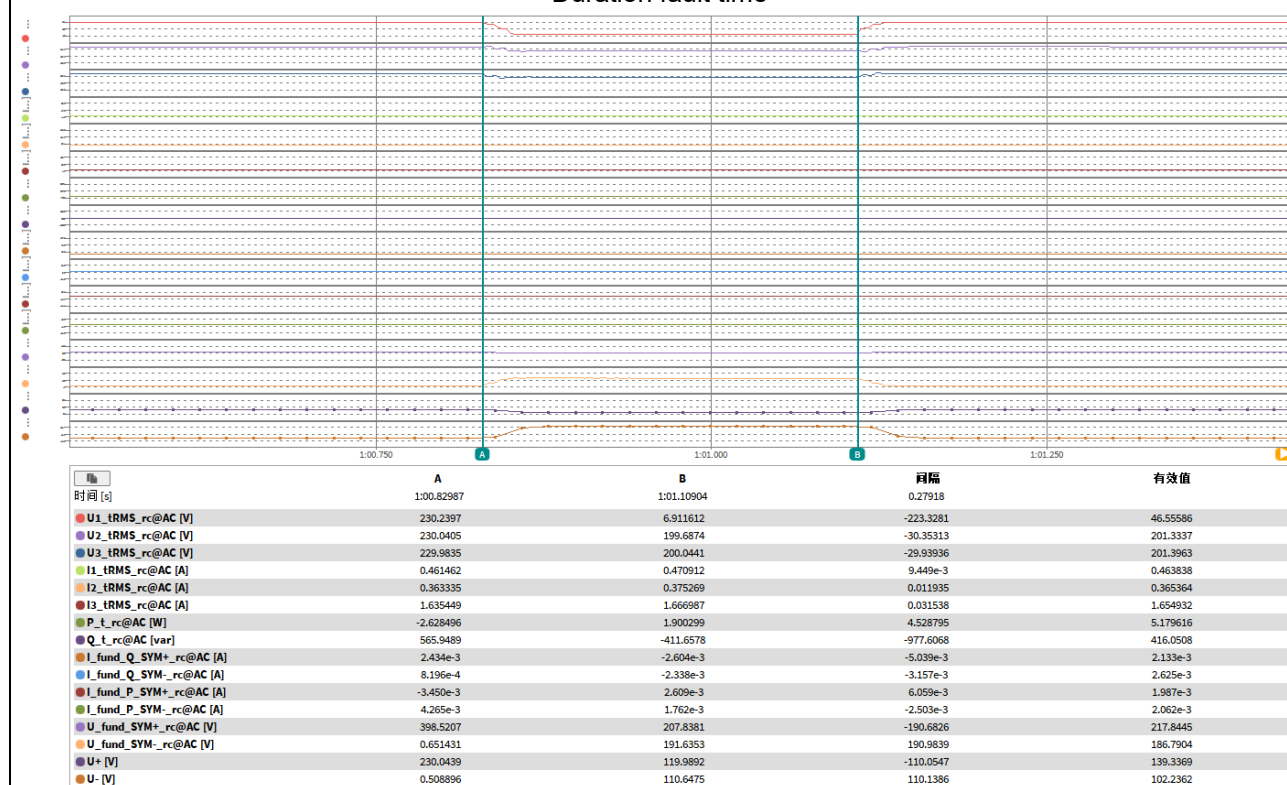
Test – VD5.0

Duration fault time



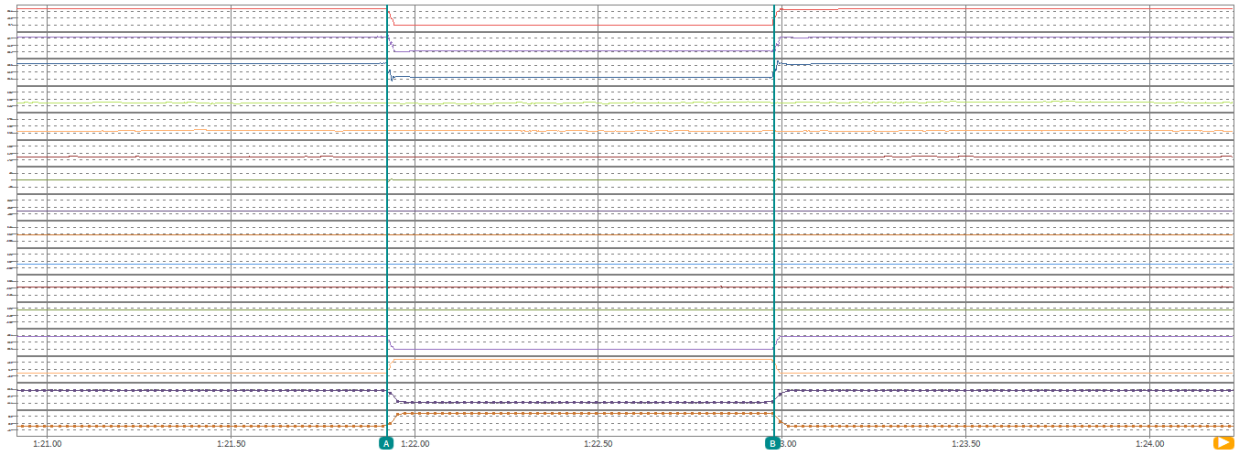
Test – VD6.0

Duration fault time



Test – VD7.0

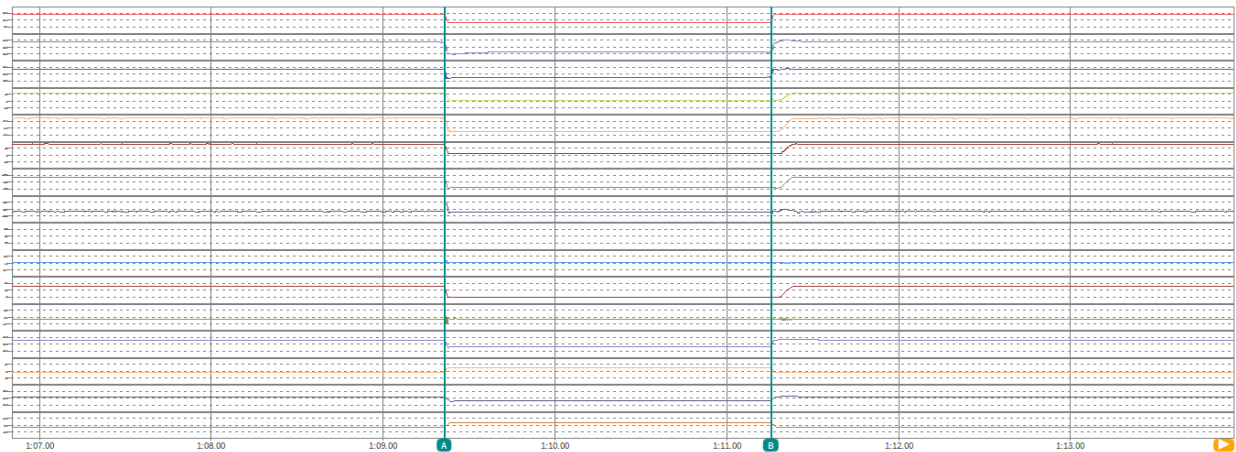
Duration fault time



时间 [s]	A	B	间隔	有效值
1:21.92588	1:22.97673	1:05086		
U1_tRMS_rc@AC [V]	229.7517	57.19124	-172.5605	61.32491
U2_tRMS_rc@AC [V]	230.0183	203.3124	-26.70586	202.4054
U3_tRMS_rc@AC [V]	230.4300	203.4500	-26.97993	202.6039
I1_tRMS_rc@AC [A]	0.466488	0.475937	9.449e-3	0.470265
I2_tRMS_rc@AC [A]	0.406294	0.397475	-8.819e-3	0.397490
I3_tRMS_rc@AC [A]	1.869543	1.842380	-0.027164	1.844702
P_t_rc@AC [W]	-5.611653	0.097084	5.708736	3.365358
Q_t_rc@AC [var]	631.4054	-482.8632	-1114.269	482.0667
I_fund_Q_SYM+_rc@AC [A]	1.432e-3	-1.935e-3	-3.367e-3	2.219e-3
I_fund_Q_SYM-_rc@AC [A]	4.094e-4	1.101e-3	6.927e-4	2.056e-3
I_fund_P_SYM+_rc@AC [A]	-6.580e-3	-1.014e-3	5.567e-3	2.346e-3
I_fund_P_SYM-_rc@AC [A]	7.780e-4	-4.382e-4	-1.216e-3	2.218e-3
U_fund_SYM+_rc@AC [V]	398.4660	251.1622	-147.3037	251.3188
U_fund_SYM-_rc@AC [V]	0.193979	151.5617	151.3677	149.9056
U+ [V]	230.0765	144.0397	-86.03675	147.6557
U- [V]	0.684905	87.13554	86.45063	85.45859

Test – VD8.0

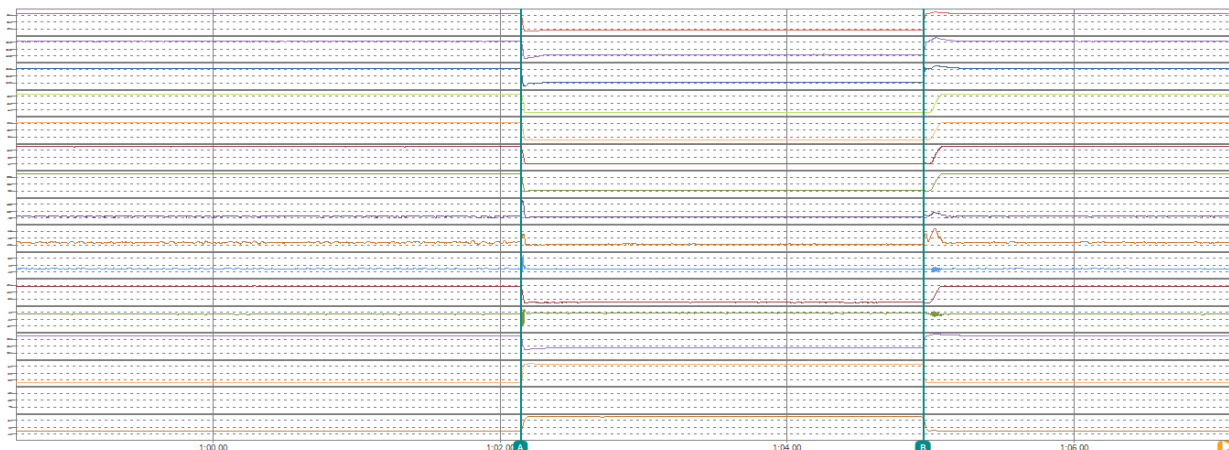
Duration fault time



时间 [s]	A	B	间隔	有效值
1:09.36052	1:11.26060	1:90008		
U1_tRMS_rc@AC [V]	230.2755	115.1433	-115.1322	116.0588
U2_tRMS_rc@AC [V]	229.5005	206.5633	-22.93716	206.2603
U3_tRMS_rc@AC [V]	230.2548	206.0514	-24.20345	206.3849
I1_tRMS_rc@AC [A]	179.2946	5.279268	-174.0154	17.06872
I2_tRMS_rc@AC [A]	180.2893	5.893249	-174.3961	17.05663
I3_tRMS_rc@AC [A]	183.9098	4.999499	-178.9103	15.94809
P_t_rc@AC [W]	124985.8	2362.452	-122623.4	9321.735
Q_t_rc@AC [var]	2446.003	-1603.707	-4049.710	4950.157
I_fund_Q_SYM+_rc@AC [A]	0.647635	-1.161592	-1.809227	1.237055
I_fund_Q_SYM-_rc@AC [A]	1.594108	0.607337	-0.986772	2.003516
I_fund_P_SYM+_rc@AC [A]	181.1275	4.479922	-176.6476	14.20347
I_fund_P_SYM-_rc@AC [A]	0.814462	0.296600	-0.517862	1.270613
U_fund_SYM+_rc@AC [V]	398.3799	297.5455	-100.8344	297.9827
U_fund_SYM-_rc@AC [V]	1.178820	99.26438	98.08556	98.84501
U+ [V]	230.0452	171.8308	-58.21443	172.7061
U- [V]	0.811748	57.39907	56.58732	56.76484

Test – VD9.0

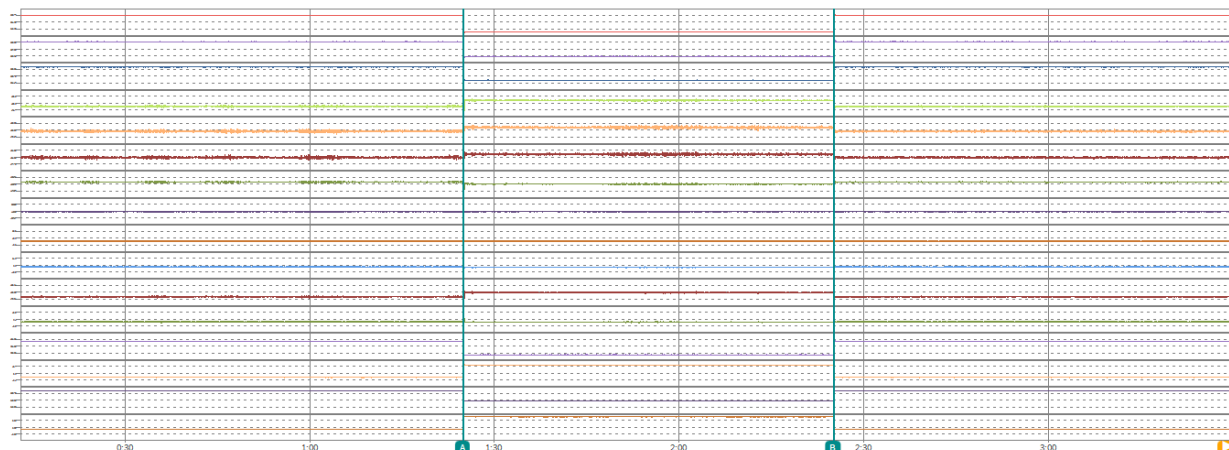
Duration fault time



时间 [s]	A	B	间隔	有效值
1:02.14782	1:04.95162	2:80380		
U1_tRMS_rc@AC [V]	230.5128	172.5282	-57.98460	172.7390
U2_tRMS_rc@AC [V]	229.8869	215.6648	-14.22211	215.6287
U3_tRMS_rc@AC [V]	229.8176	215.6312	-14.18642	215.5897
I1_tRMS_rc@AC [A]	179.0931	4.746177	-174.3469	15.33357
I2_tRMS_rc@AC [A]	181.0495	5.641684	-175.4078	15.12533
I3_tRMS_rc@AC [A]	183.4895	5.865183	-177.6243	14.77261
P_t_rc@AC [W]	125052.4	2893.107	-122159.3	8871.126
Q_t_rc@AC [var]	2284.812	-1588.007	-3872.818	4363.069
I_fund_Q_SYM+_rc@AC [A]	0.841154	-0.571221	-1.412375	0.956966
I_fund_Q_SYM-_rc@AC [A]	0.126020	-0.057675	-0.183895	1.472270
I_fund_P_SYM+_rc@AC [A]	161.1822	4.787435	-176.3948	13.24207
I_fund_P_SYM-_rc@AC [A]	-1.192451	0.182756	1.375207	1.136438
U_fund_SYM+_rc@AC [V]	398.4918	346.8659	-51.62592	346.8969
U_fund_SYM-_rc@AC [V]	1.030961	50.07063	49.03967	49.62844
U+ [V]	230.0592	200.3304	-29.72879	200.5921
U- [V]	0.609691	28.76168	28.15199	28.51104

Test – VD10.0

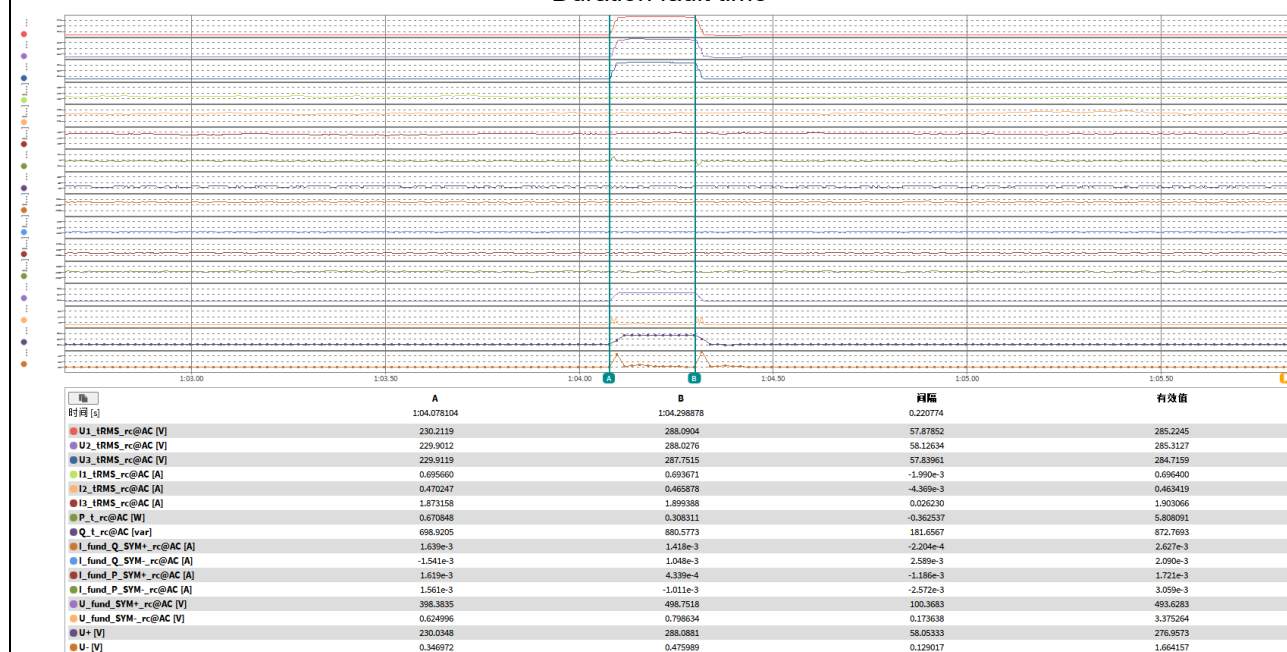
Duration fault time



时间 [s]	A	B	间隔	有效值
1:24.9662	2:25.0975	1:00.1313		
U1_tRMS_rc@AC [V]	230.5272	207.5109	-23.01630	207.5141
U2_tRMS_rc@AC [V]	229.8827	225.2010	-4.681625	225.1693
U3_tRMS_rc@AC [V]	229.8004	225.2371	-4.563293	225.1665
I1_tRMS_rc@AC [A]	179.5349	190.9462	11.41135	190.9983
I2_tRMS_rc@AC [A]	180.5237	185.0860	4.562393	185.5423
I3_tRMS_rc@AC [A]	183.6455	188.0951	4.449539	187.7866
P_t_rc@AC [W]	125065.9	123549.2	-1516.672	123569.1
Q_t_rc@AC [var]	2390.657	5486.350	3095.692	5602.669
I_fund_Q_SYM+_rc@AC [A]	0.772563	0.642629	-0.129933	0.606445
I_fund_Q_SYM-_rc@AC [A]	0.492651	-0.920382	-1.413033	0.641792
I_fund_P_SYM+_rc@AC [A]	181.2009	188.0195	6.818573	188.0810
I_fund_P_SYM-_rc@AC [A]	-0.733153	-1.540161	-0.807008	1.653745
U_fund_SYM+_rc@AC [V]	398.4867	379.5558	-18.93091	379.4901
U_fund_SYM-_rc@AC [V]	0.913911	21.26183	20.34792	21.10660
U+ [V]	230.0634	219.1038	-10.95960	219.1098
U- [V]	0.586678	12.17649	11.58981	12.18357

Test – VS1.0

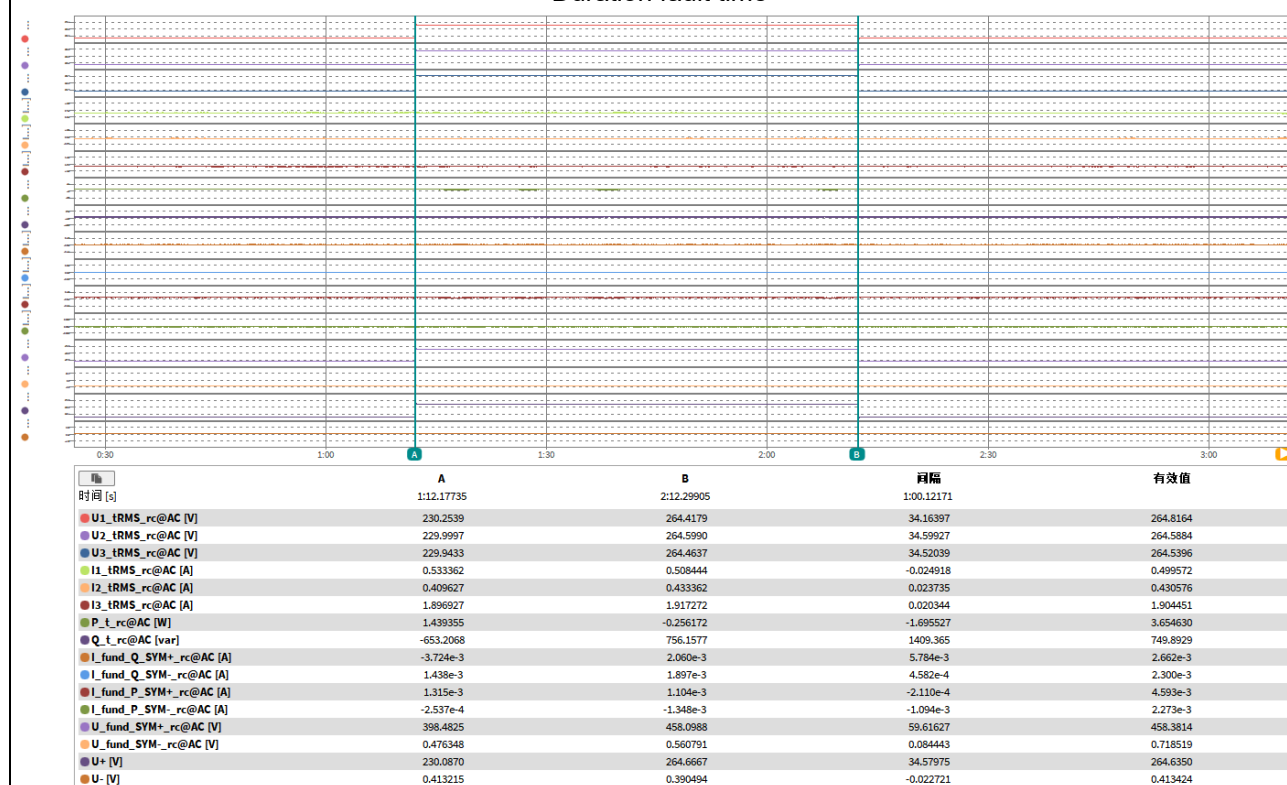
Duration fault time

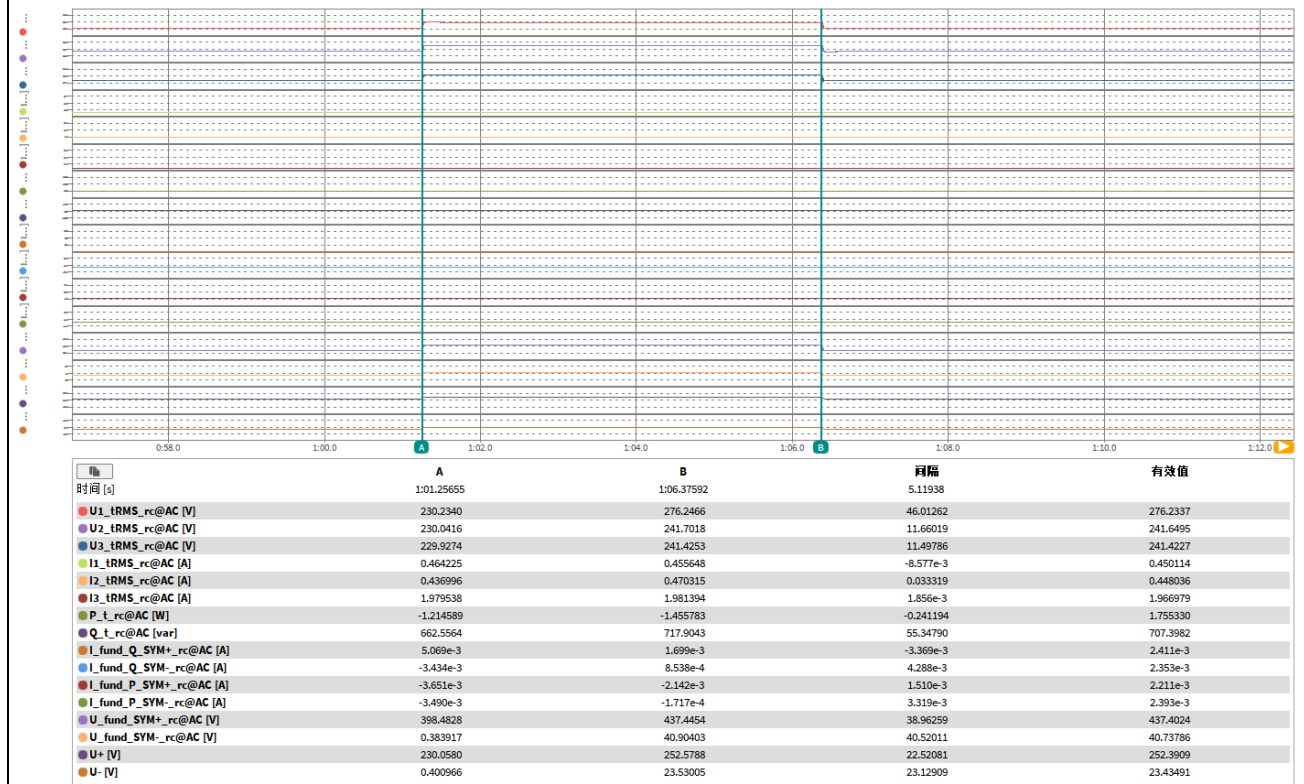
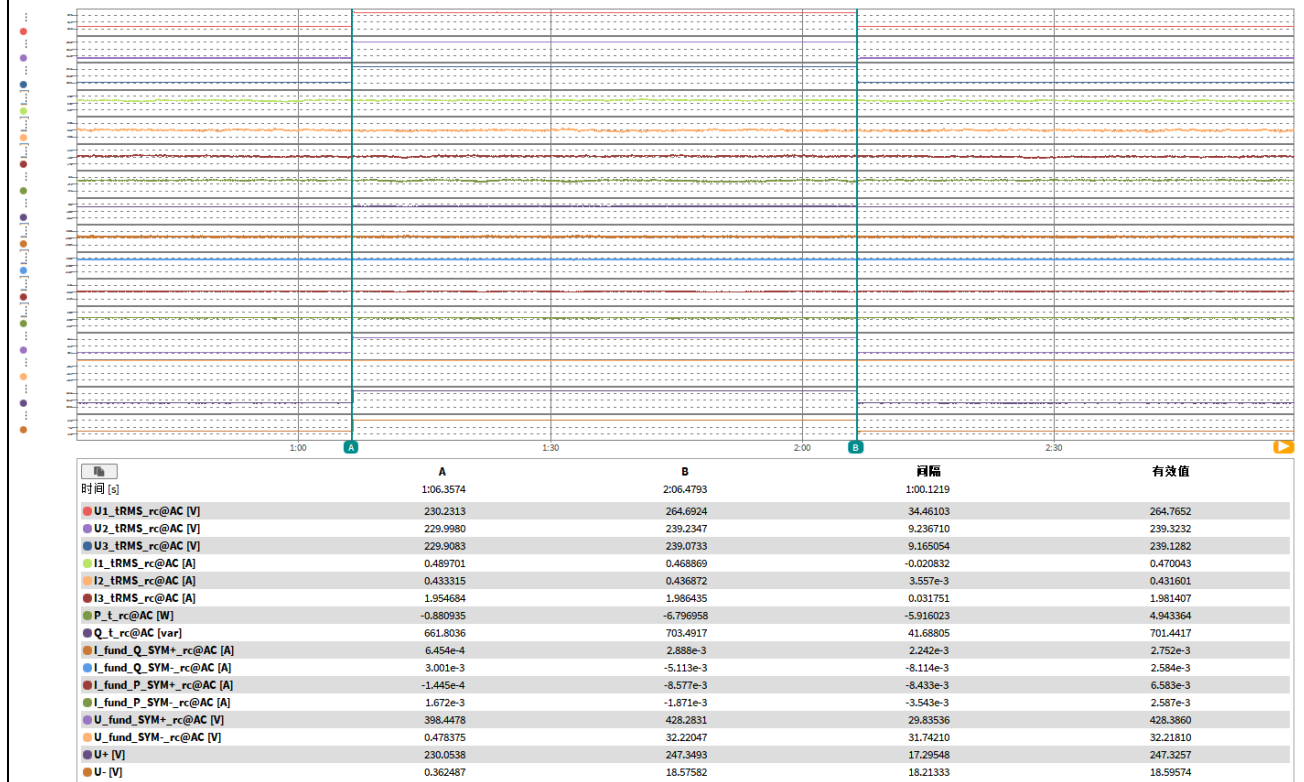


Test – VS2.1.0

Duration fault time

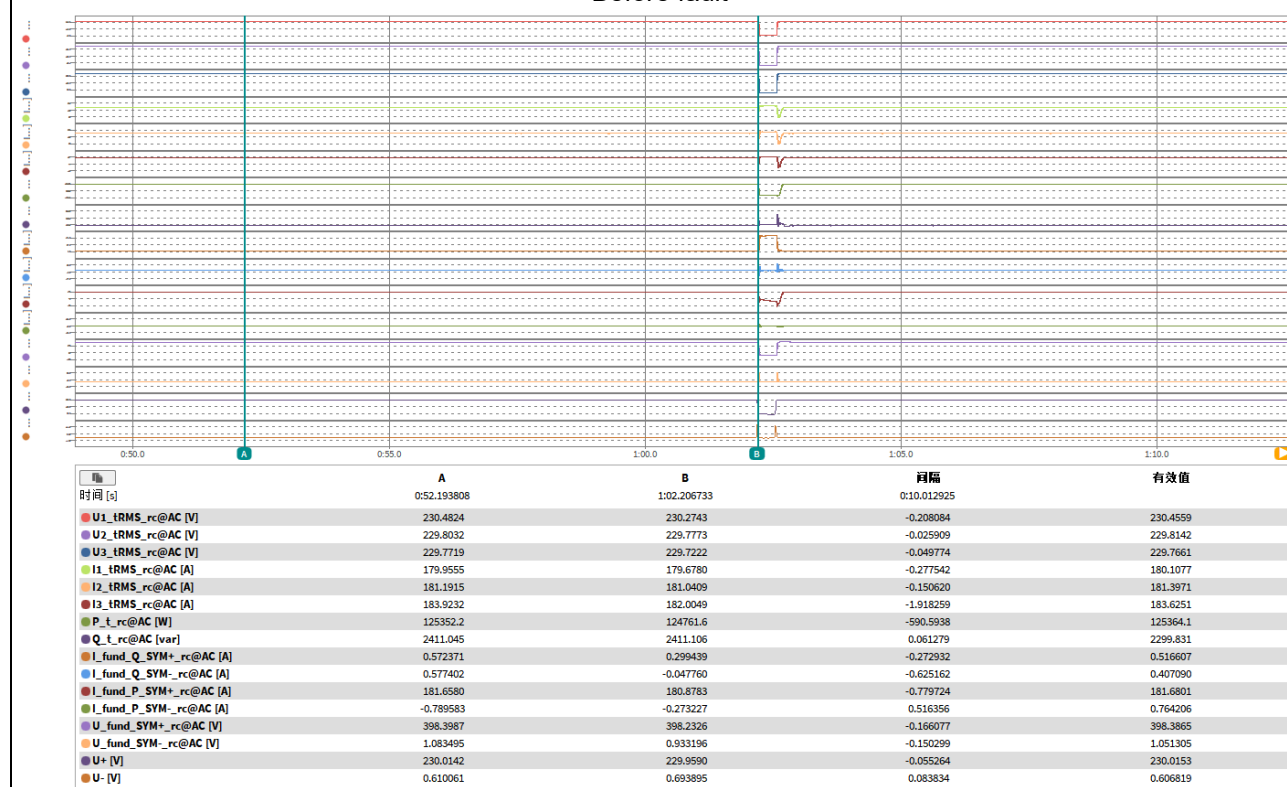


Test – VS2.2.0
Duration fault timeTest – VS3.0
Duration fault time

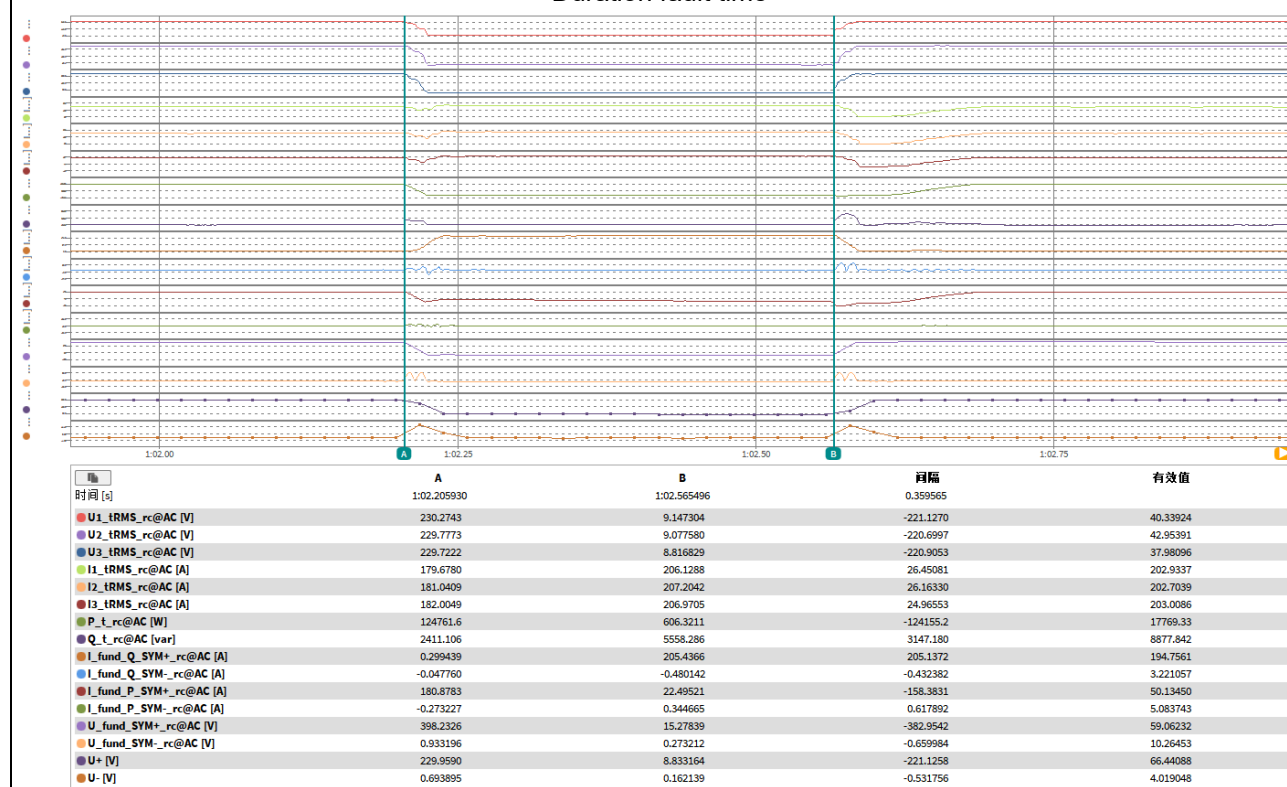
Test – VS4.1.0
Duration fault timeTest – VS4.2.0
Duration fault time

Test - VD1.1

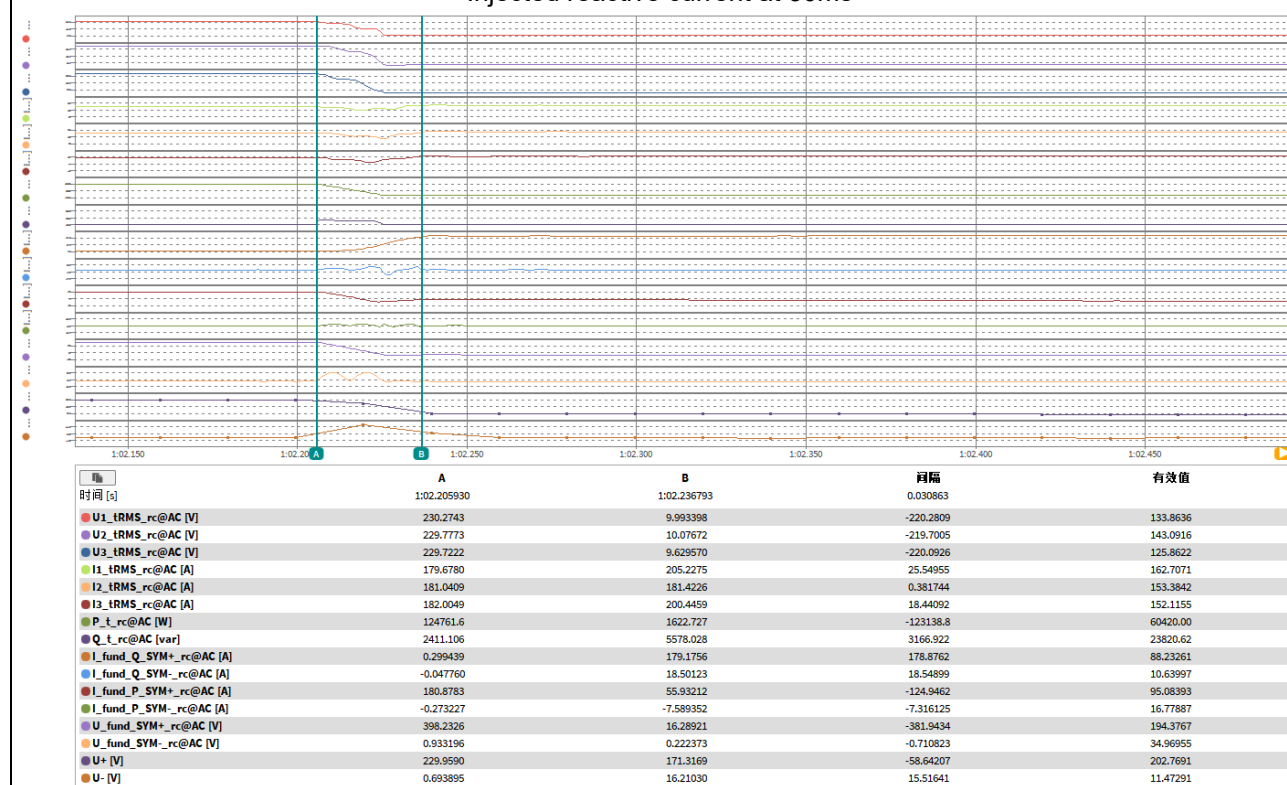
Before fault



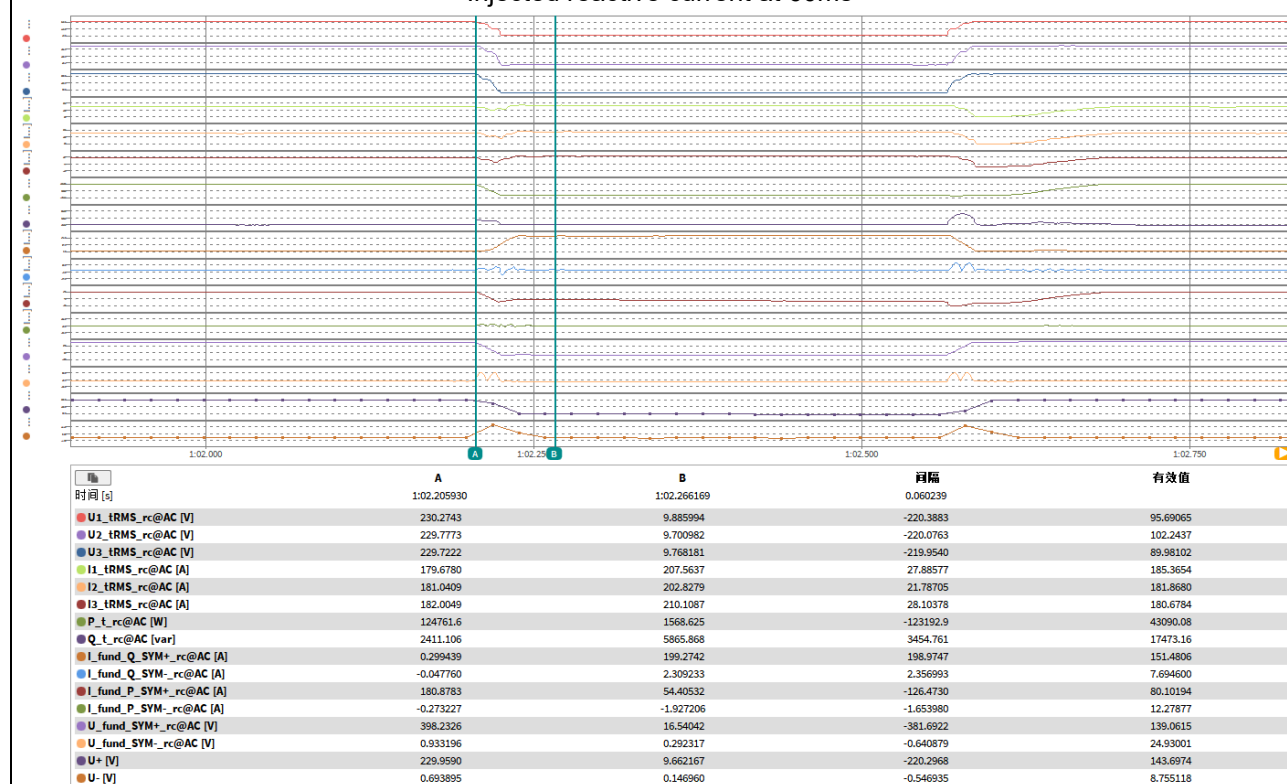
Duration fault time



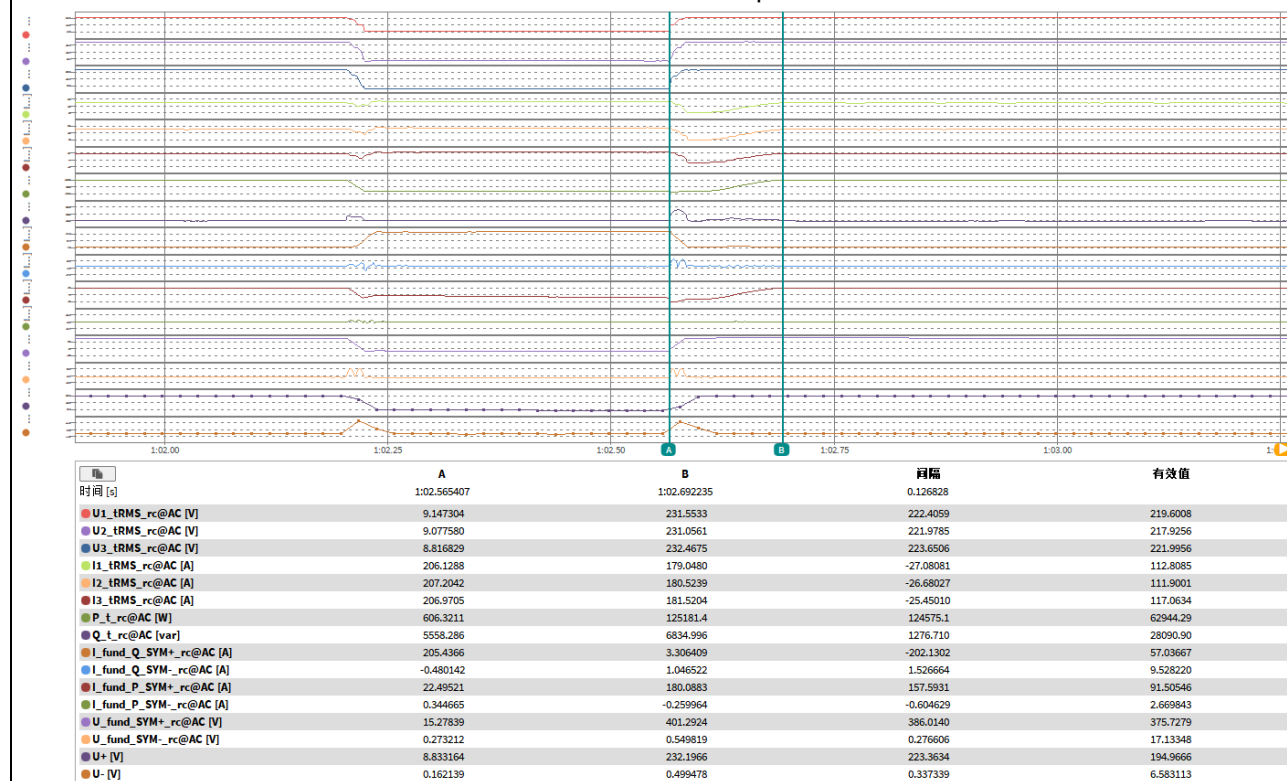
Injected reactive current at 30ms



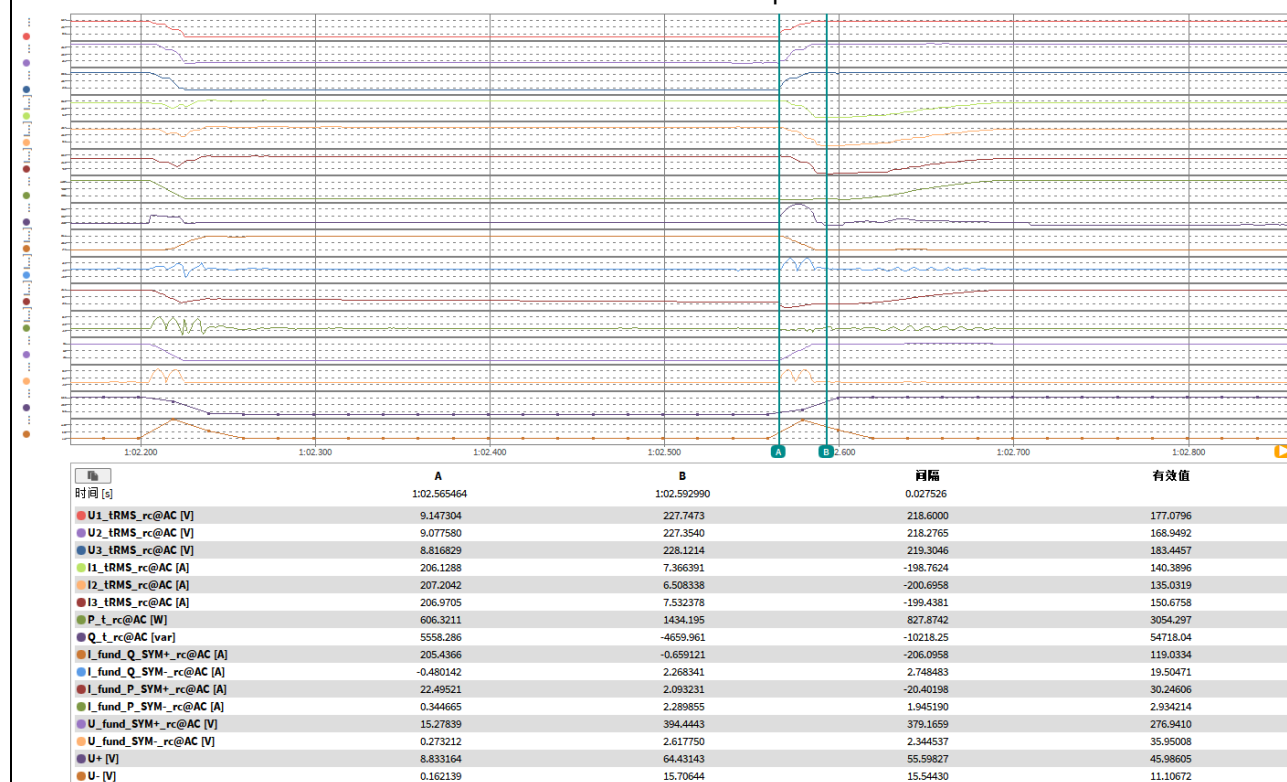
Injected reactive current at 60ms



Recover time – Active power

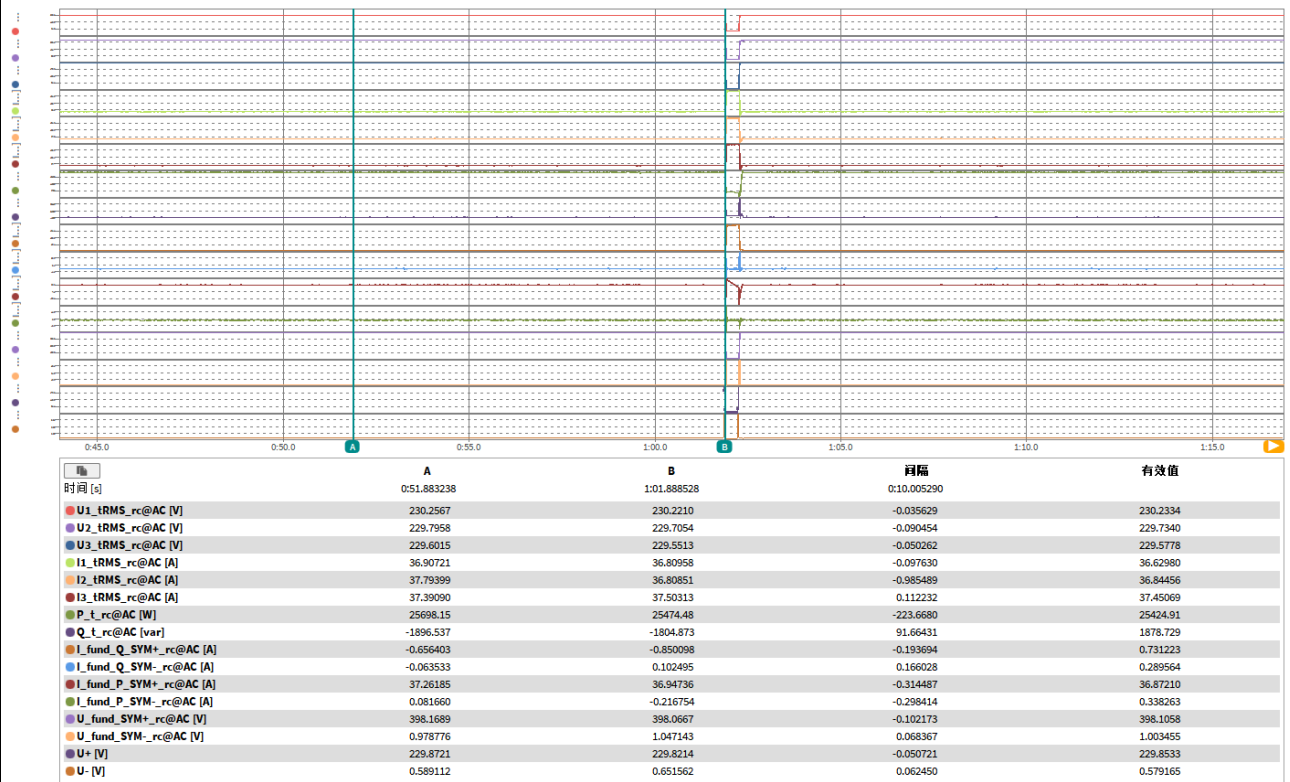


Recover time – Reactive power

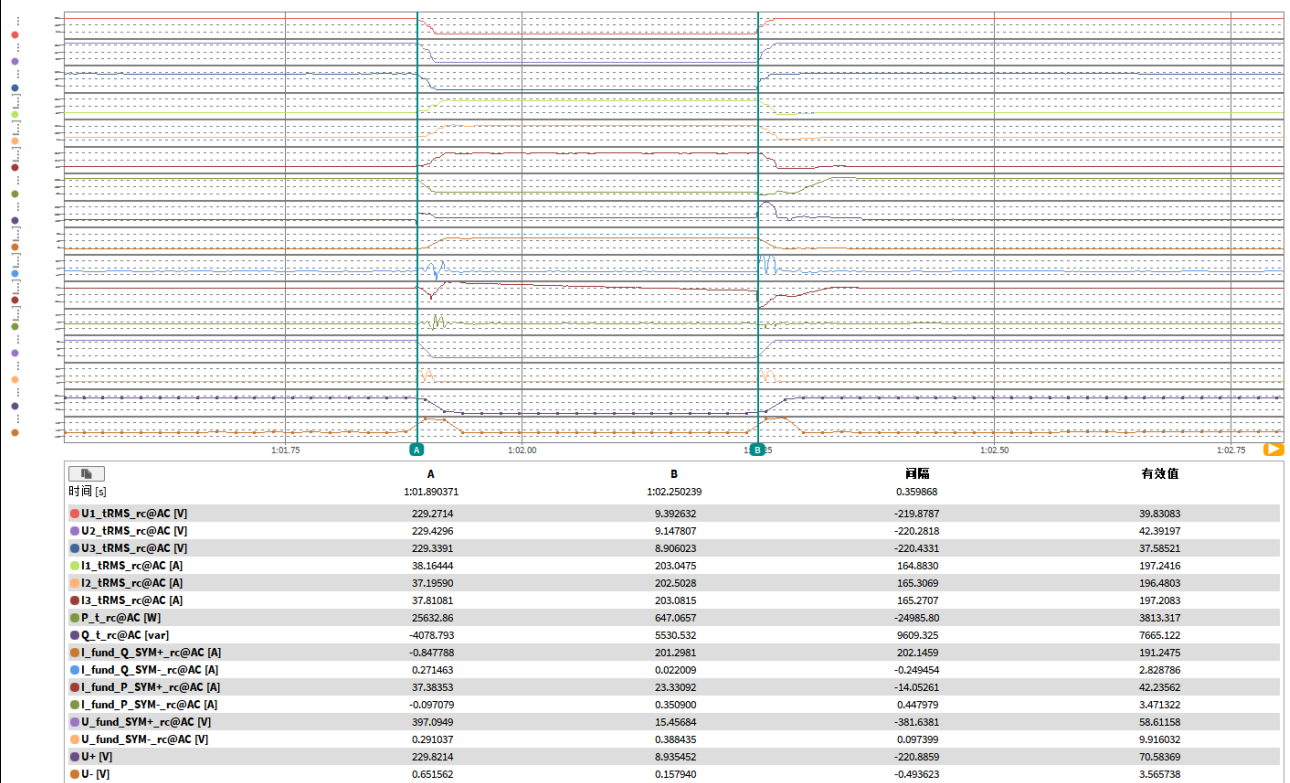


Test - VD1.2

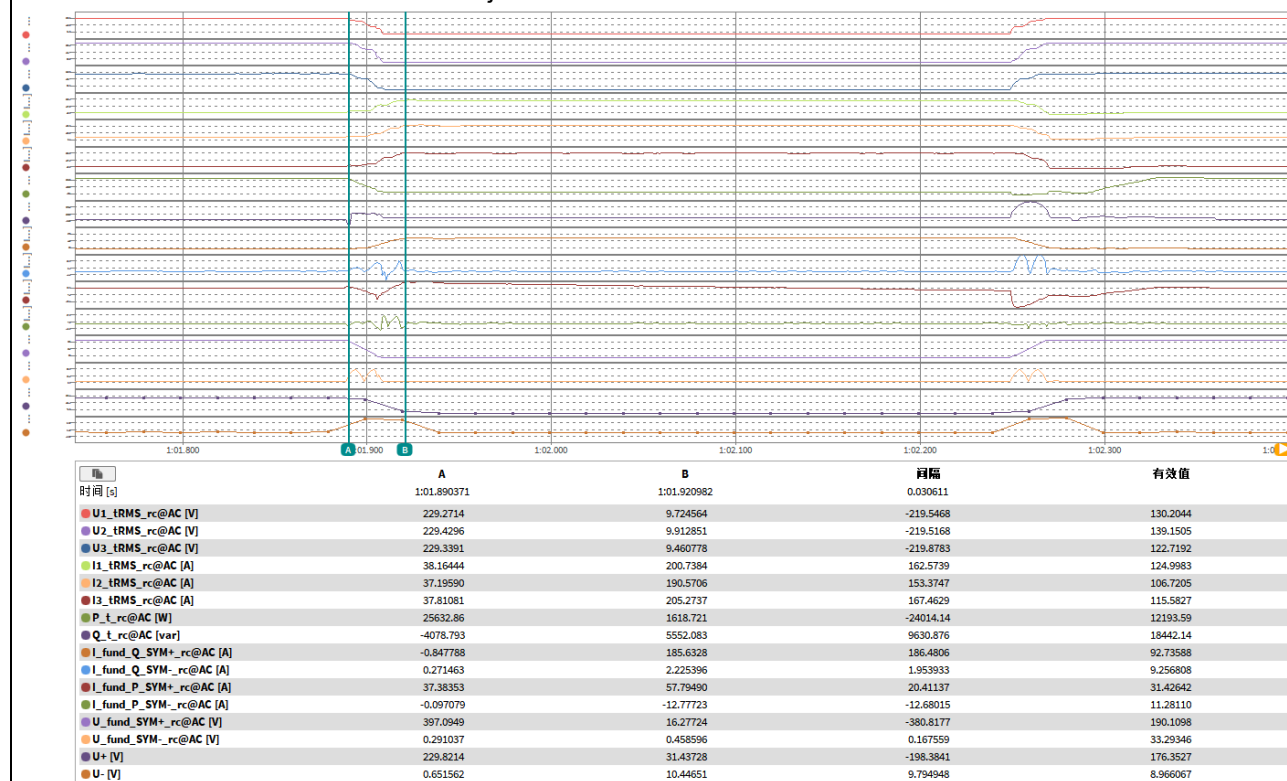
Before fault



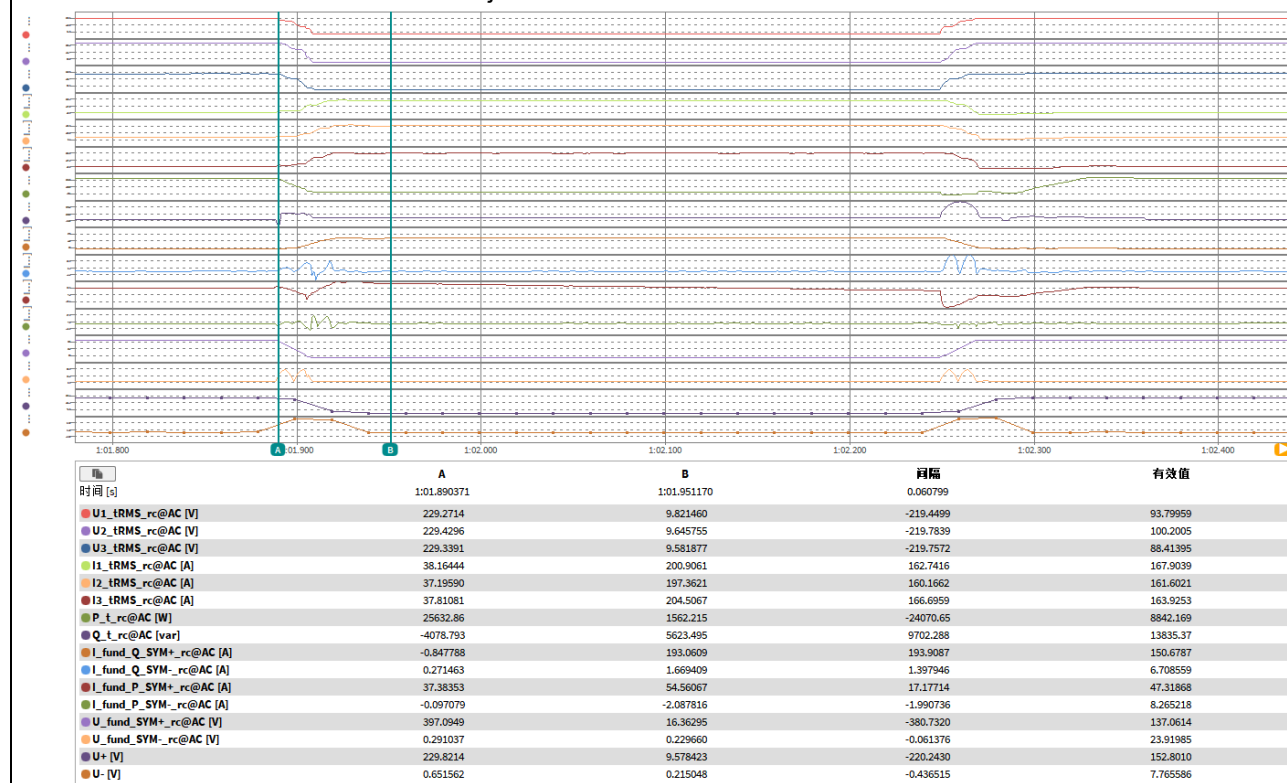
Duration fault time



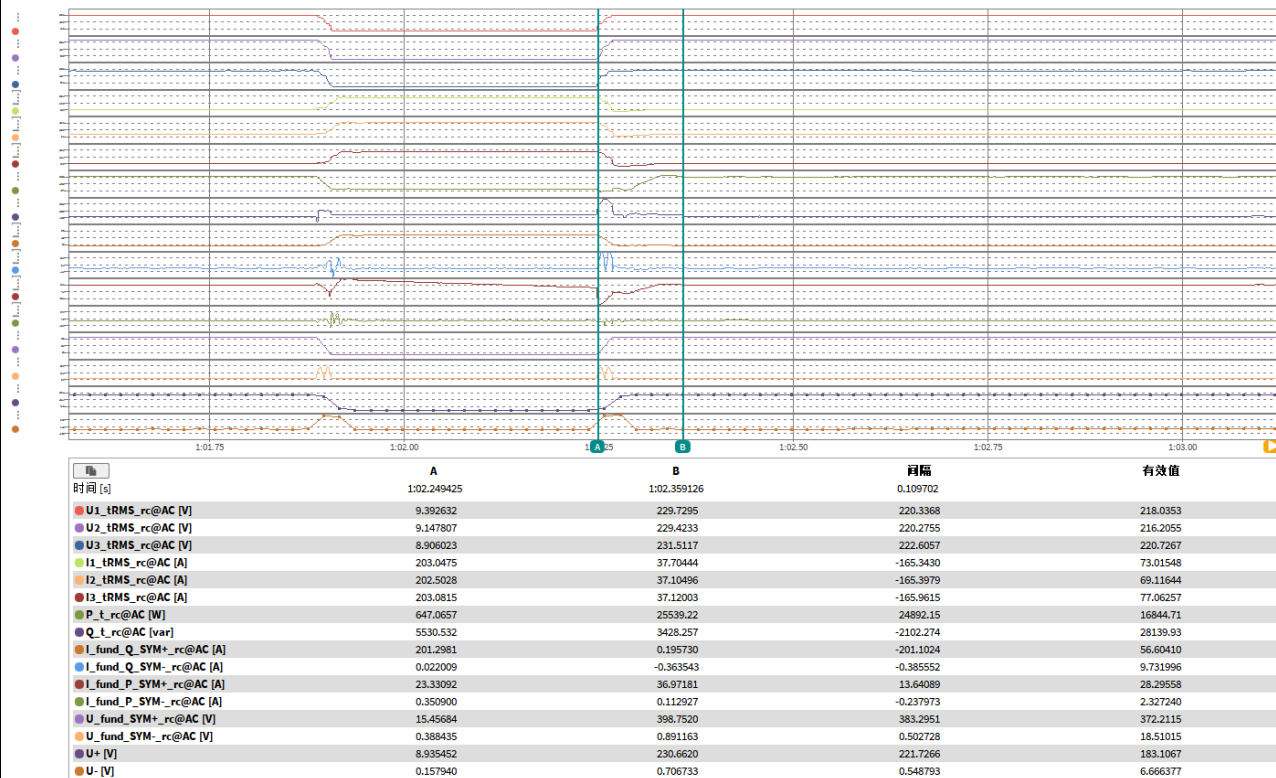
Injected reactive current at 30ms



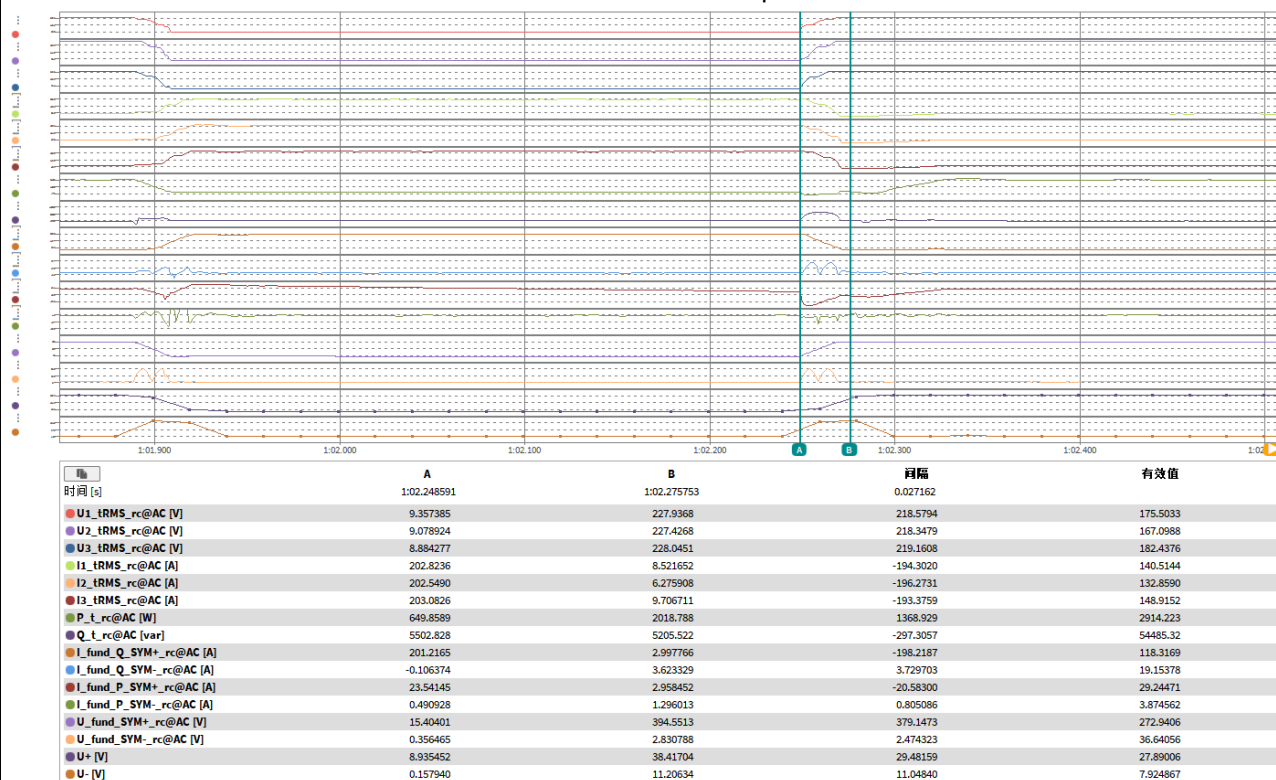
Injected reactive current at 60ms



Recover time – Active power



Recover time – Reactive power

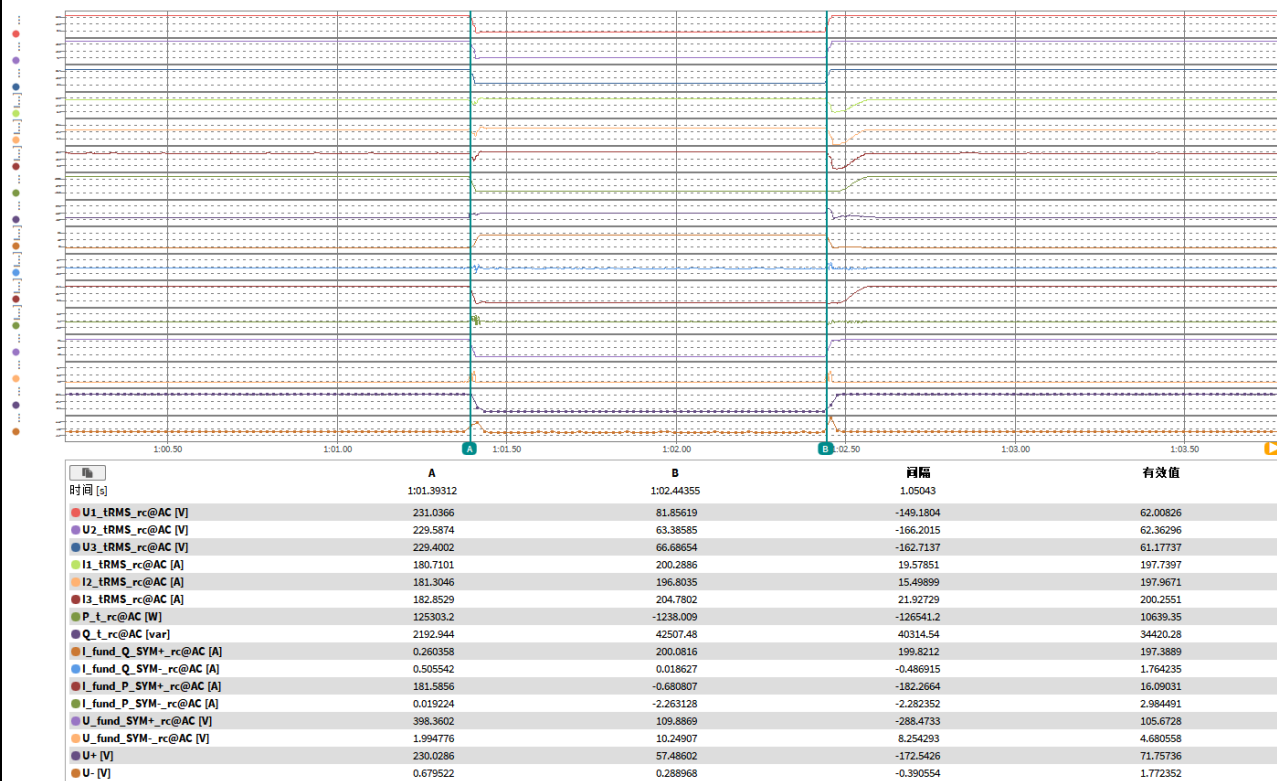


Test – VD2.1.a

Before fault



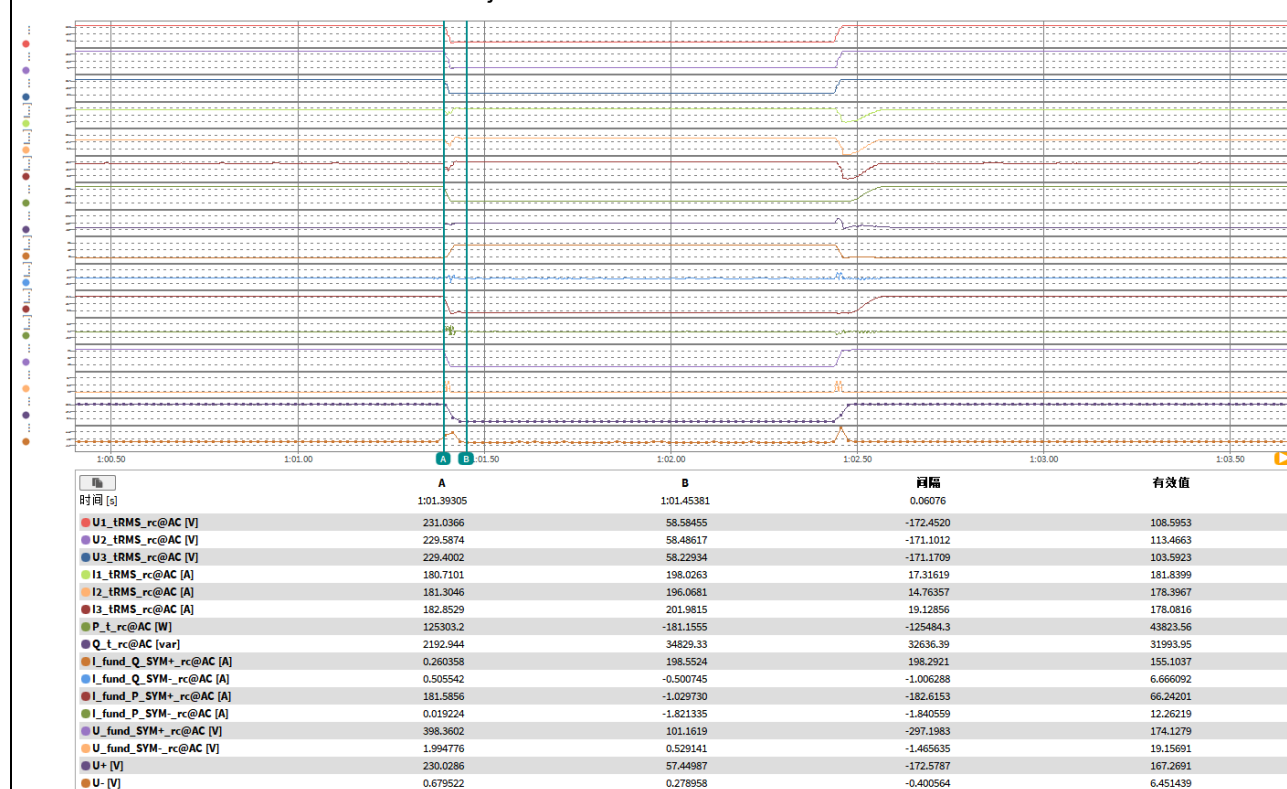
Duration fault time



Injected reactive current at 30ms



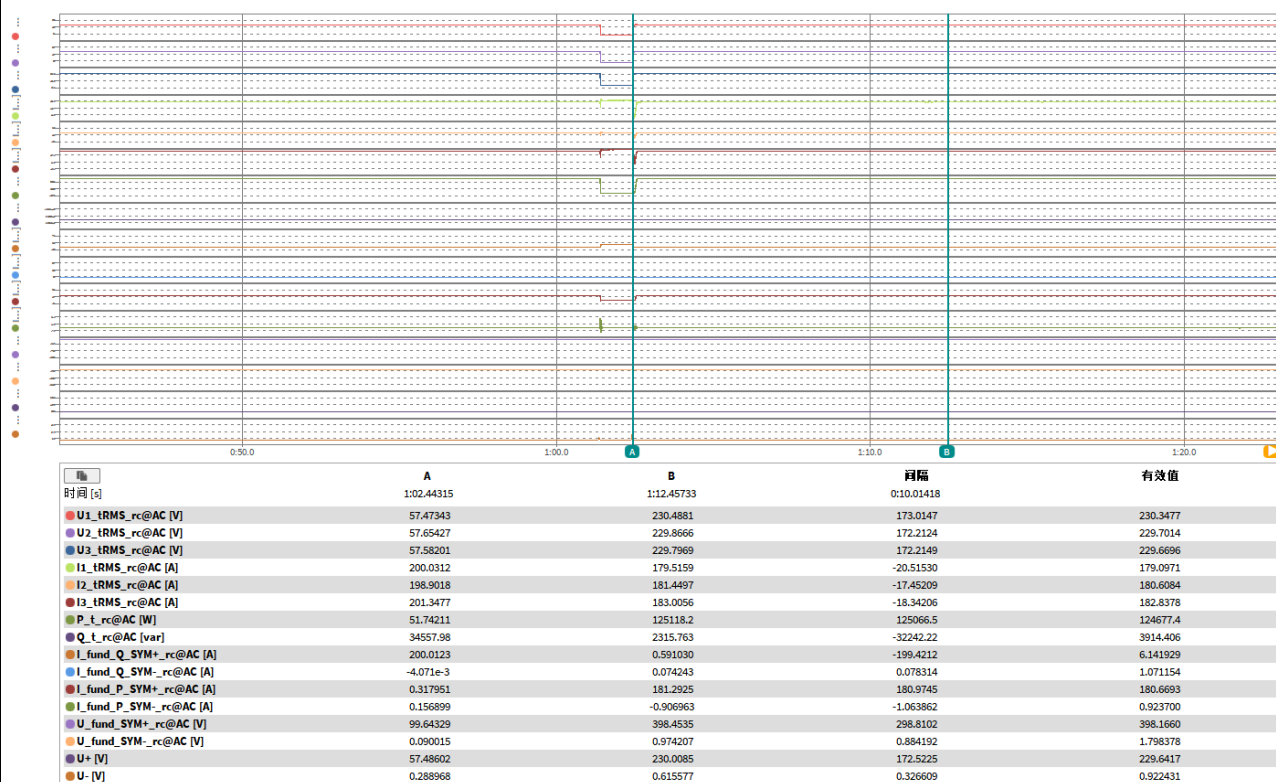
Injected reactive current at 60ms



Recover time – Active power

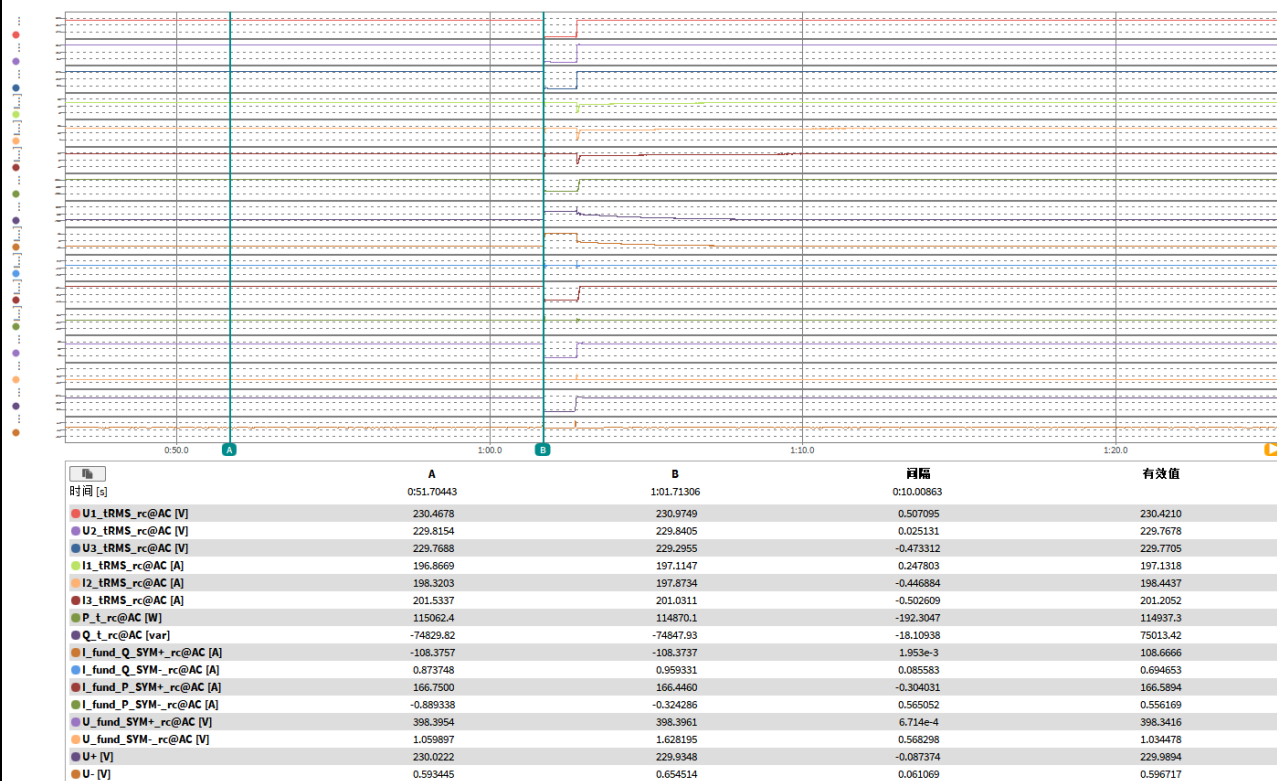


Recover time – Reactive power



Test – VD2.1.b

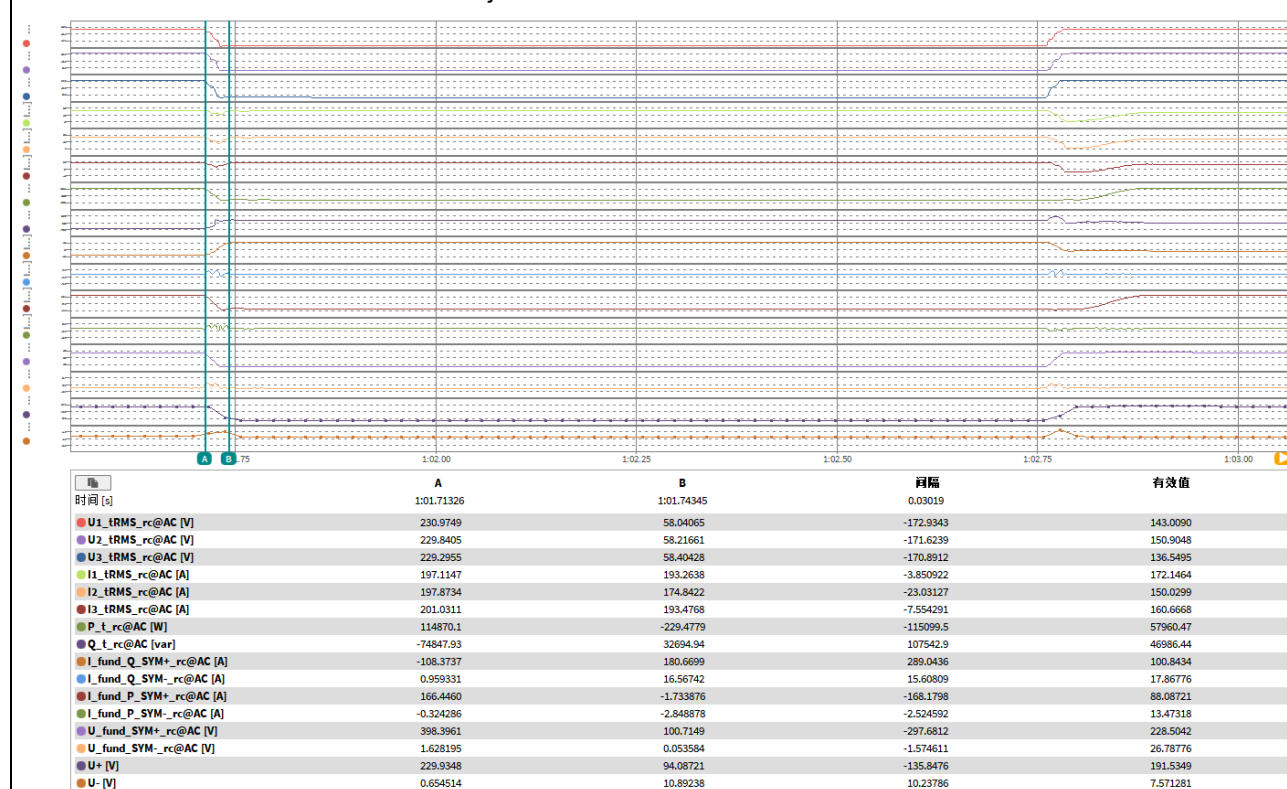
Before fault



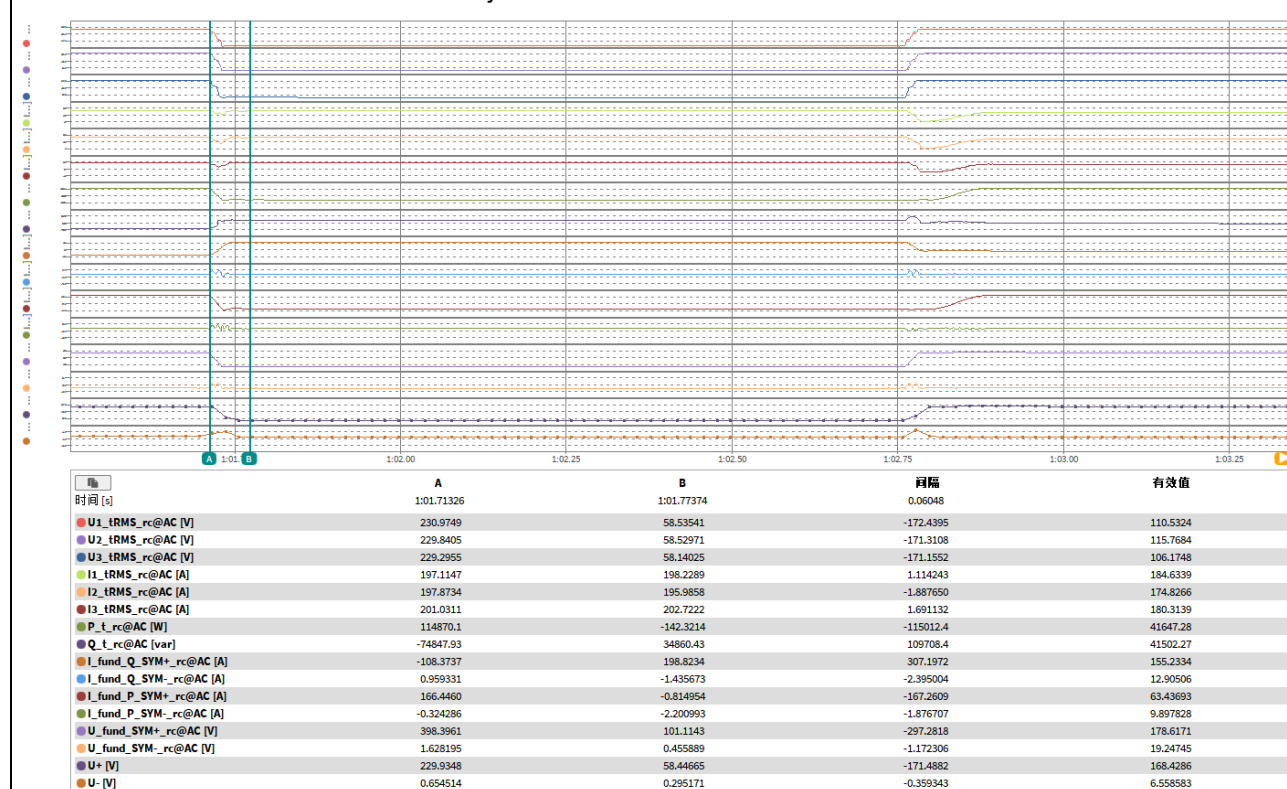
Duration fault time



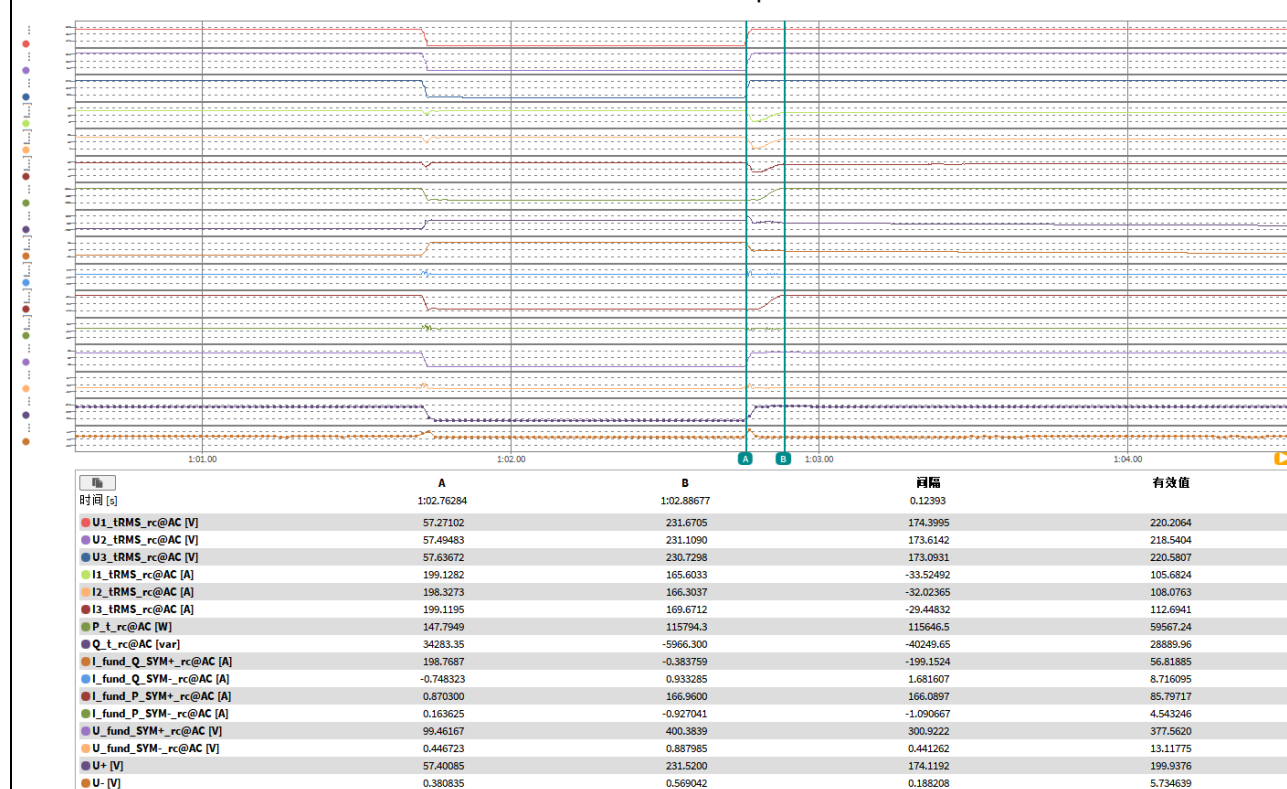
Injected reactive current at 30ms



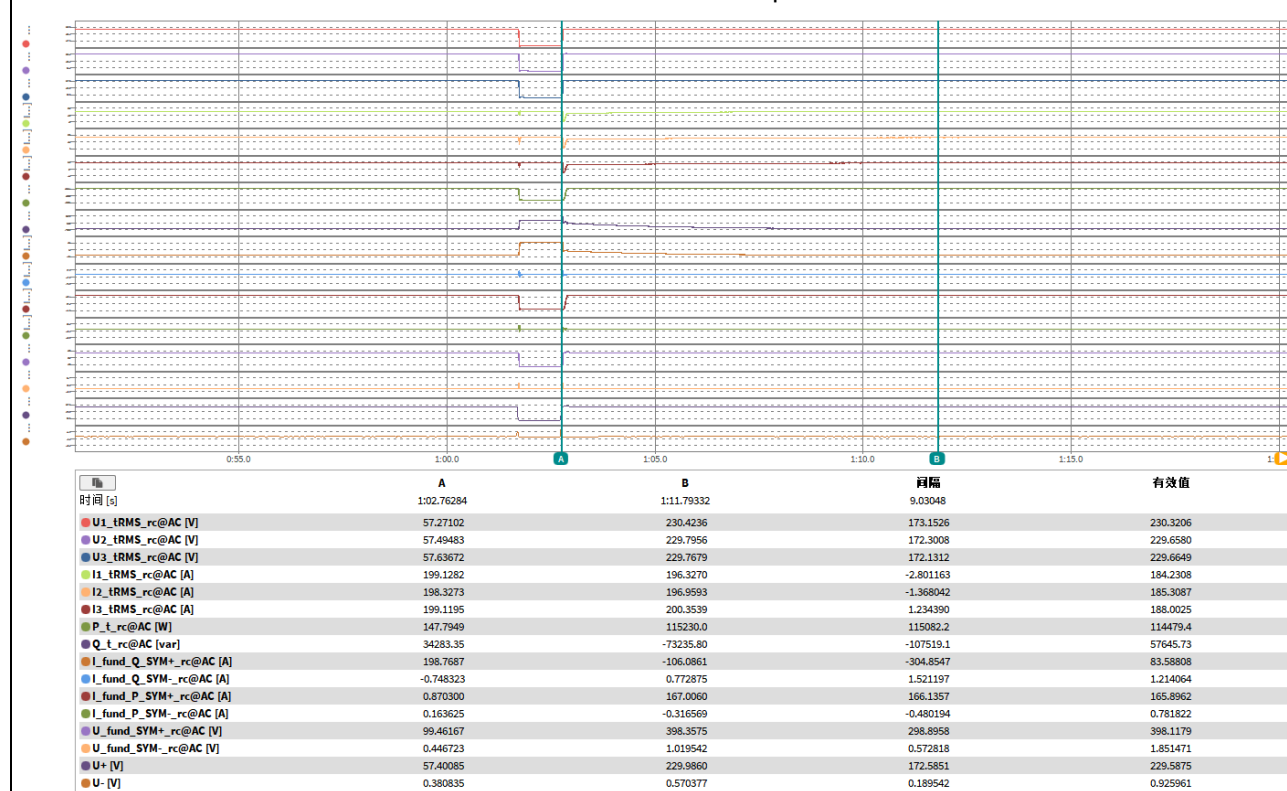
Injected reactive current at 60ms



Recover time – Active power

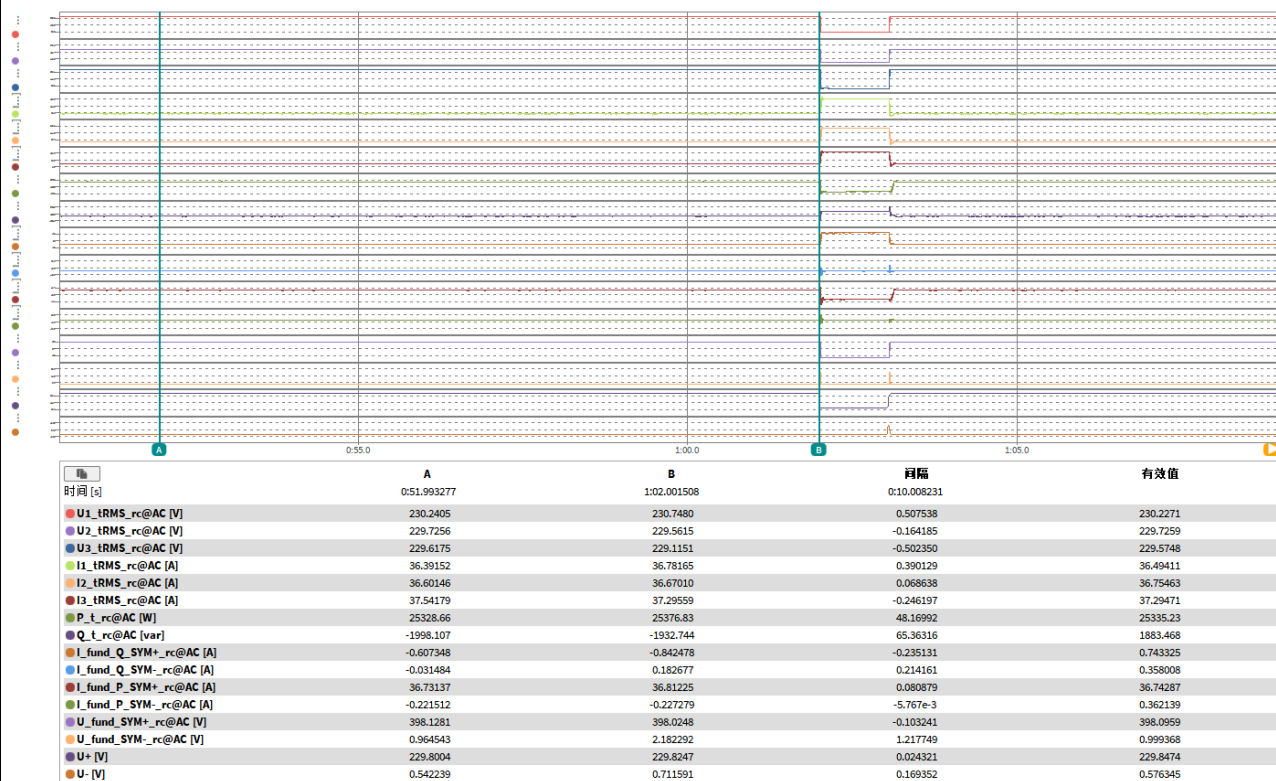


Recover time – Reactive power

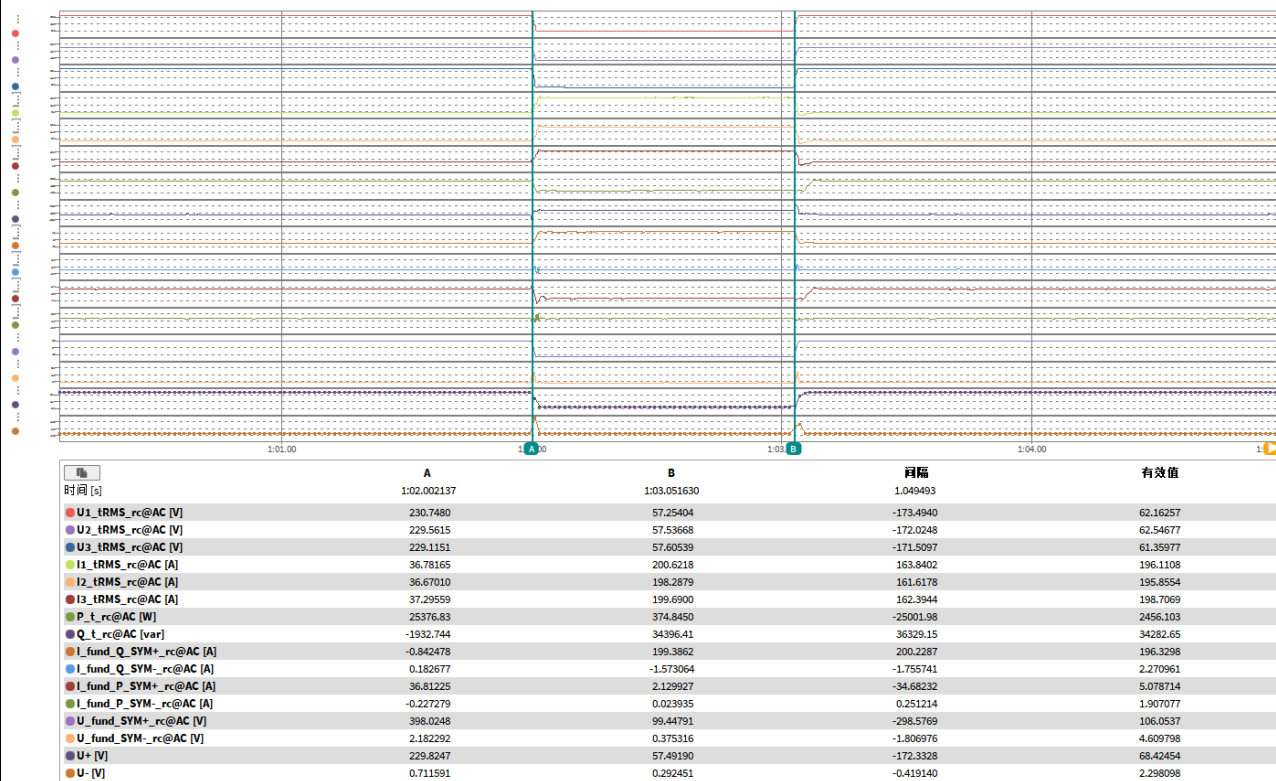


Test – VD2.2

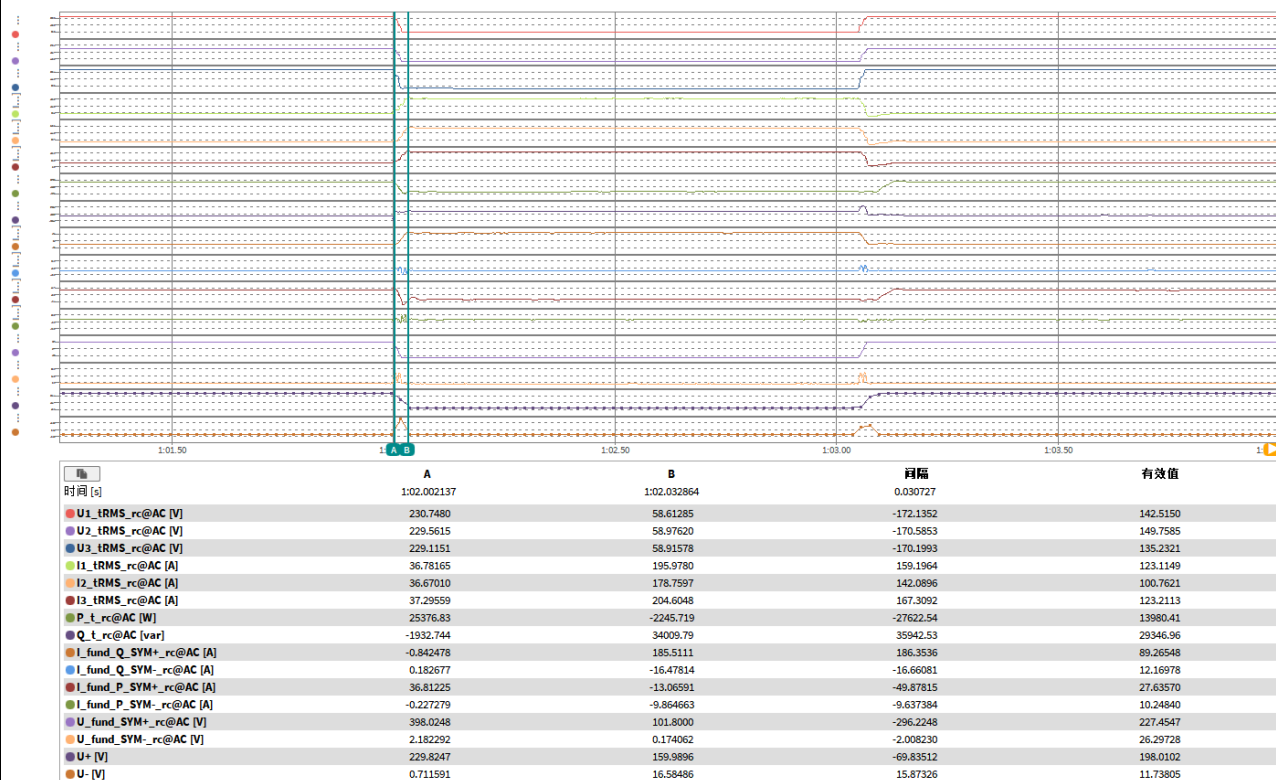
Before fault



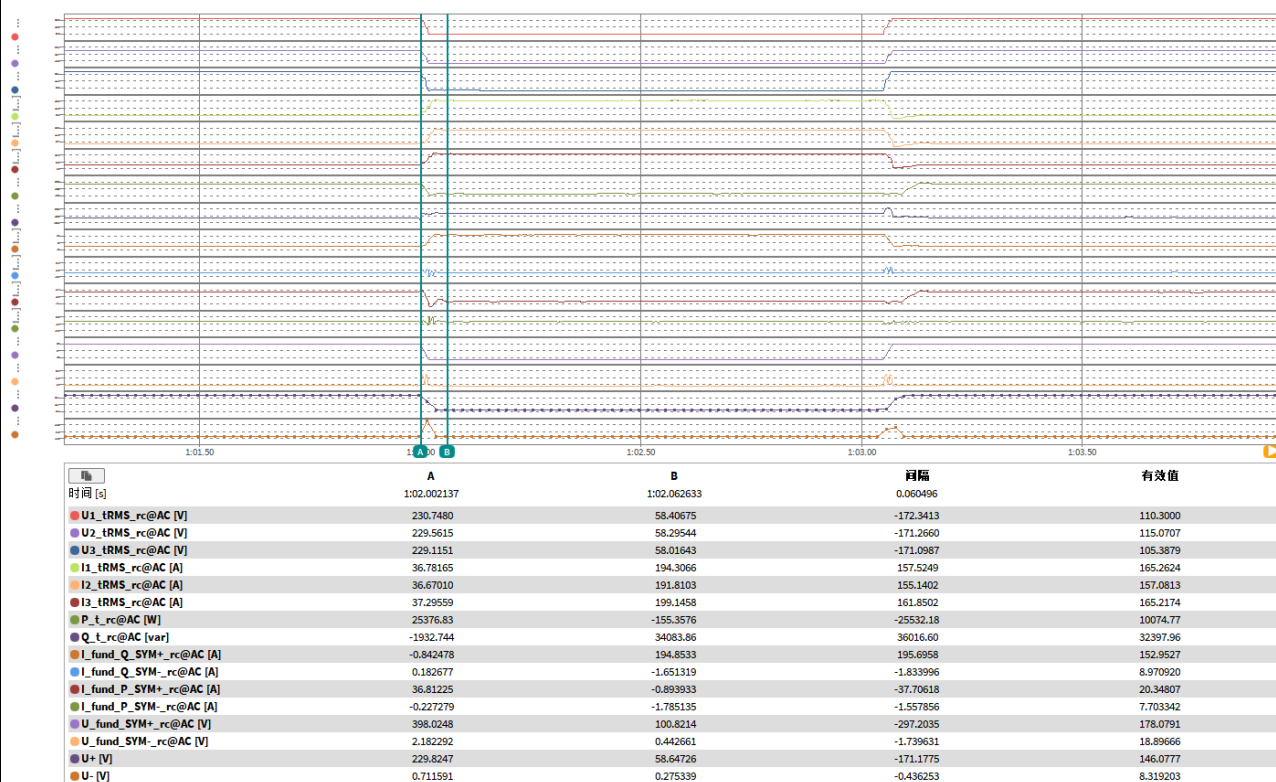
Duration fault time



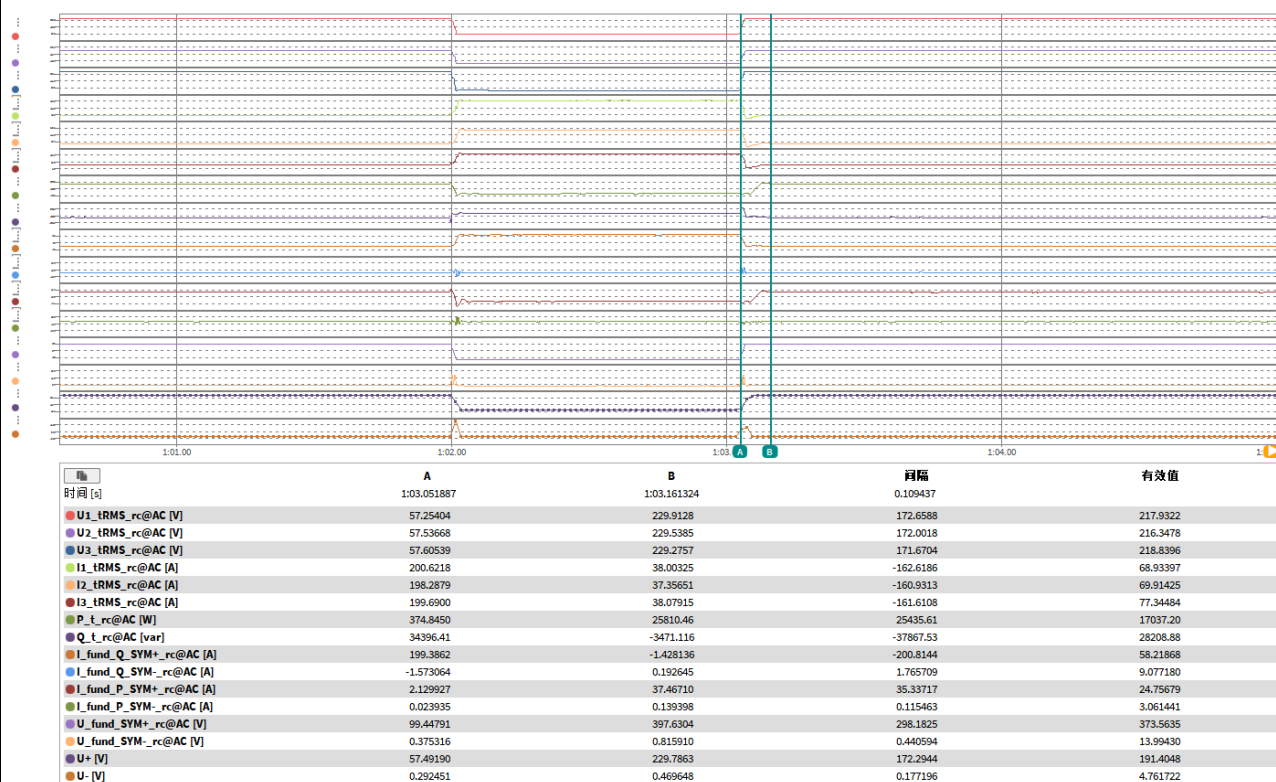
Injected reactive current at 30ms



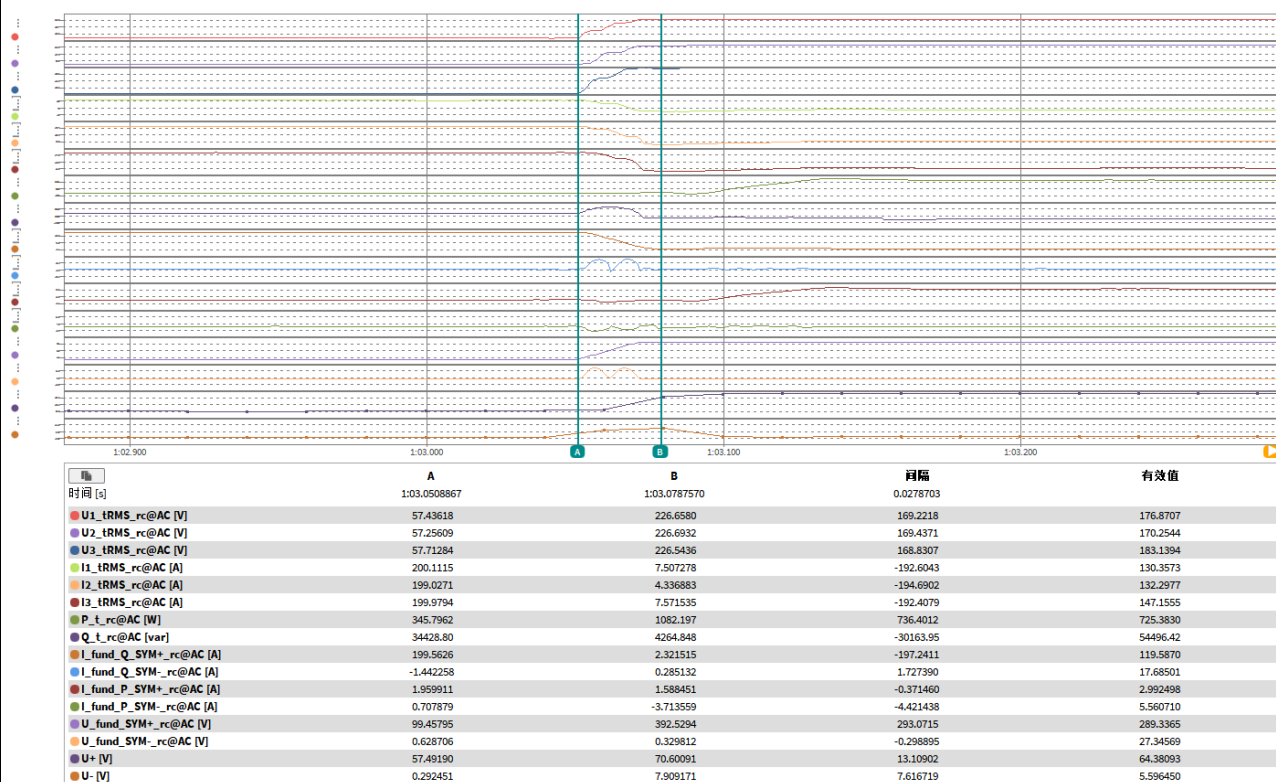
Injected reactive current at 60ms



Recover time – Active power

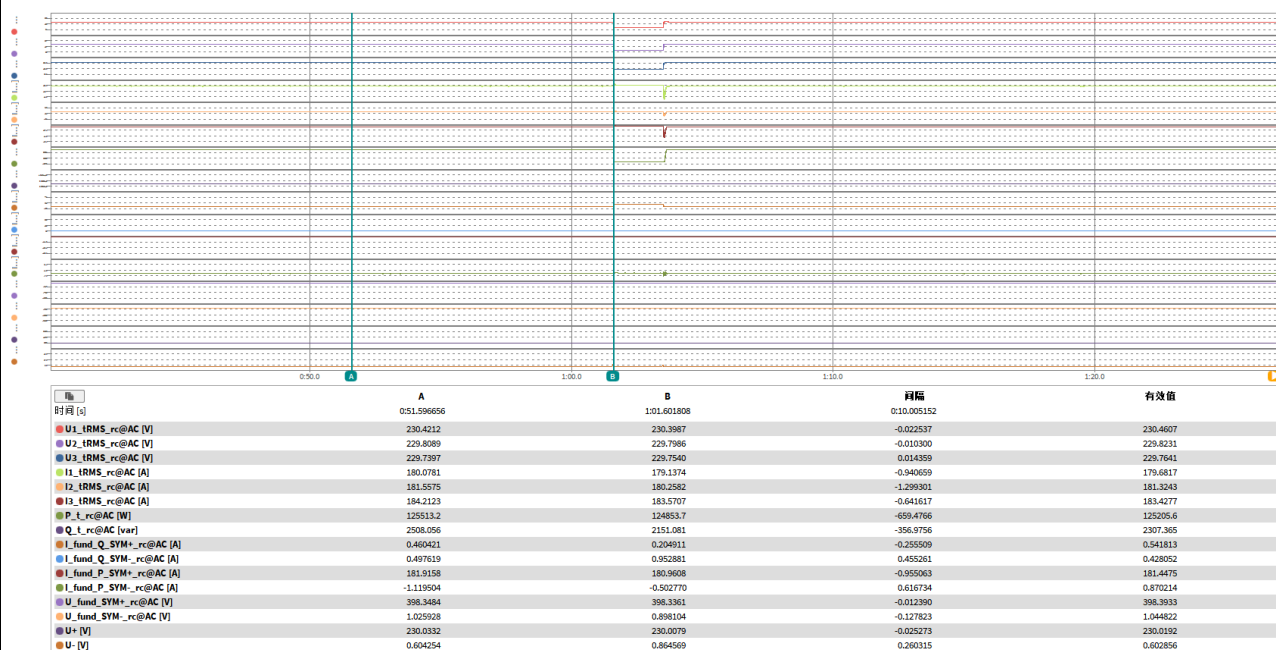


Recover time – Reactive power

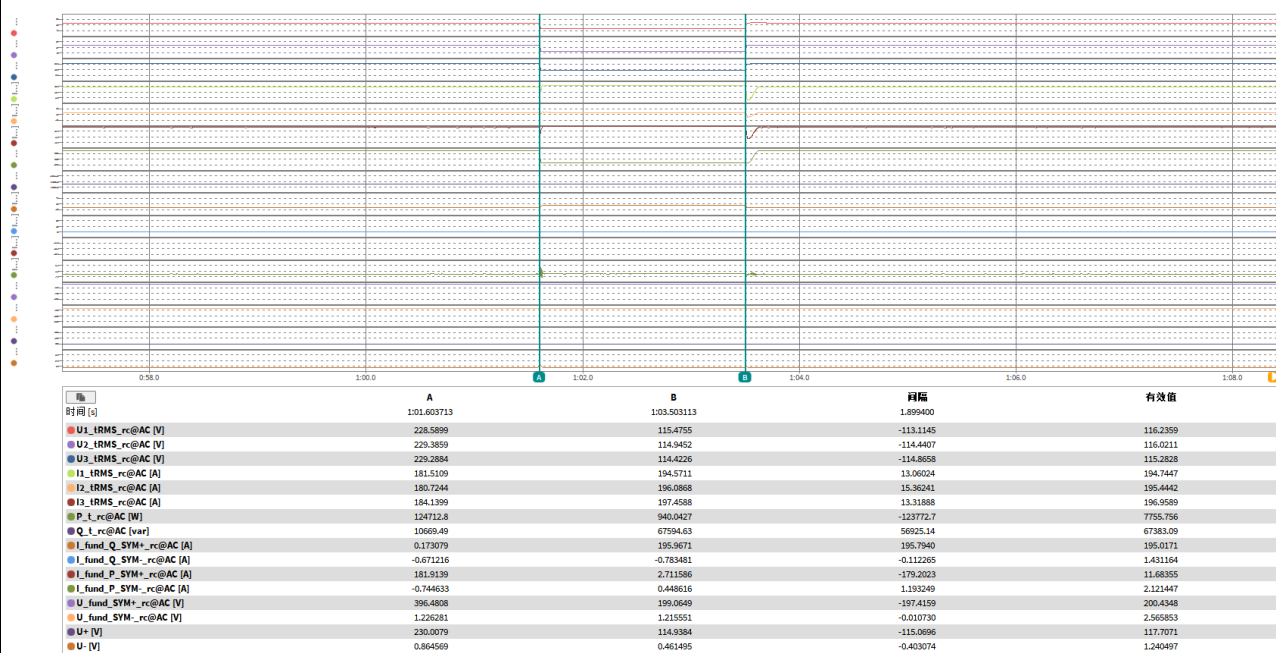


Test – VD3.1

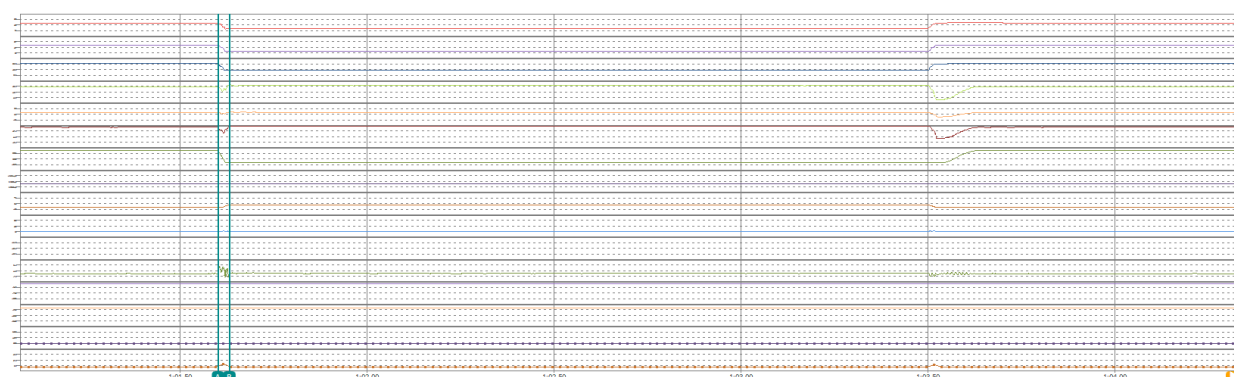
Before fault



Duration fault time

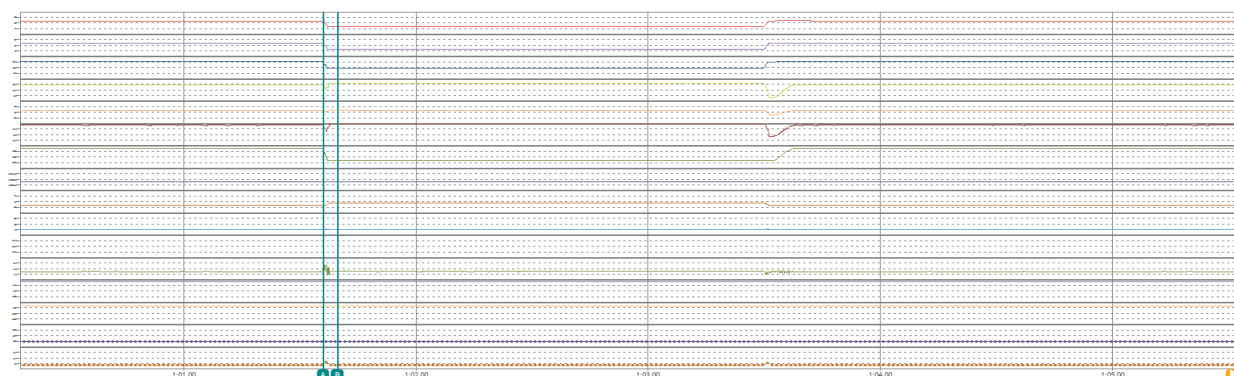


Injected reactive current at 30ms



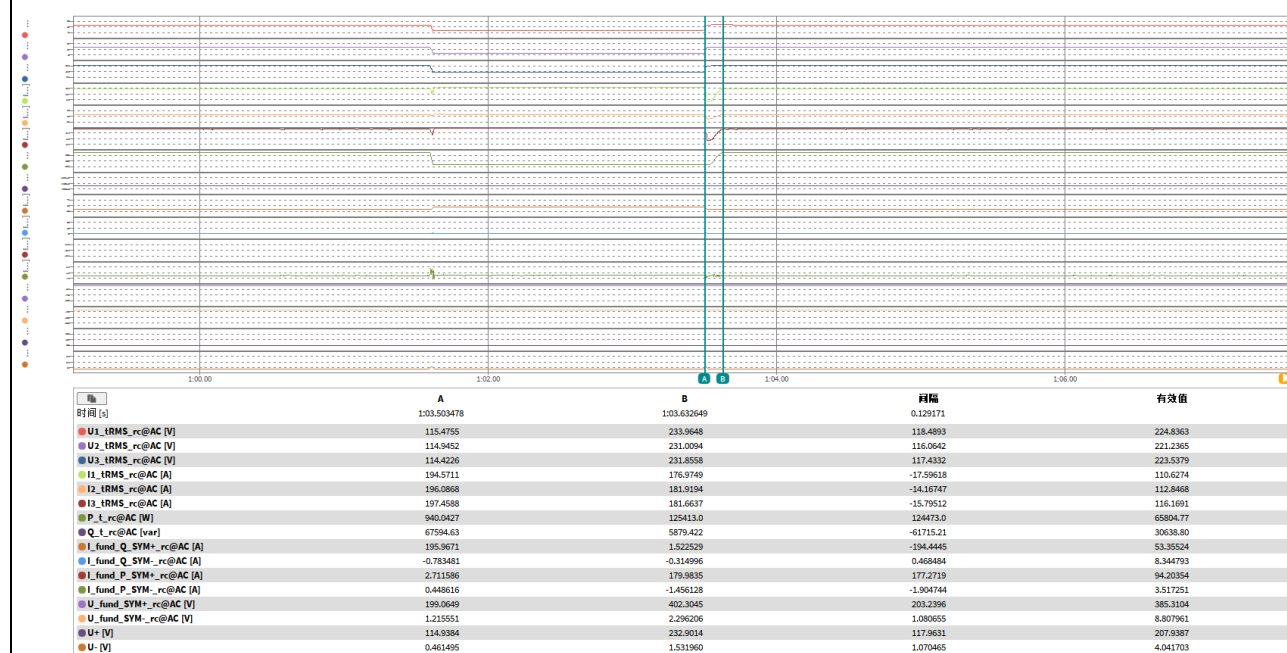
时间 [s]	A	B	间隔	有效值
1:01.603290	1:01.633491	0.030201		
U1_RMS_rc@AC [V]	230.4110	113.8525	-116.5585	164.8929
U2_RMS_rc@AC [V]	229.8167	113.7965	-116.0202	170.4609
U3_RMS_rc@AC [V]	229.7535	113.7351	-116.0185	159.6256
I1_RMS_rc@AC [A]	179.1814	203.1889	24.00757	159.5410
I2_RMS_rc@AC [A]	180.1725	184.0729	3.900406	156.2189
I3_RMS_rc@AC [A]	183.5706	193.6442	10.07355	148.7561
P_L_rc@AC [W]	124849.7	-2440.771	-127290.4	64320.87
Q_L_rc@AC [var]	2140.467	66059.48	63919.02	43770.07
I_fund_Q_SYM+_rc@AC [A]	0.165625	187.3359	187.1703	90.49251
I_fund_Q_SYM-_rc@AC [A]	0.808981	8.926680	8.117698	10.34478
I_fund_P_SYM+_rc@AC [A]	180.9471	-7.227889	-188.1749	94.79850
I_fund_P_SYM-_rc@AC [A]	-0.711646	-11.55841	-10.84676	15.89342
U_fund_SYM+_rc@AC [V]	398.3535	197.0302	-201.3233	277.4619
U_fund_SYM-_rc@AC [V]	0.790729	0.644111	-0.146619	18.67457
U+ [V]	230.0079	188.1902	-41.81774	210.1419
U- [V]	0.864569	10.76458	9.900012	7.636219

Injected reactive current at 60ms

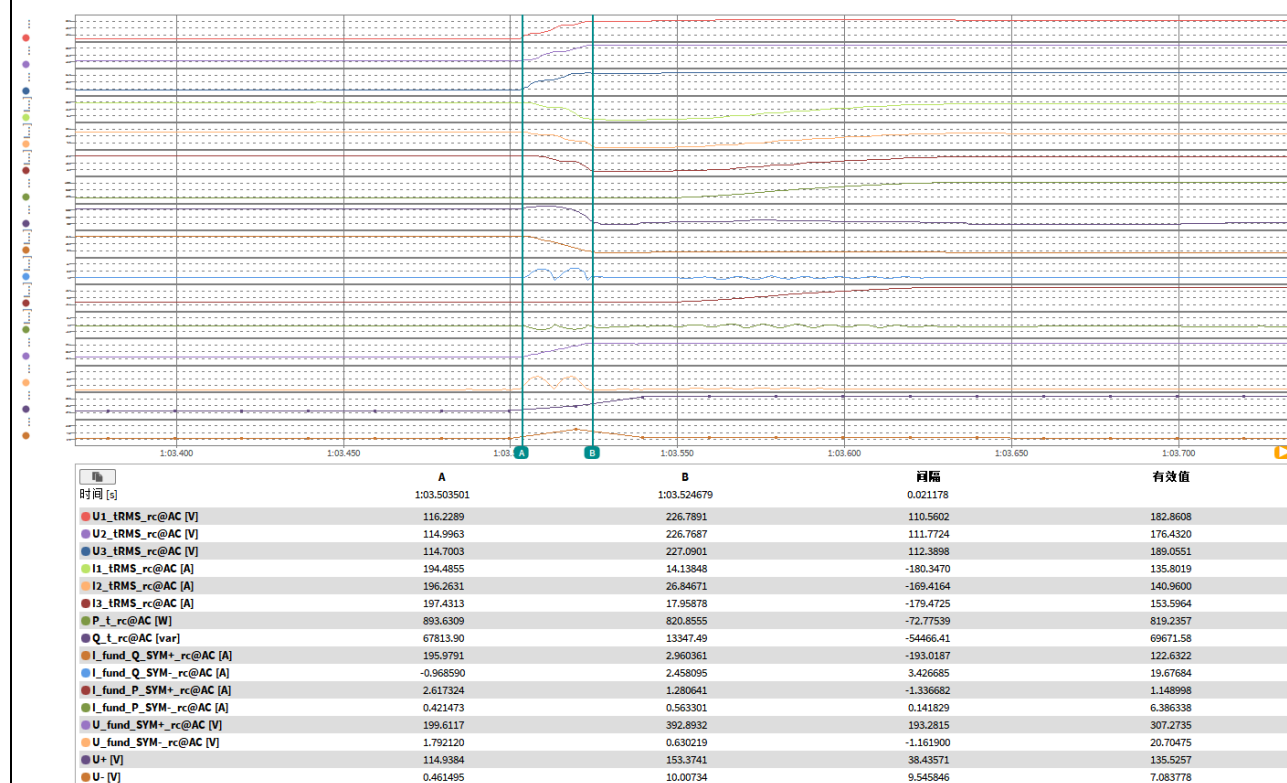


时间 [s]	A	B	间隔	有效值
1:01.603290	1:01.664146	0.060856		
U1_RMS_rc@AC [V]	230.4110	115.9674	-114.4436	142.2242
U2_RMS_rc@AC [V]	229.8167	115.9305	-113.8862	145.3752
U3_RMS_rc@AC [V]	229.7535	111.4041	-118.3495	136.2936
I1_RMS_rc@AC [A]	179.1814	198.7432	19.56181	180.5412
I2_RMS_rc@AC [A]	180.1725	199.9033	19.73079	179.9205
I3_RMS_rc@AC [A]	183.5706	202.7452	19.17453	177.4298
P_L_rc@AC [W]	124849.7	-410.3049	-125260.0	45493.11
Q_L_rc@AC [var]	2140.467	68808.03	66667.56	57693.52
I_fund_Q_SYM+_rc@AC [A]	0.165625	200.3073	200.1416	155.2846
I_fund_Q_SYM-_rc@AC [A]	0.808981	-1.398256	-2.207238	7.441987
I_fund_P_SYM+_rc@AC [A]	180.9471	-1.221398	-182.1684	67.09846
I_fund_P_SYM-_rc@AC [A]	-0.711646	0.832641	1.544287	11.36121
U_fund_SYM+_rc@AC [V]	398.3535	196.1758	-200.1777	241.0523
U_fund_SYM-_rc@AC [V]	0.790729	2.209550	2.204820	13.26778
U+ [V]	230.0079	114.4051	-115.6028	169.2560
U- [V]	0.864569	0.980985	0.116416	5.493468

Recover time – Active power

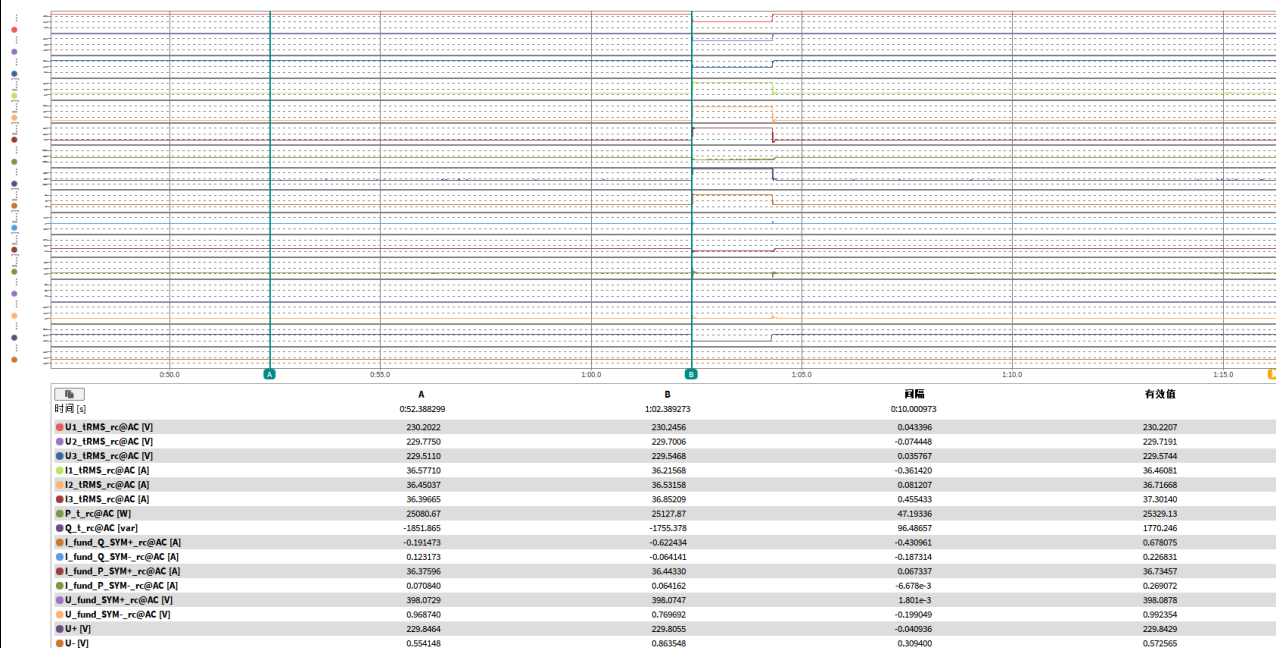


Recover time – Reactive power

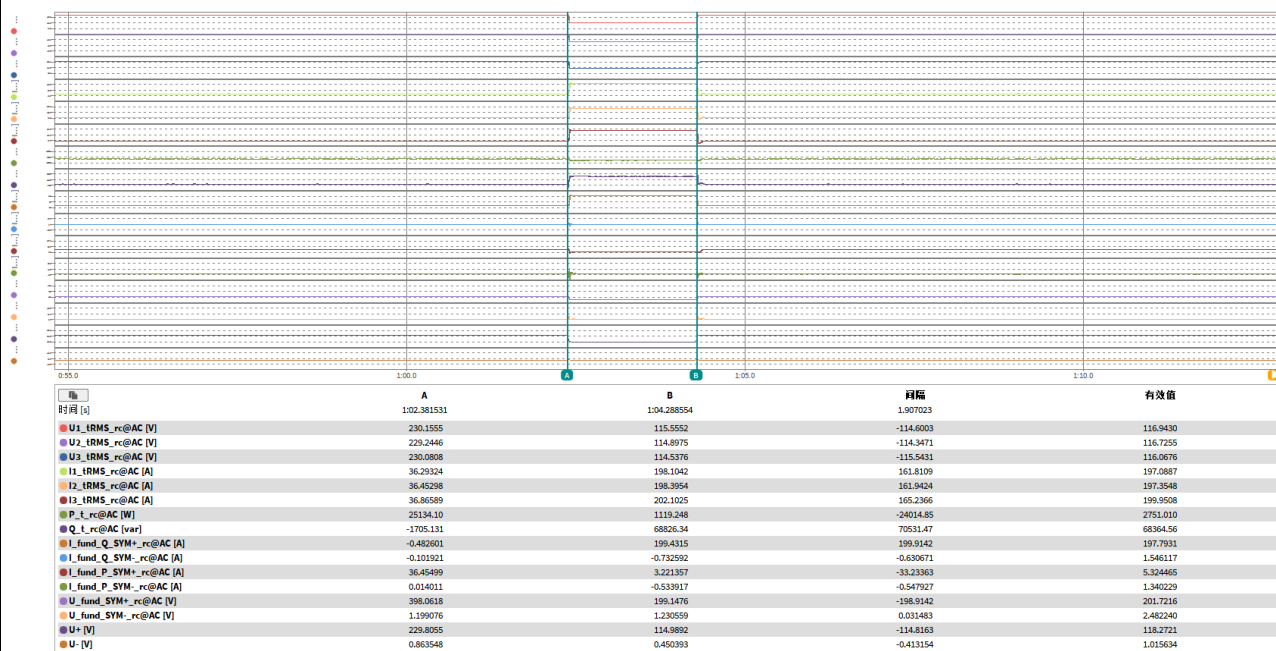


Test – VD3.2.a

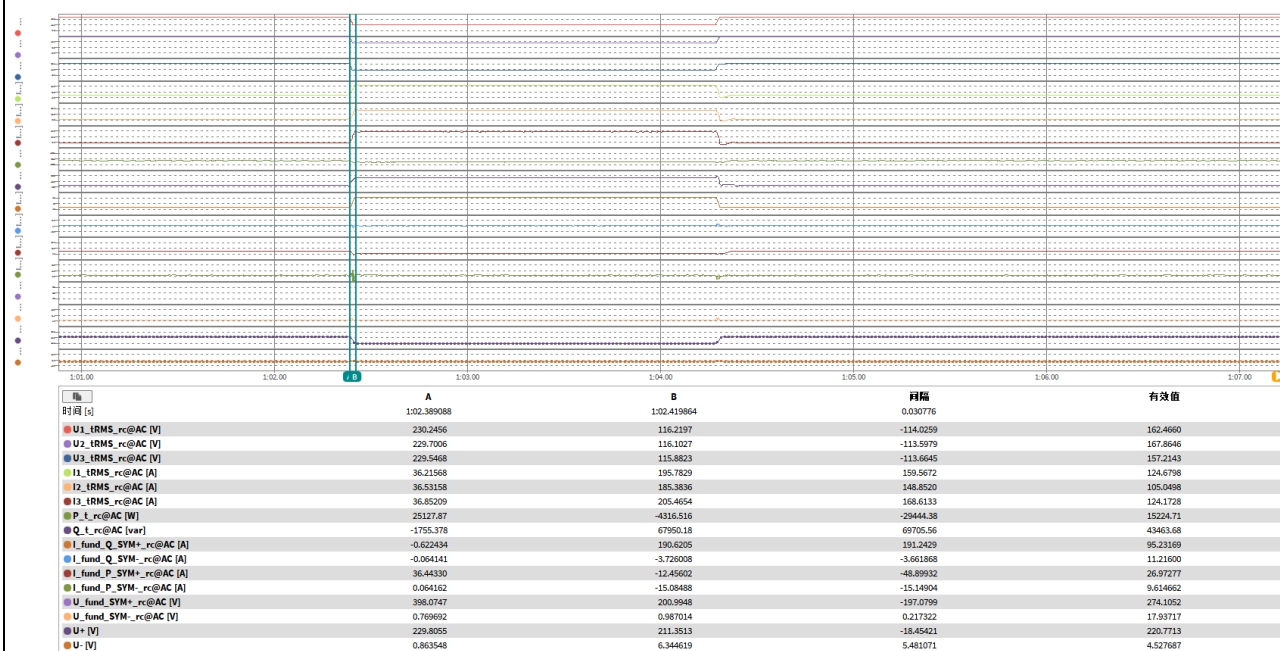
Before fault



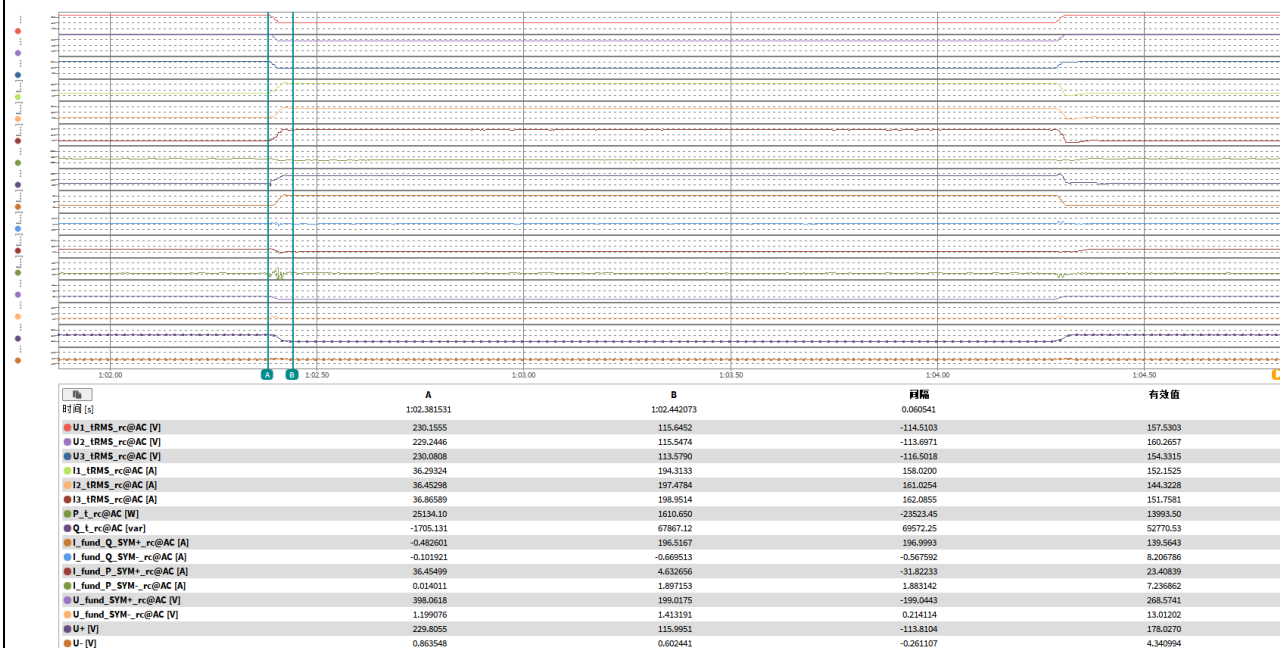
Duration fault time



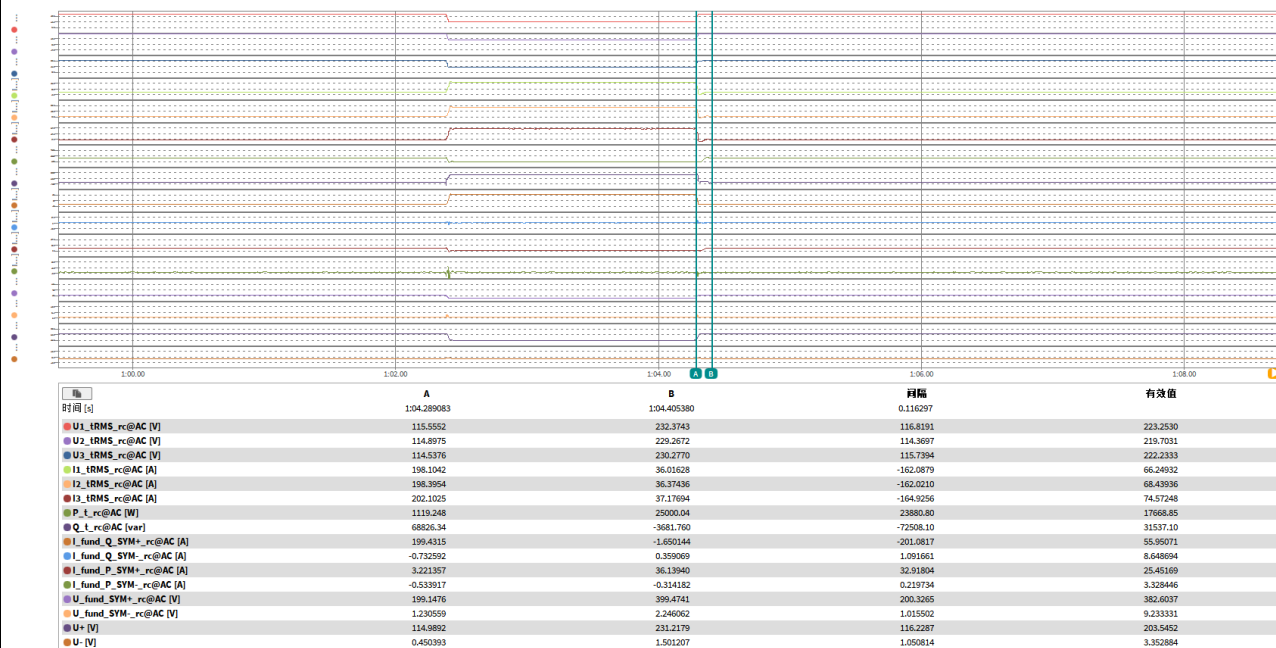
Injected reactive current at 30ms



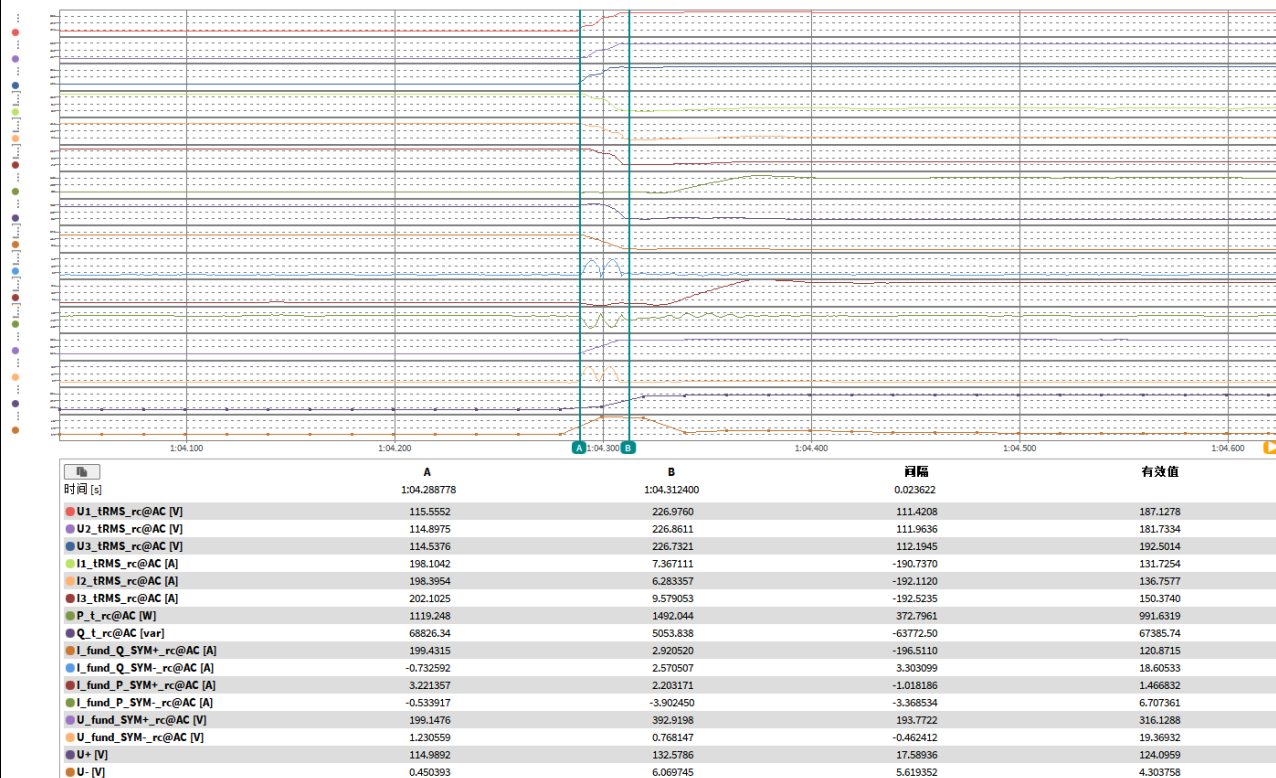
Injected reactive current at 60ms



Recover time – Active power

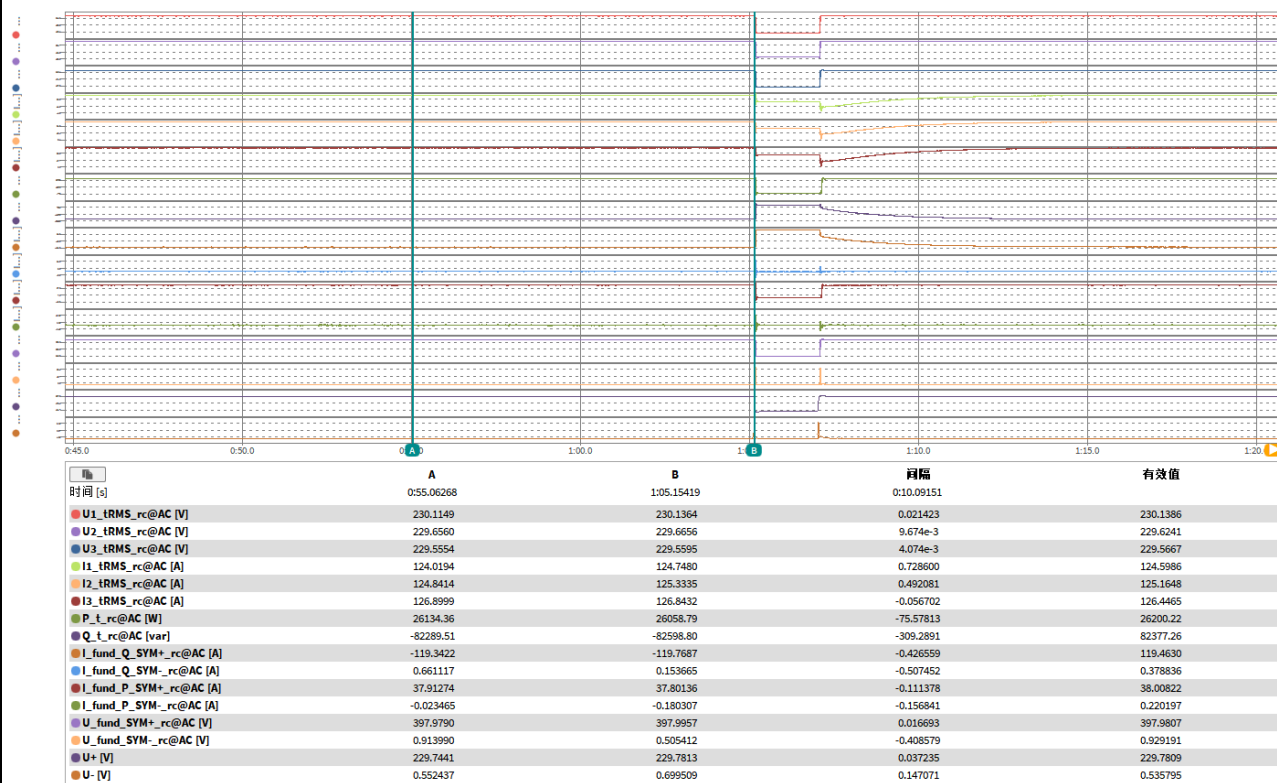


Recover time – Reactive power

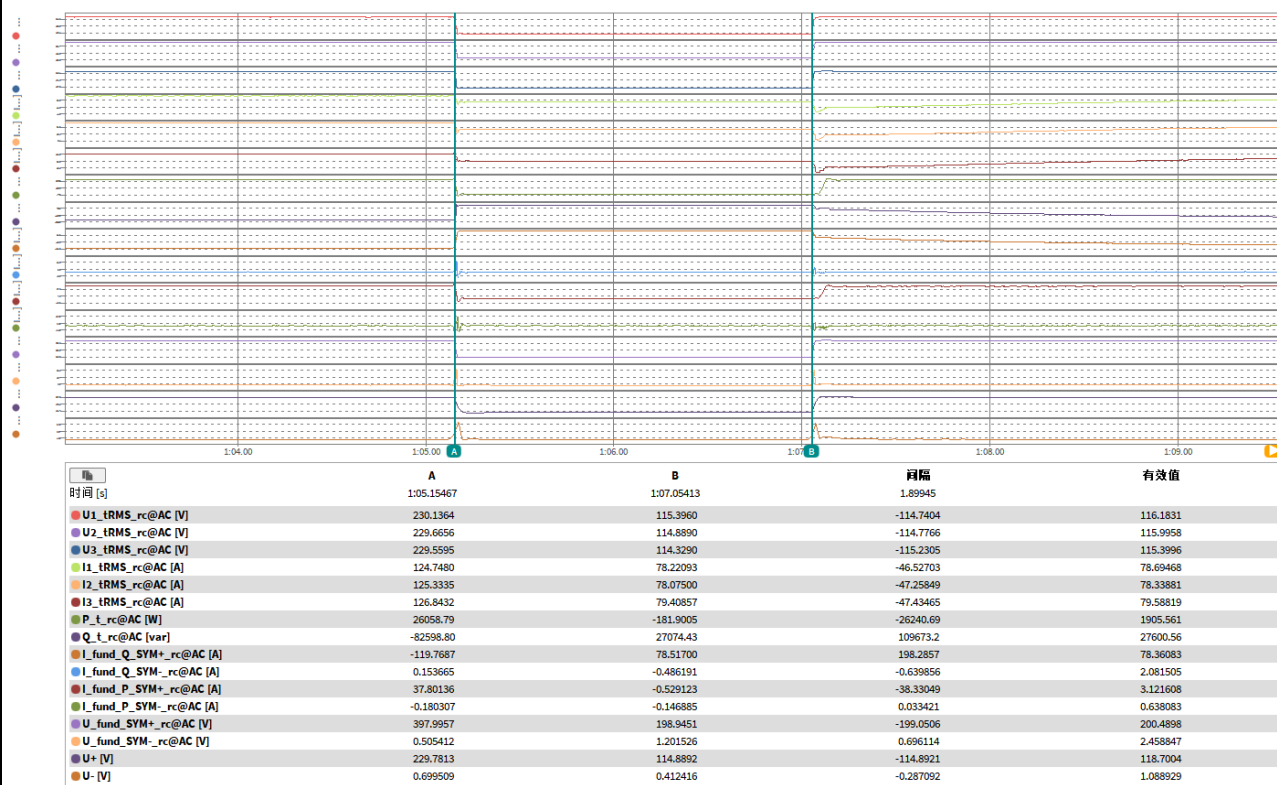


Test – VD3.2.b

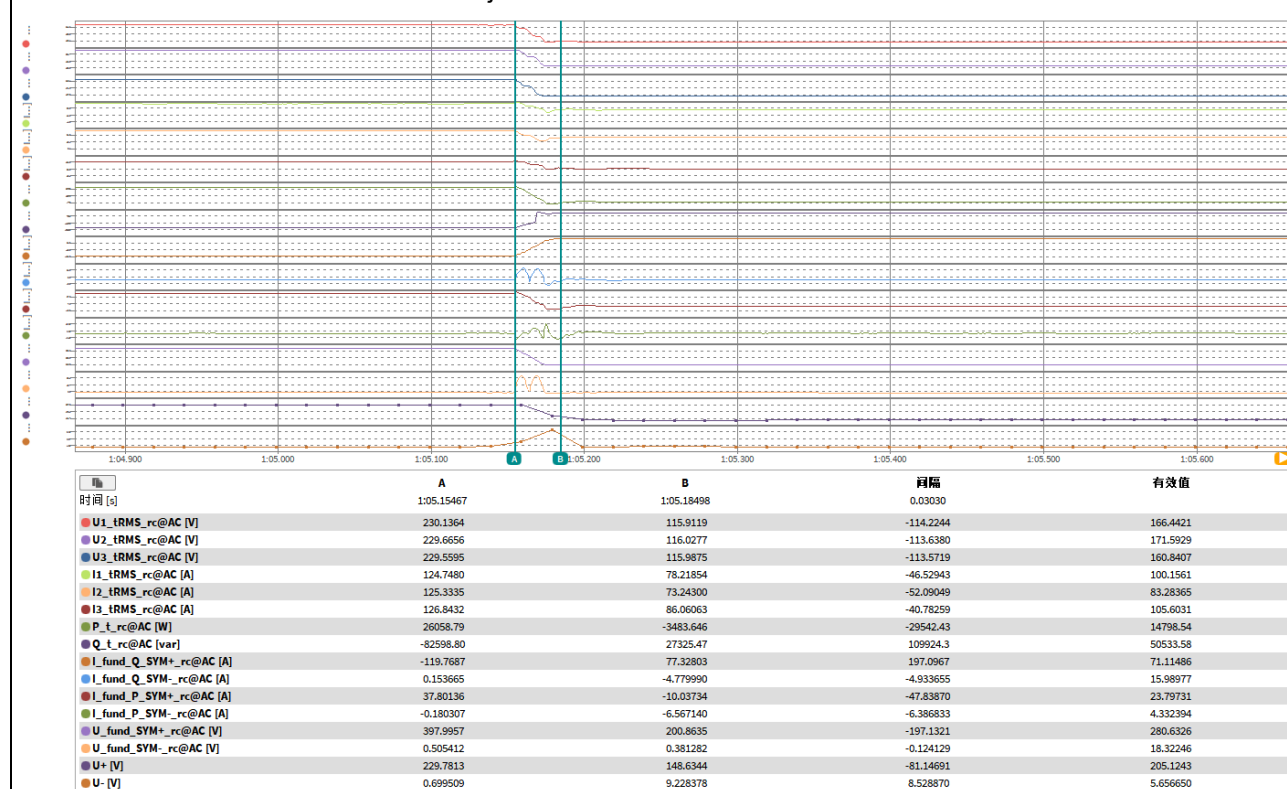
Before fault



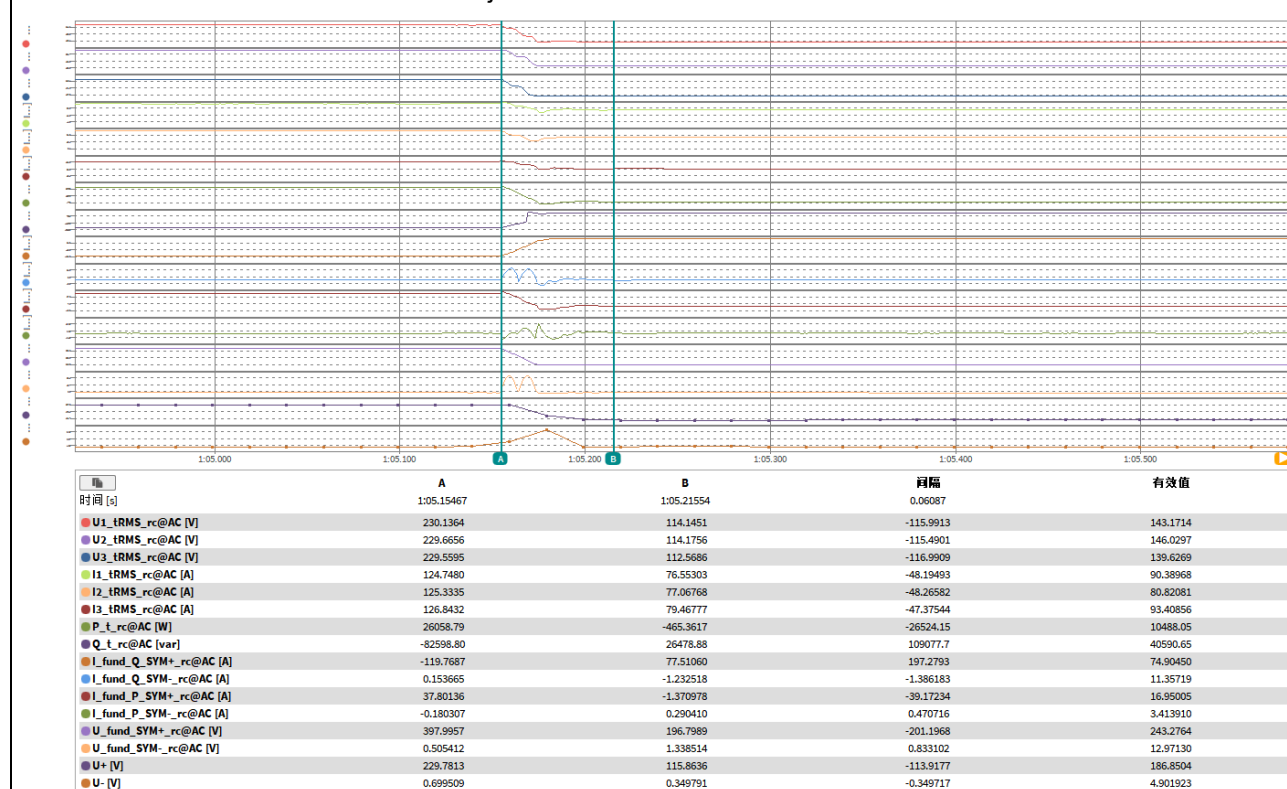
Duration fault time



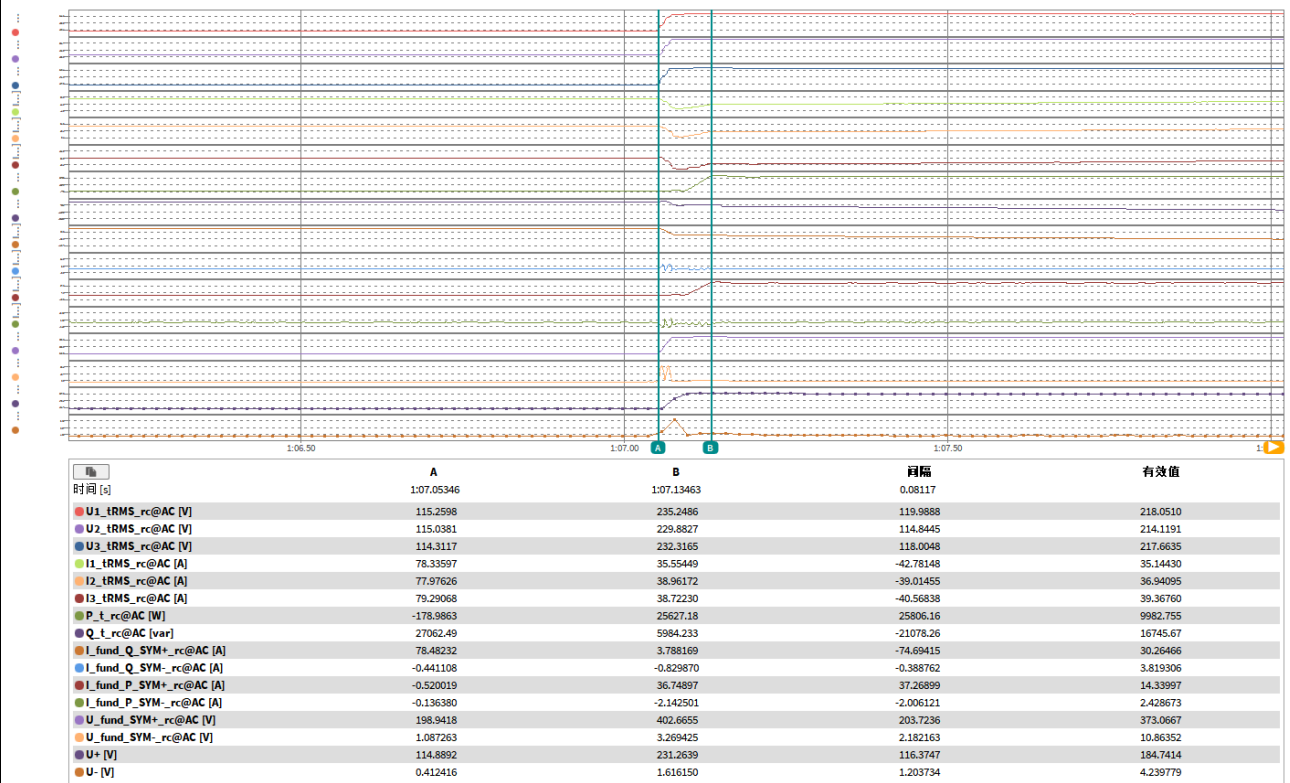
Injected reactive current at 30ms



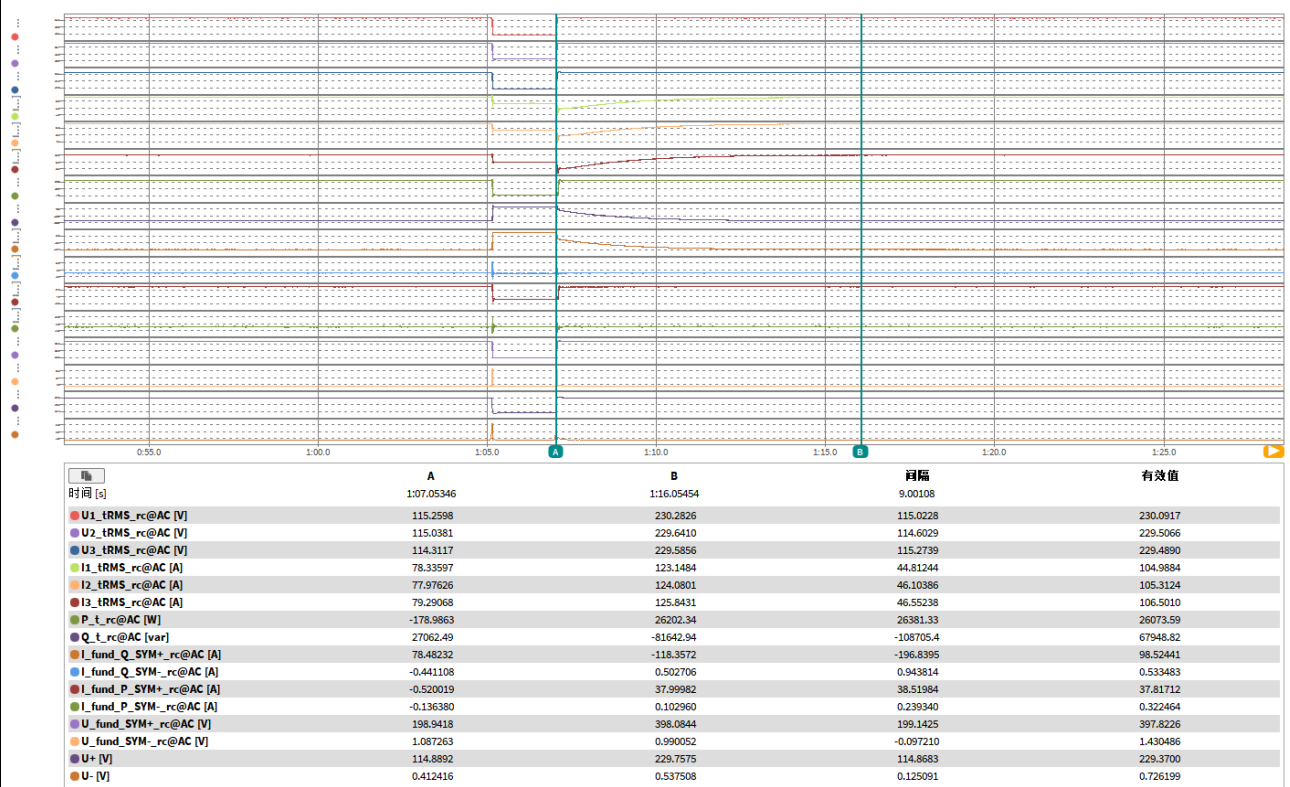
Injected reactive current at 60ms



Recover time – Active power



Recover time – Reactive power

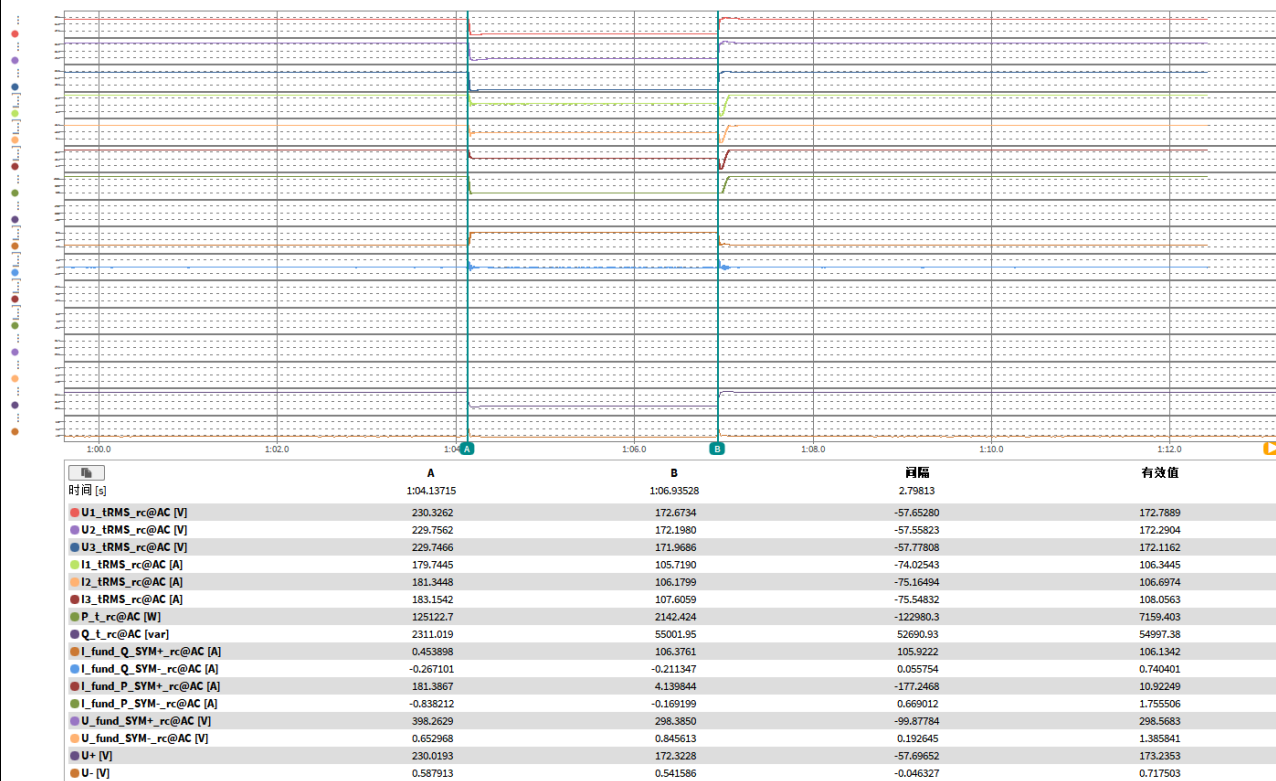


Test – VD4.1

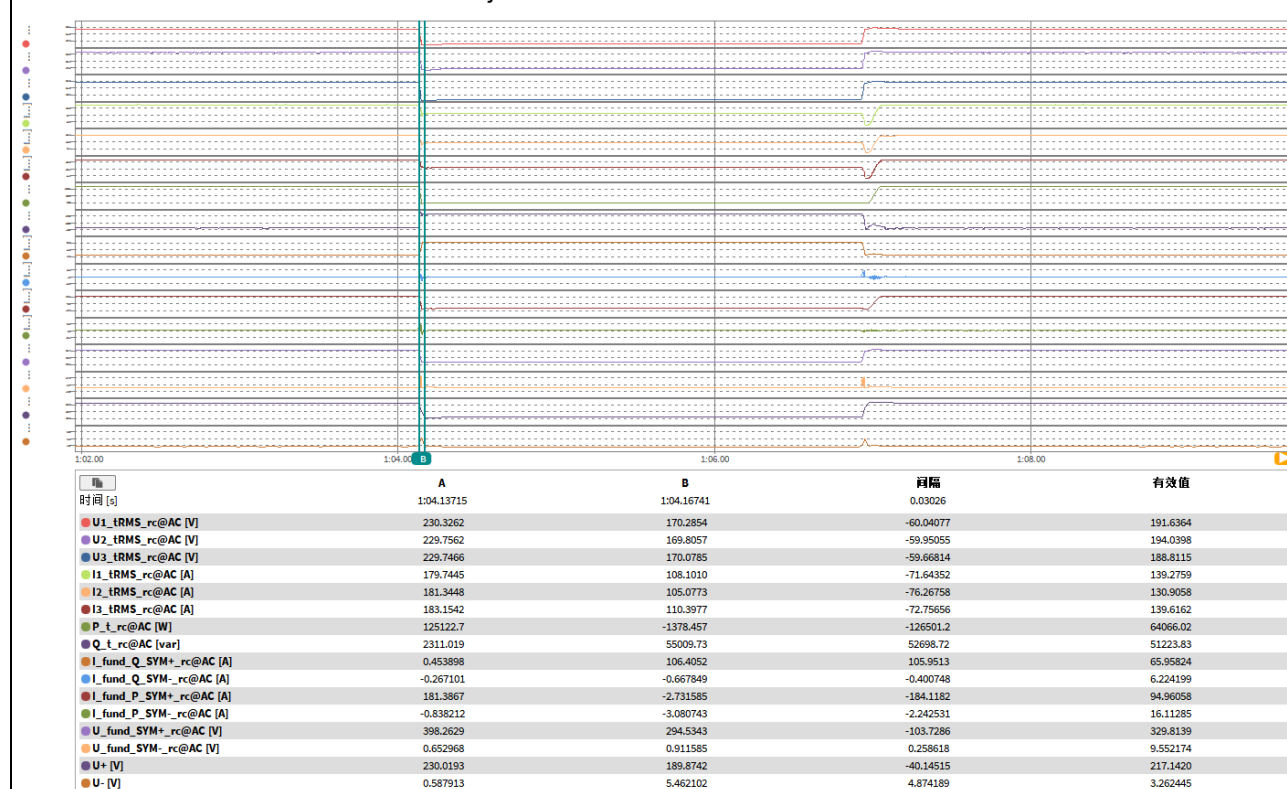
Before fault



Duration fault time



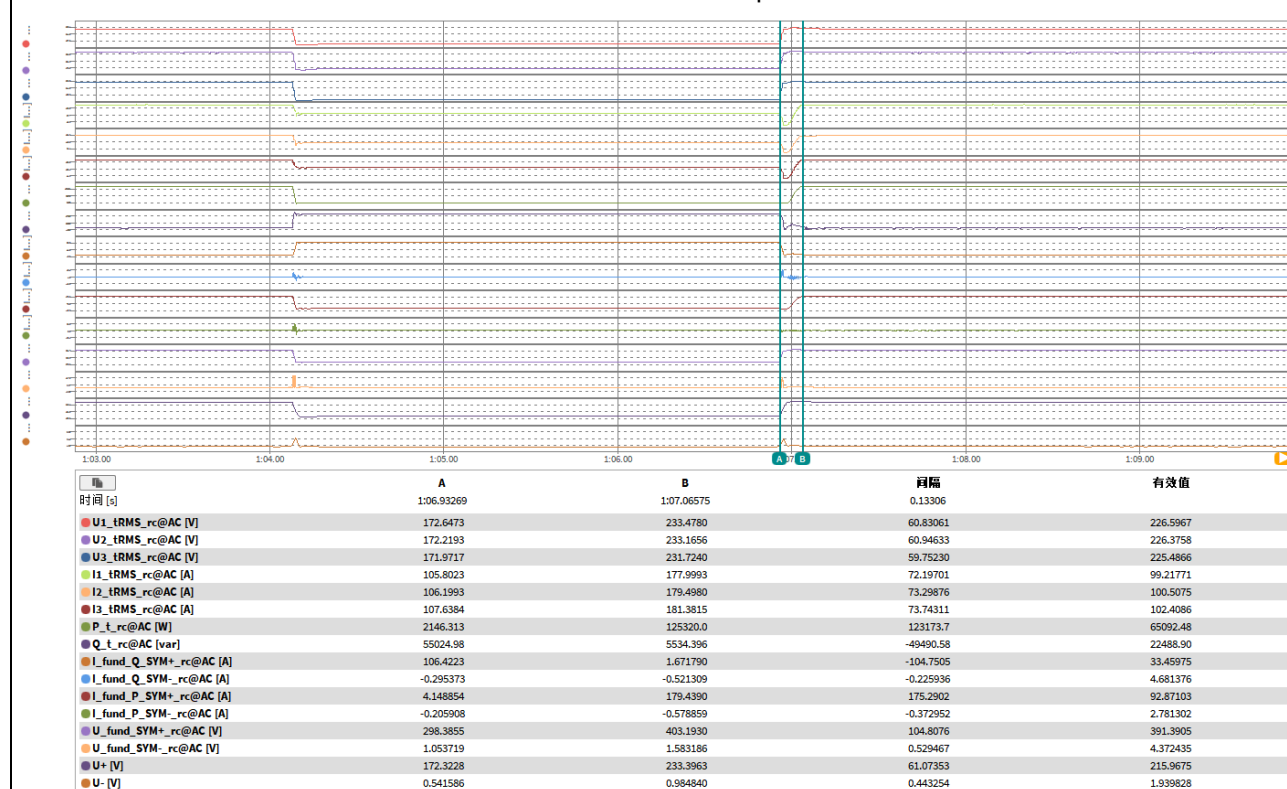
Injected reactive current at 30ms



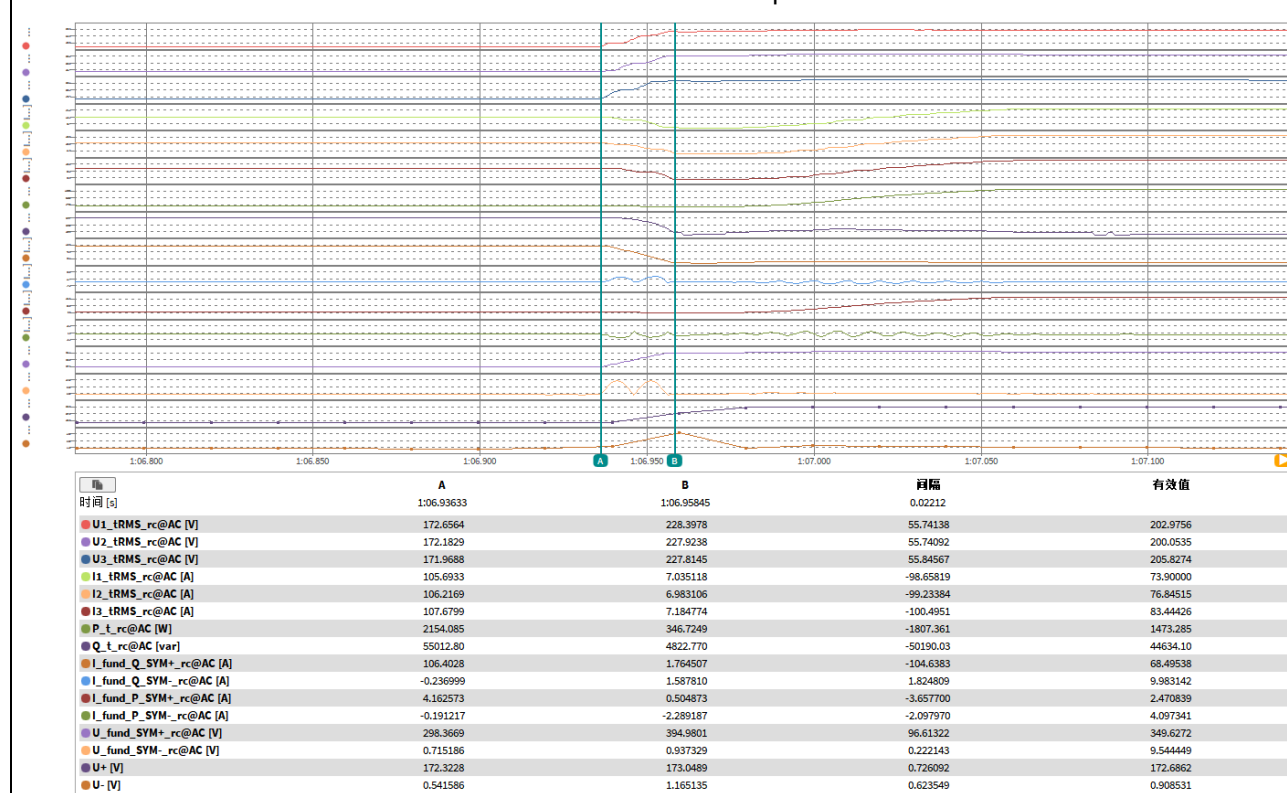
Injected reactive current at 60ms



Recover time – Active power

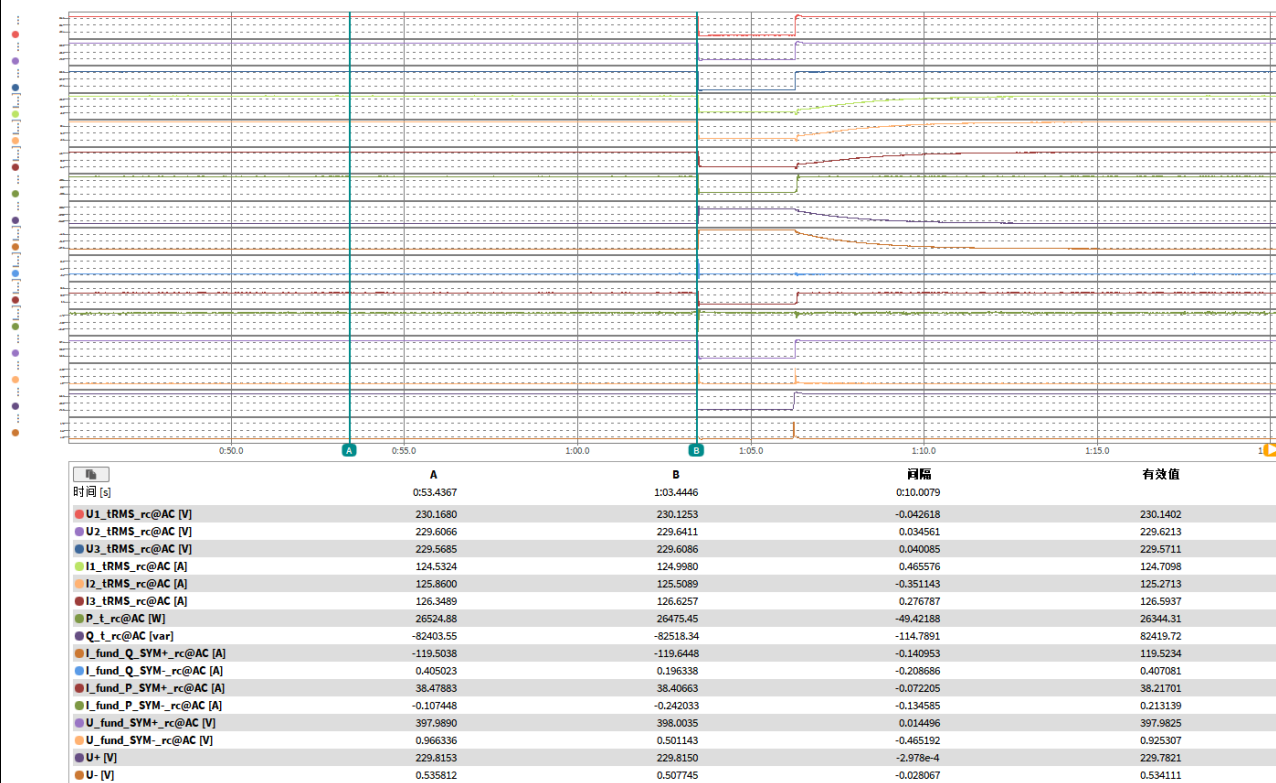


Recover time – Reactive power

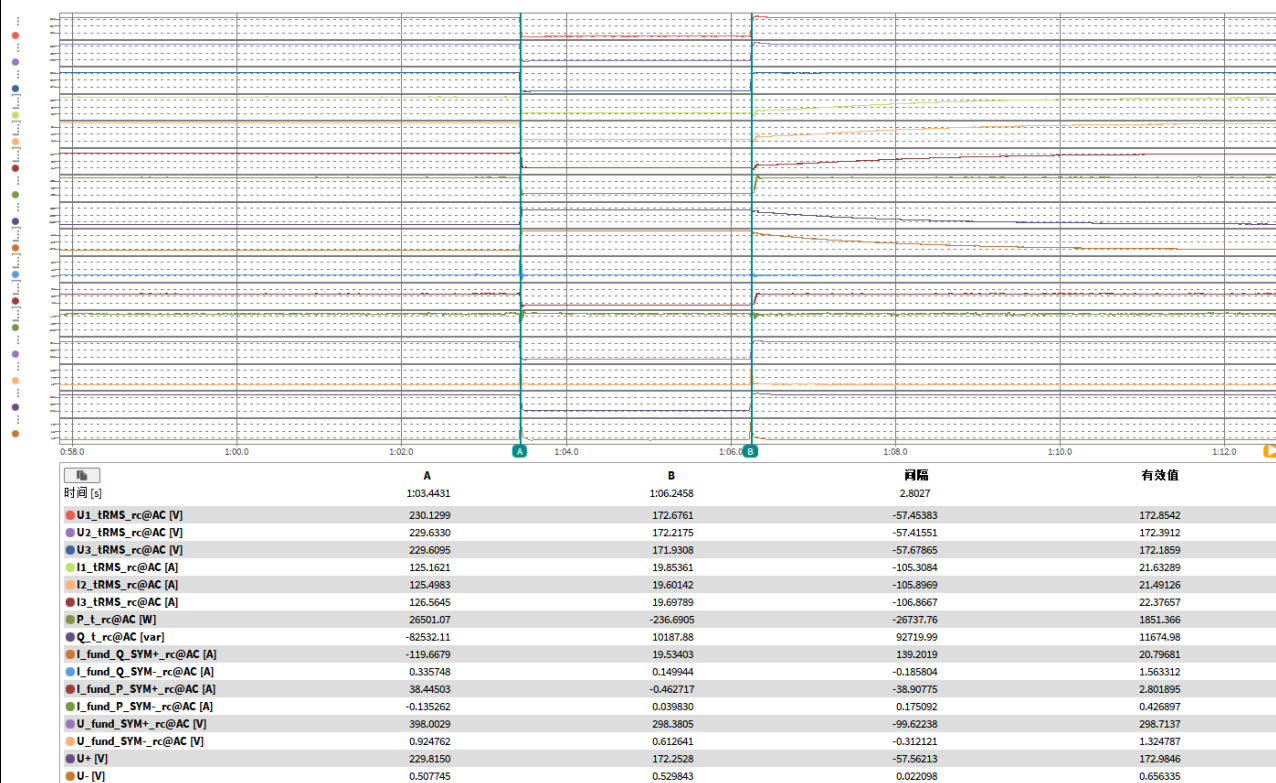


Test – VD4.2.a

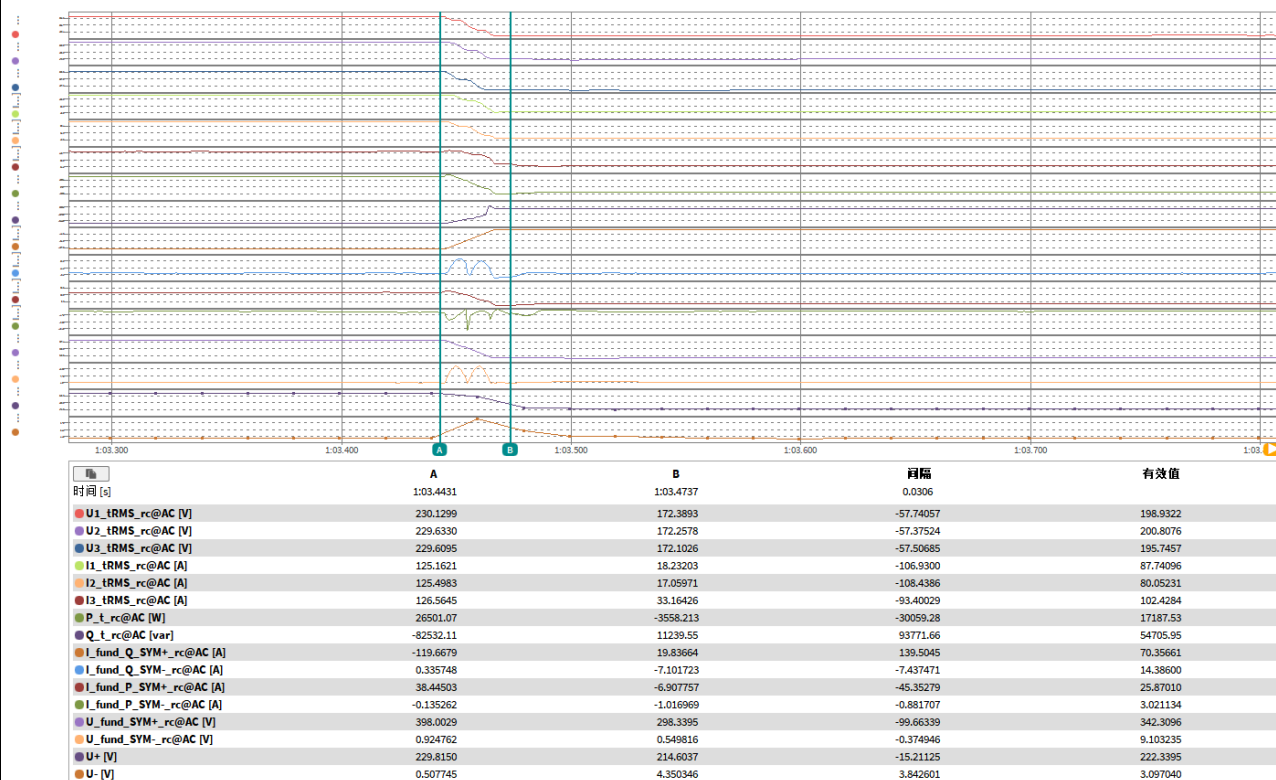
Before fault



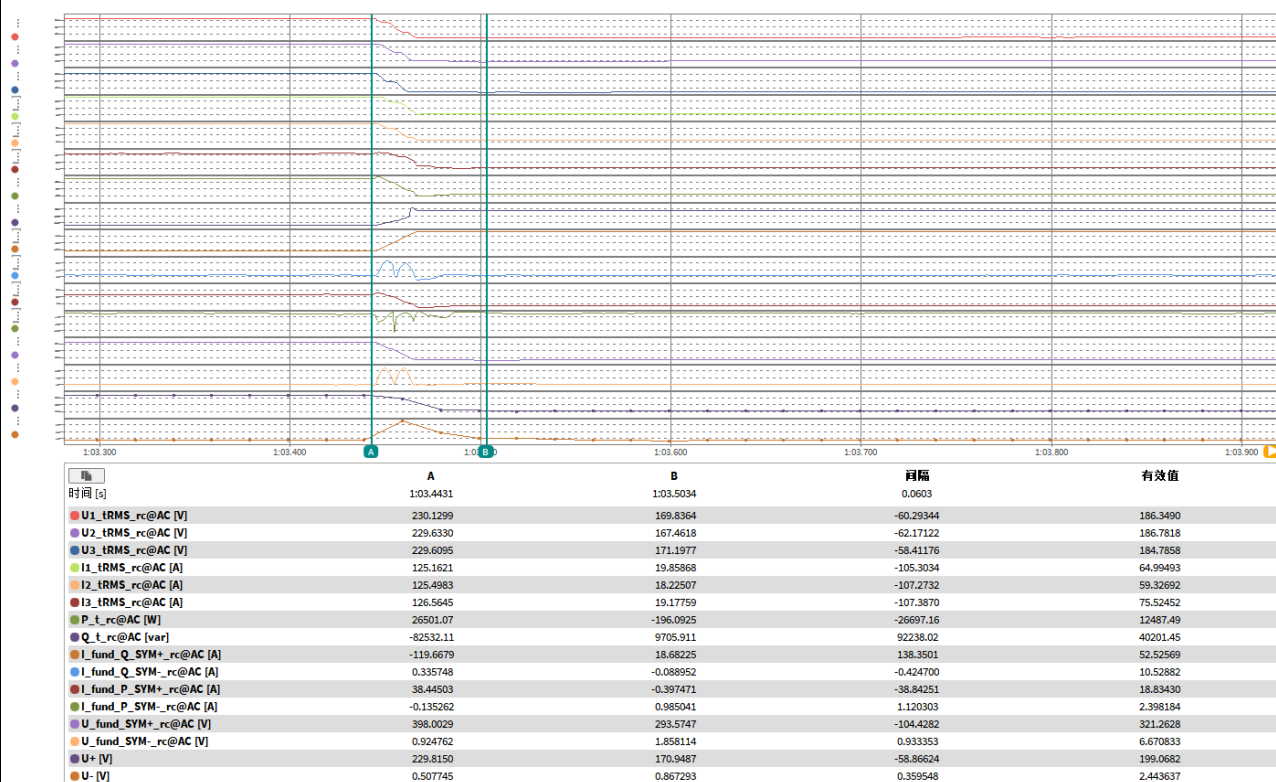
Duration fault time



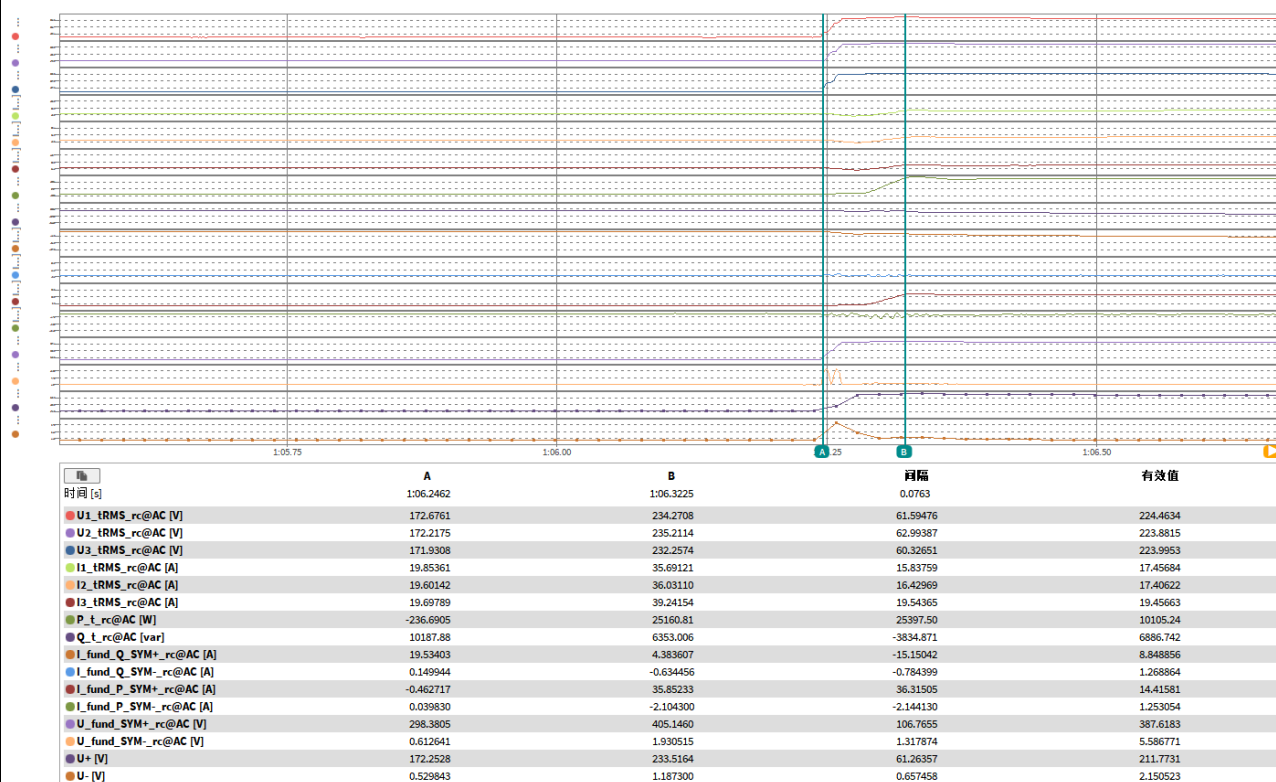
Injected reactive current at 30ms



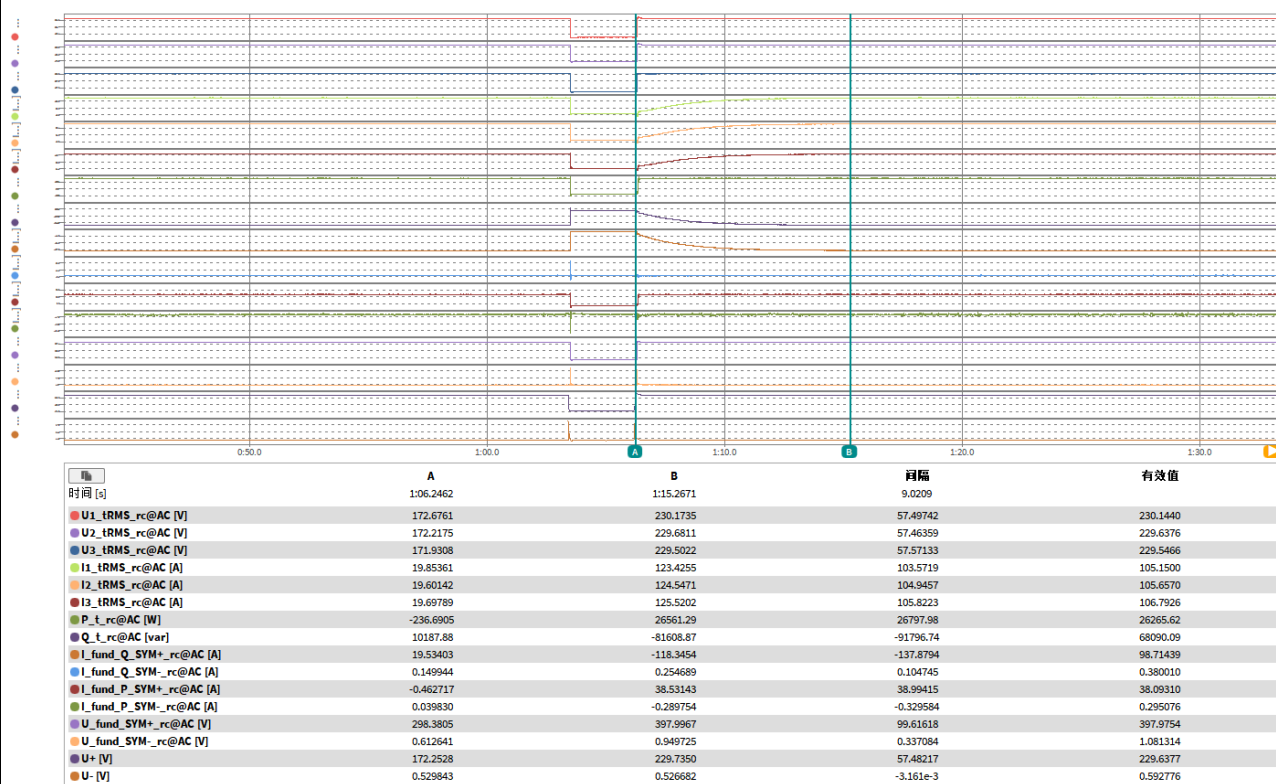
Injected reactive current at 60ms



Recover time – Active power

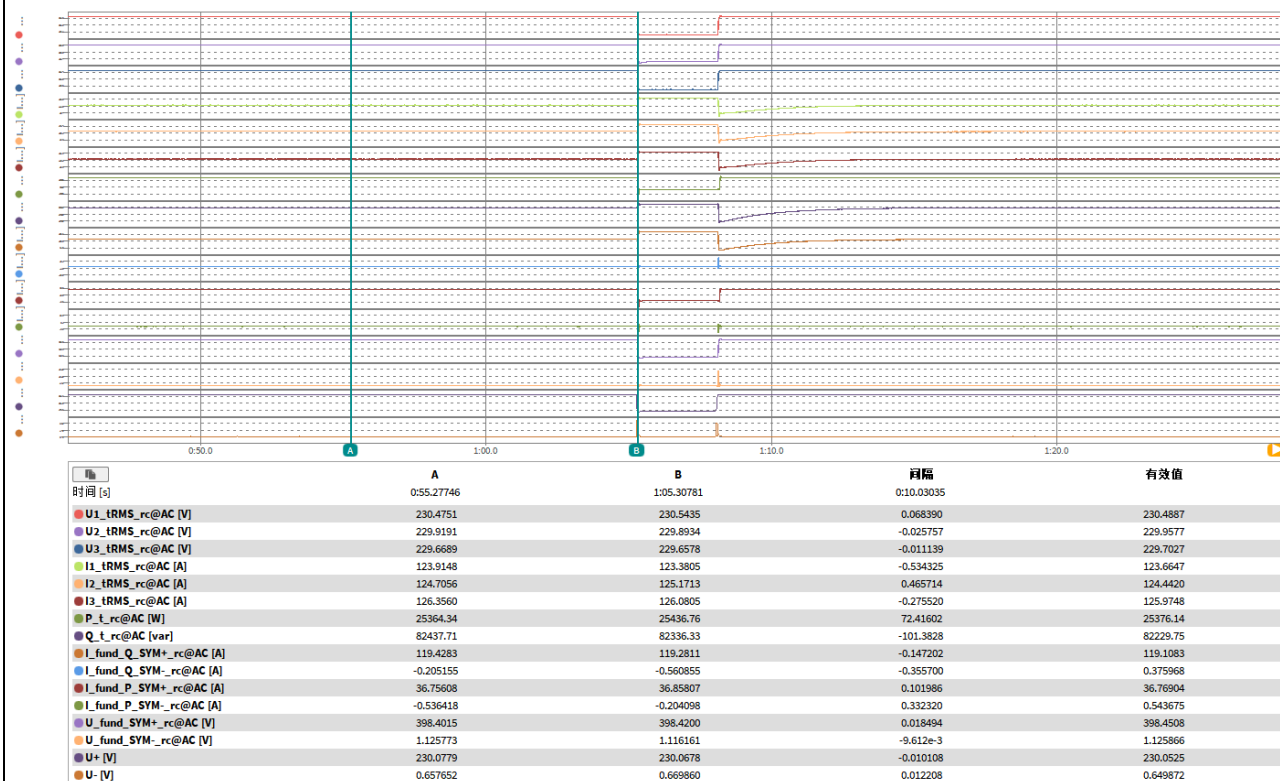


Recover time – Reactive power

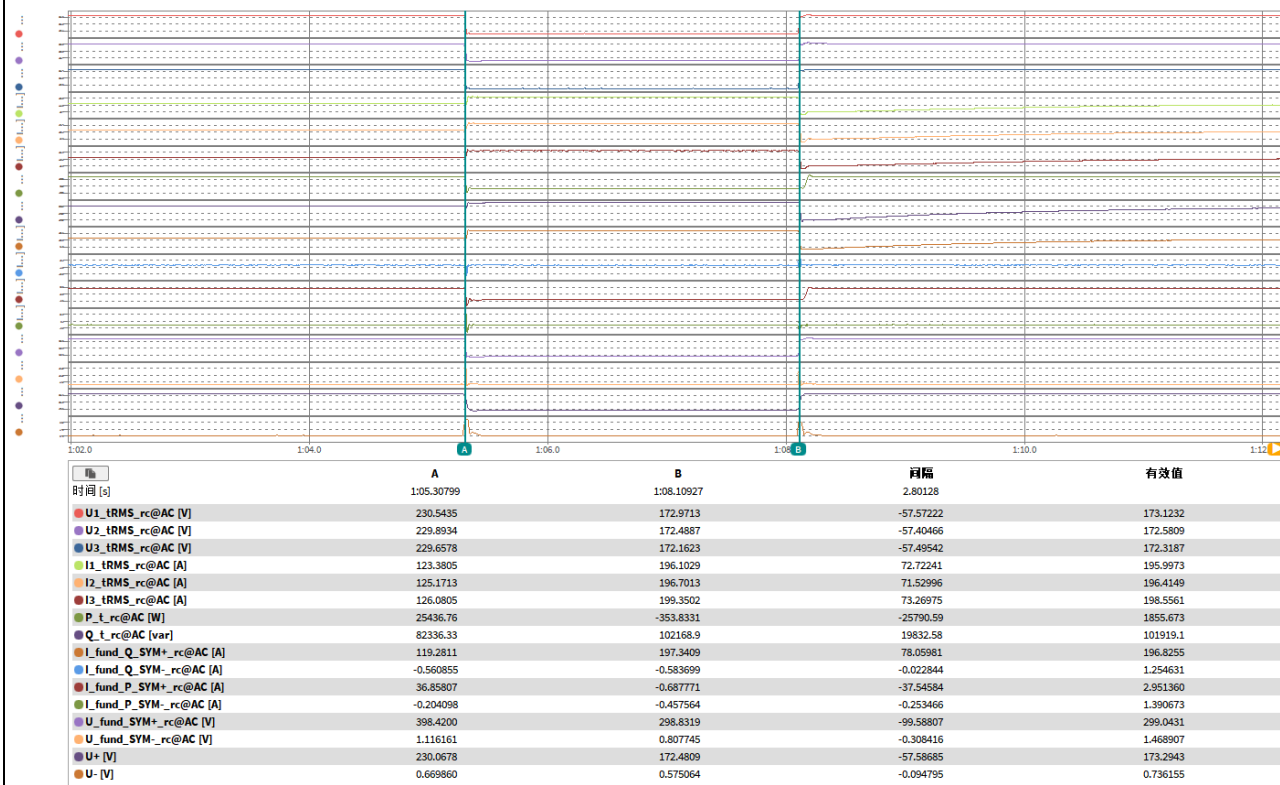


Test – VD4.2.b

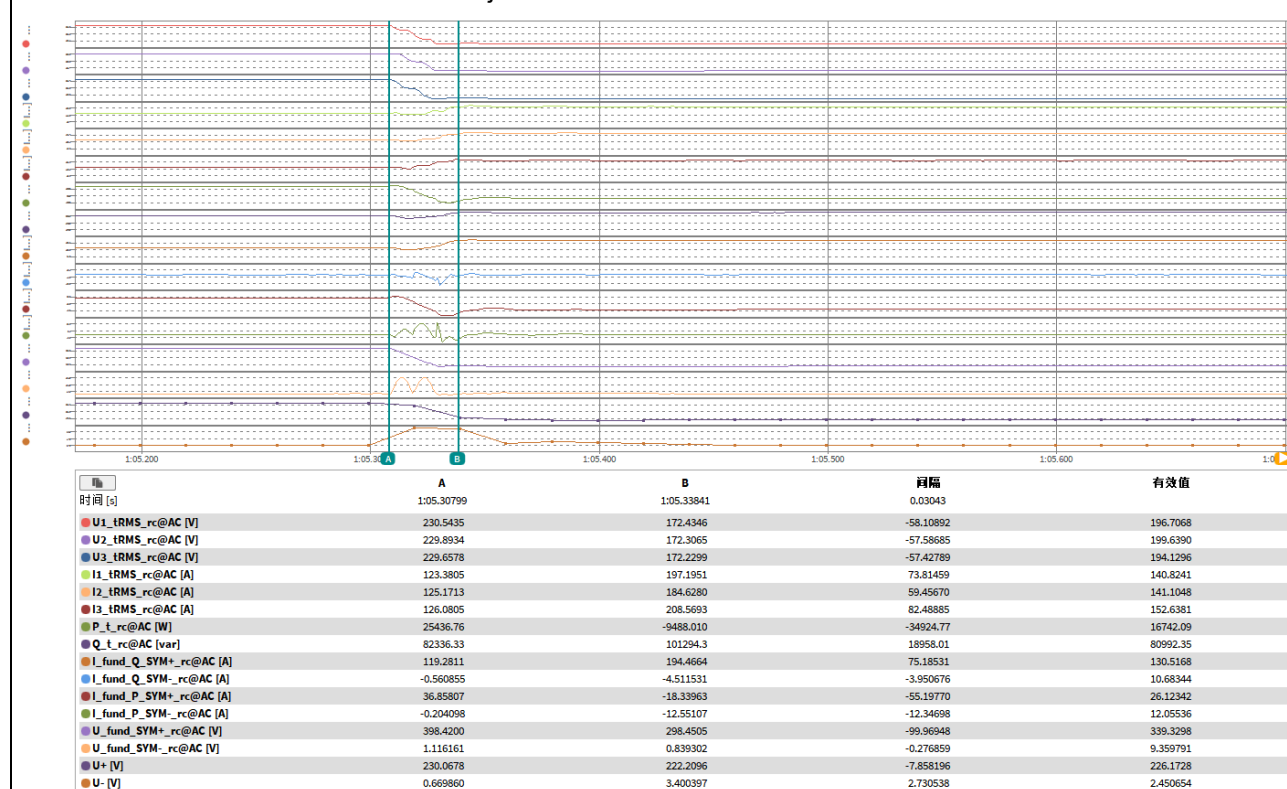
Before fault



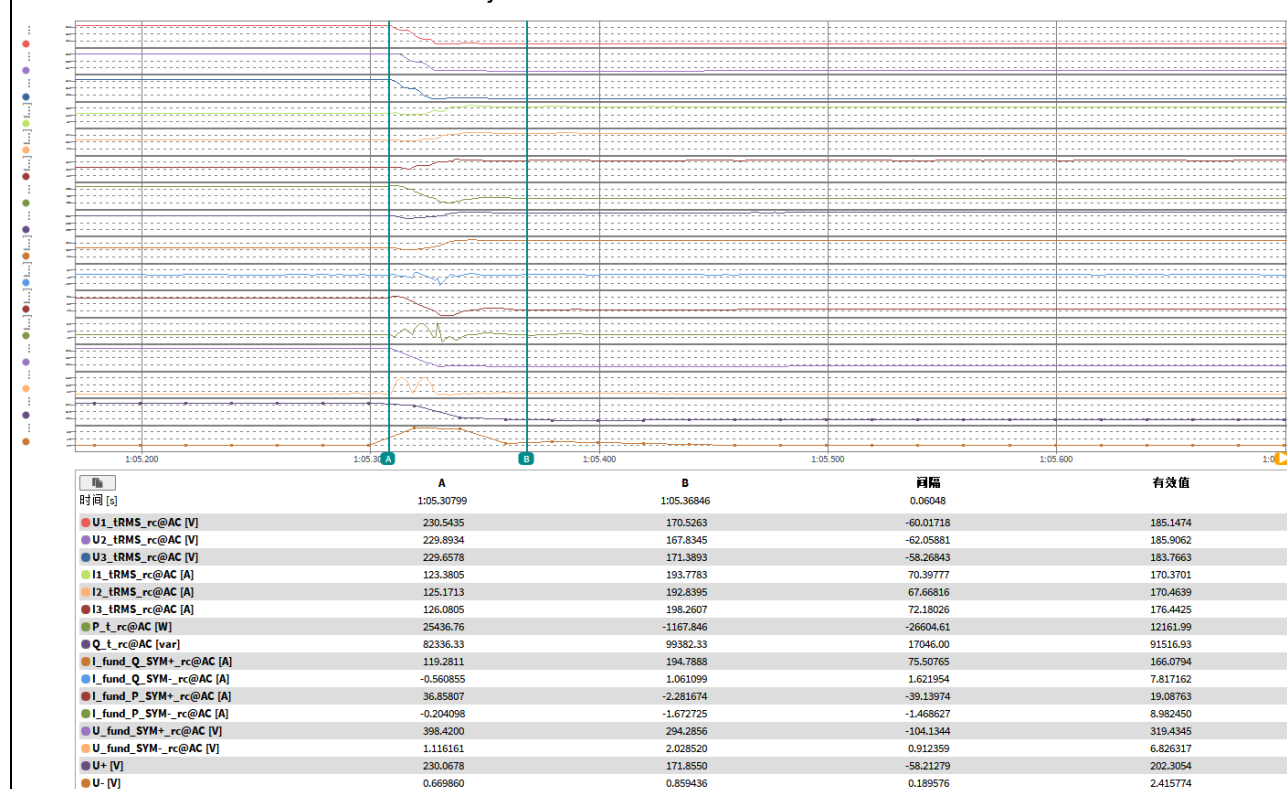
Duration fault time



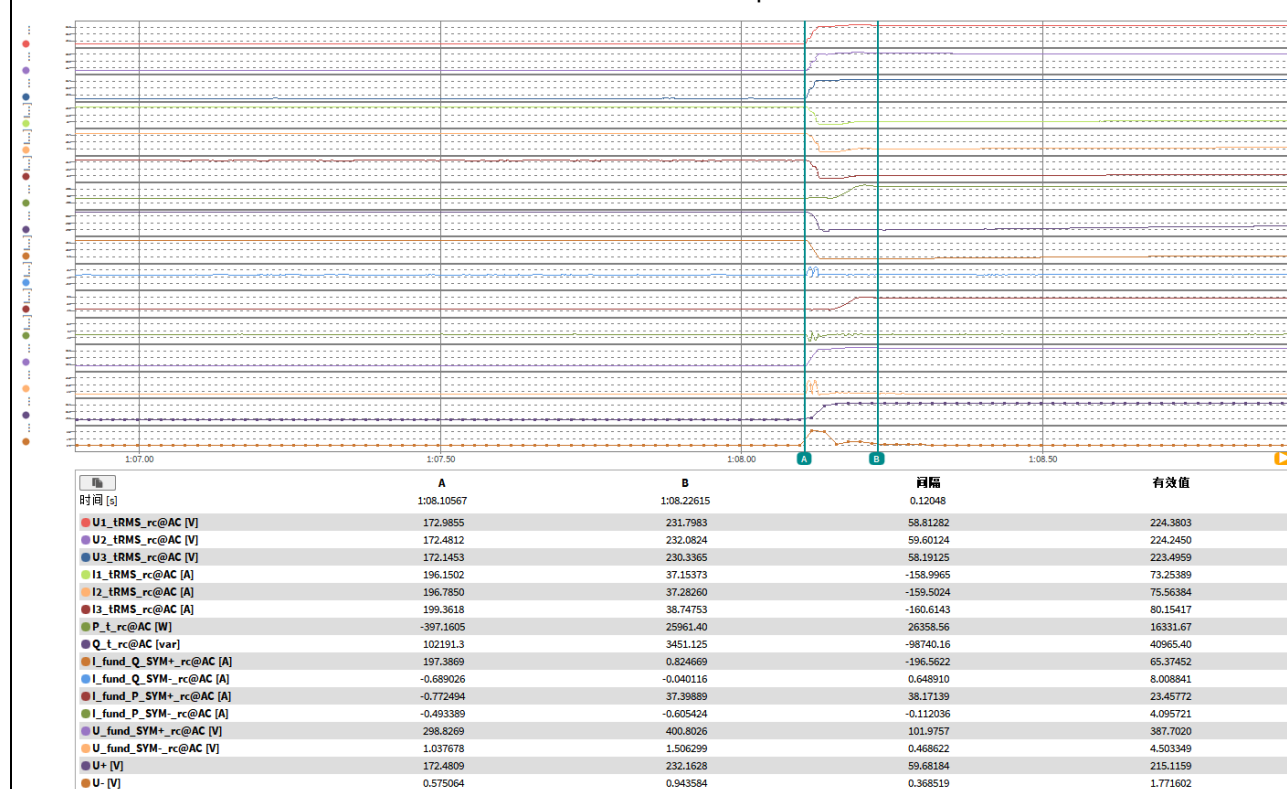
Injected reactive current at 30ms



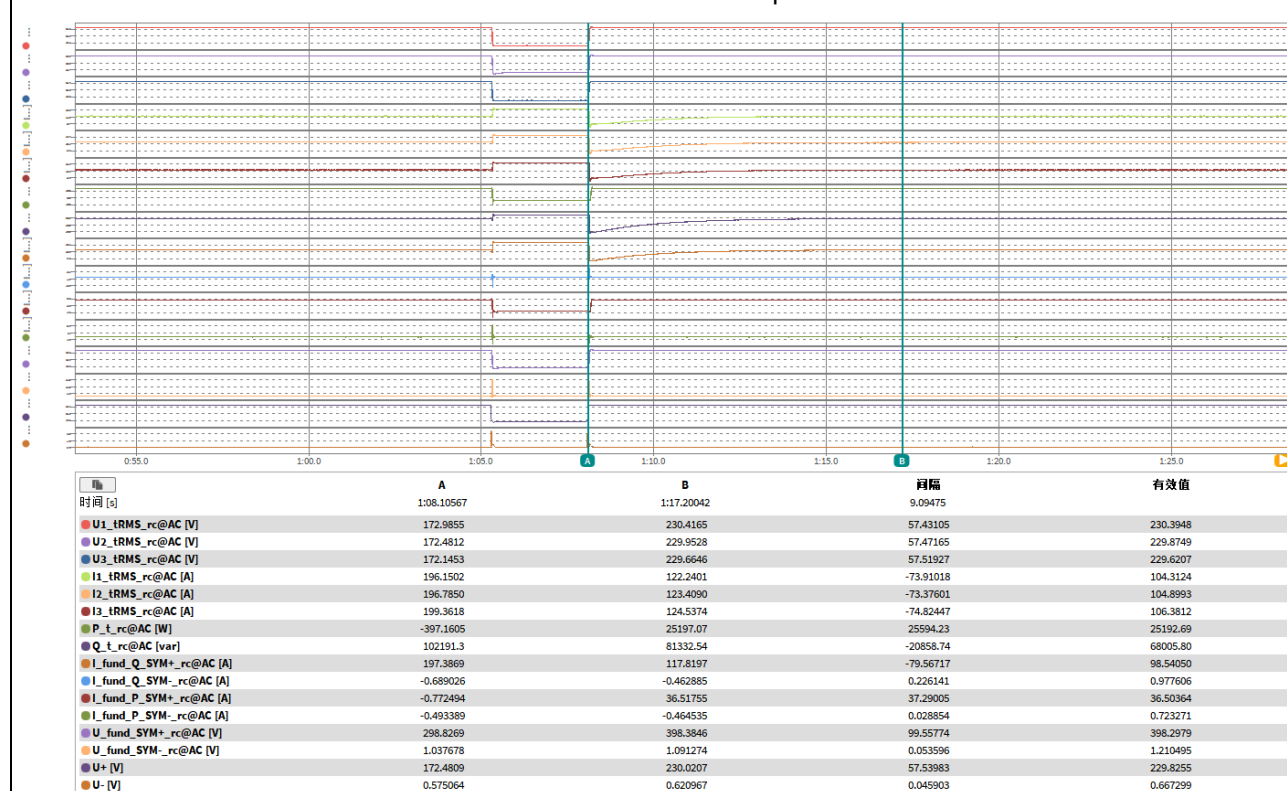
Injected reactive current at 60ms



Recover time – Active power

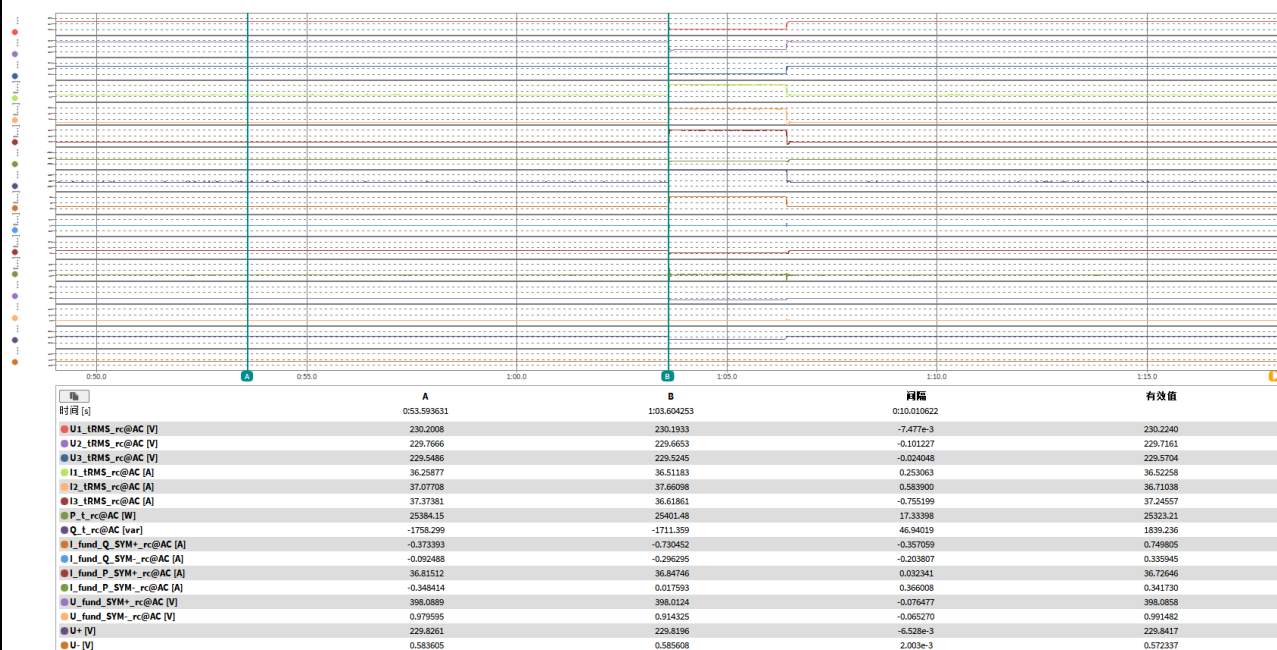


Recover time – Reactive power

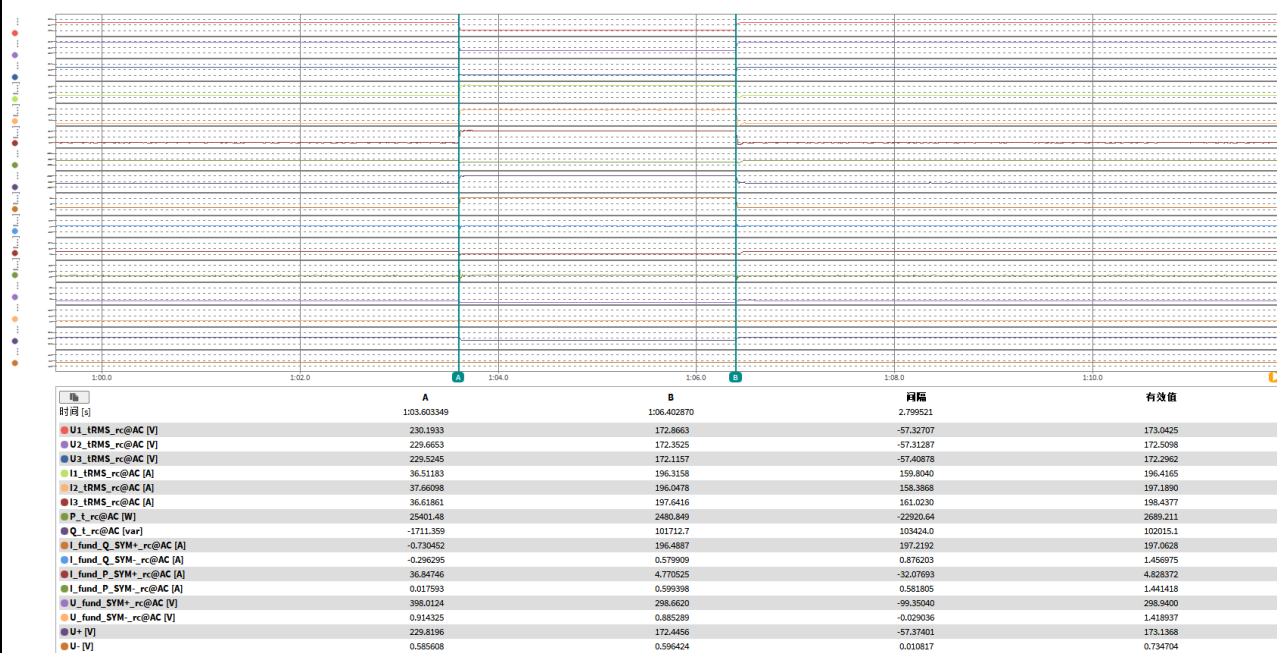


Test – VD4.2.c

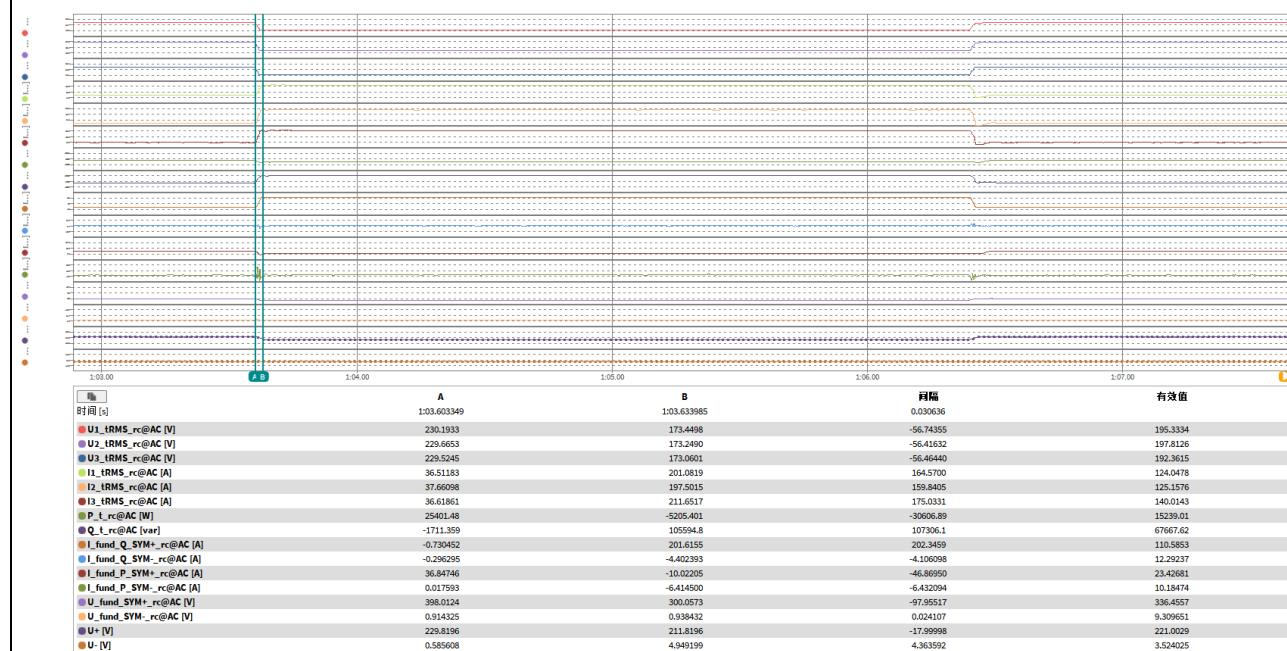
Before fault



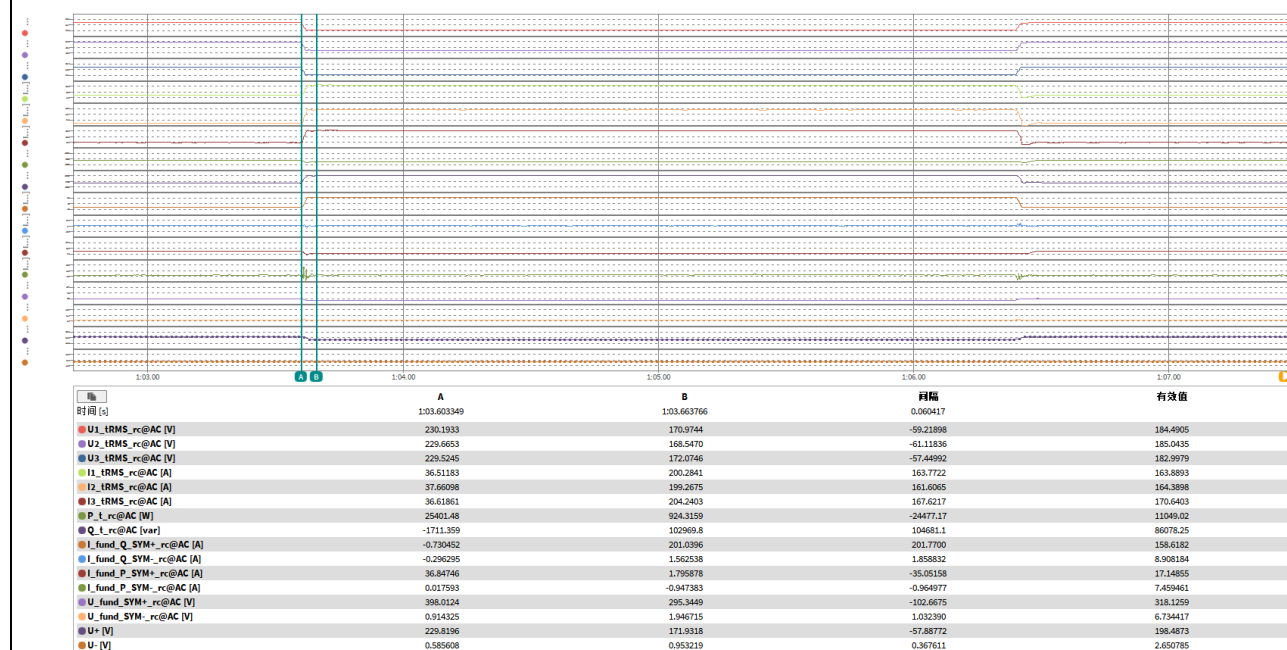
Duration fault time



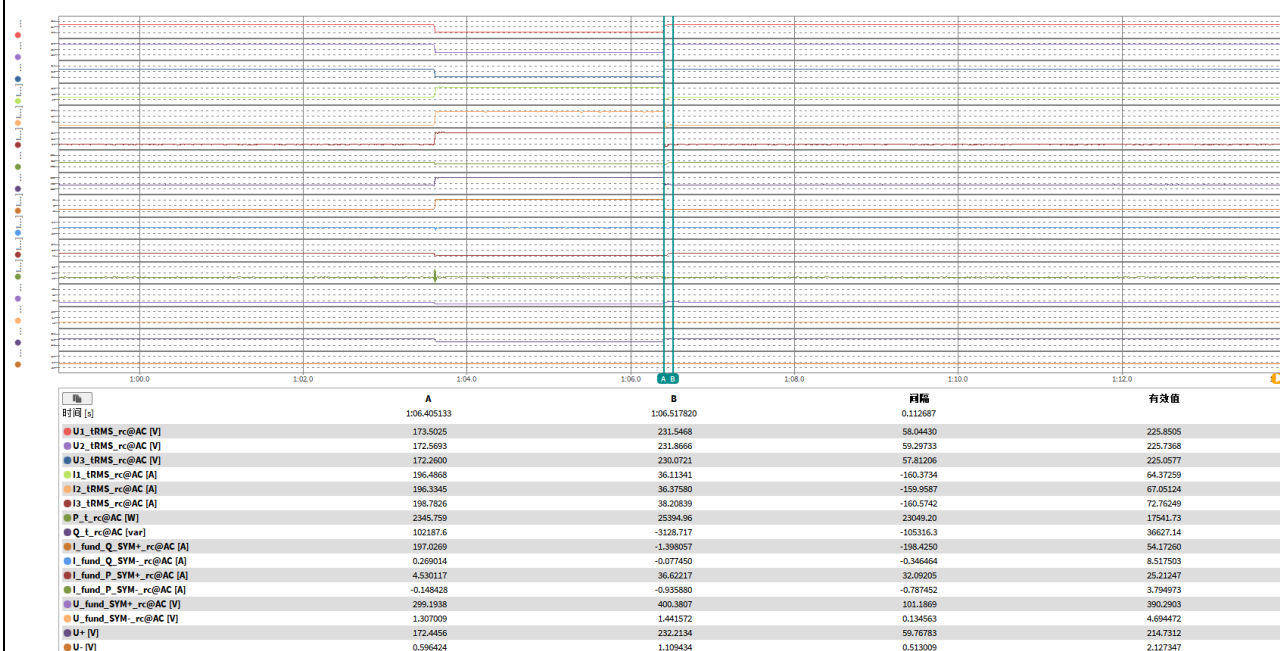
Injected reactive current at 30ms



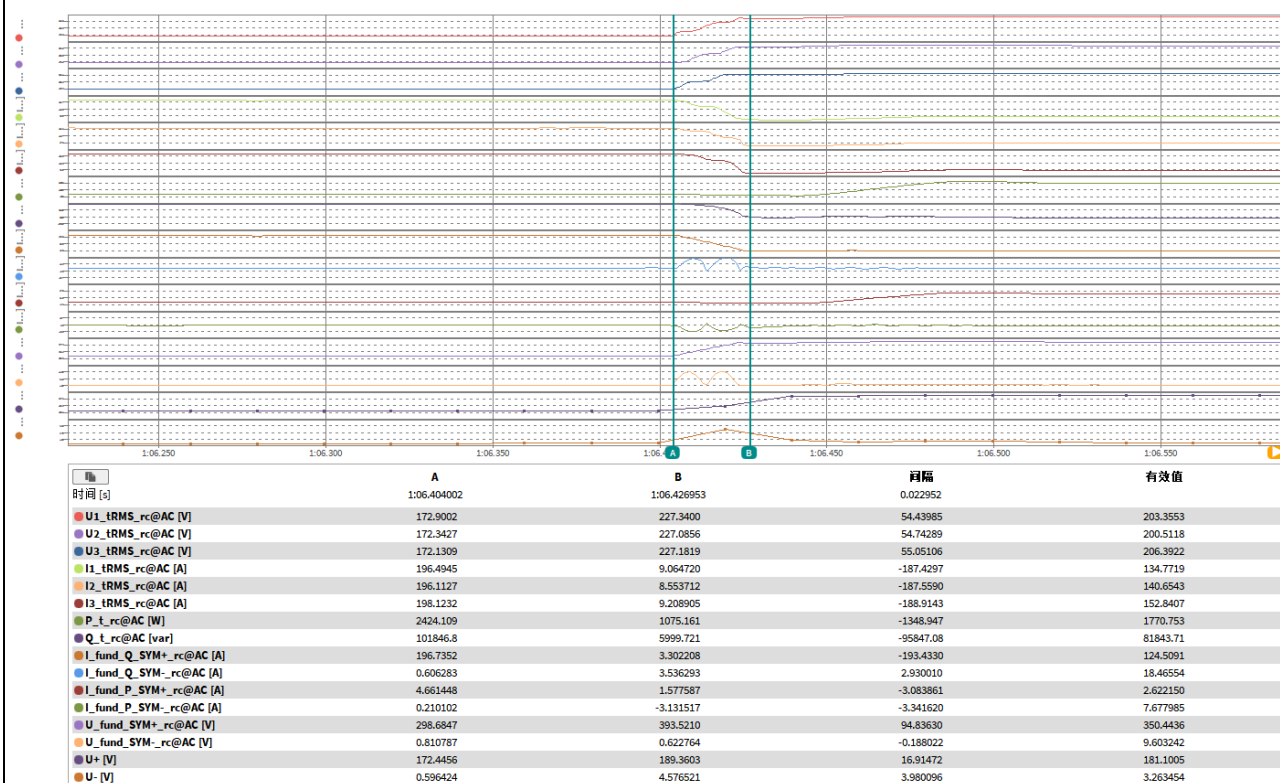
Injected reactive current at 60ms



Recover time – Active power

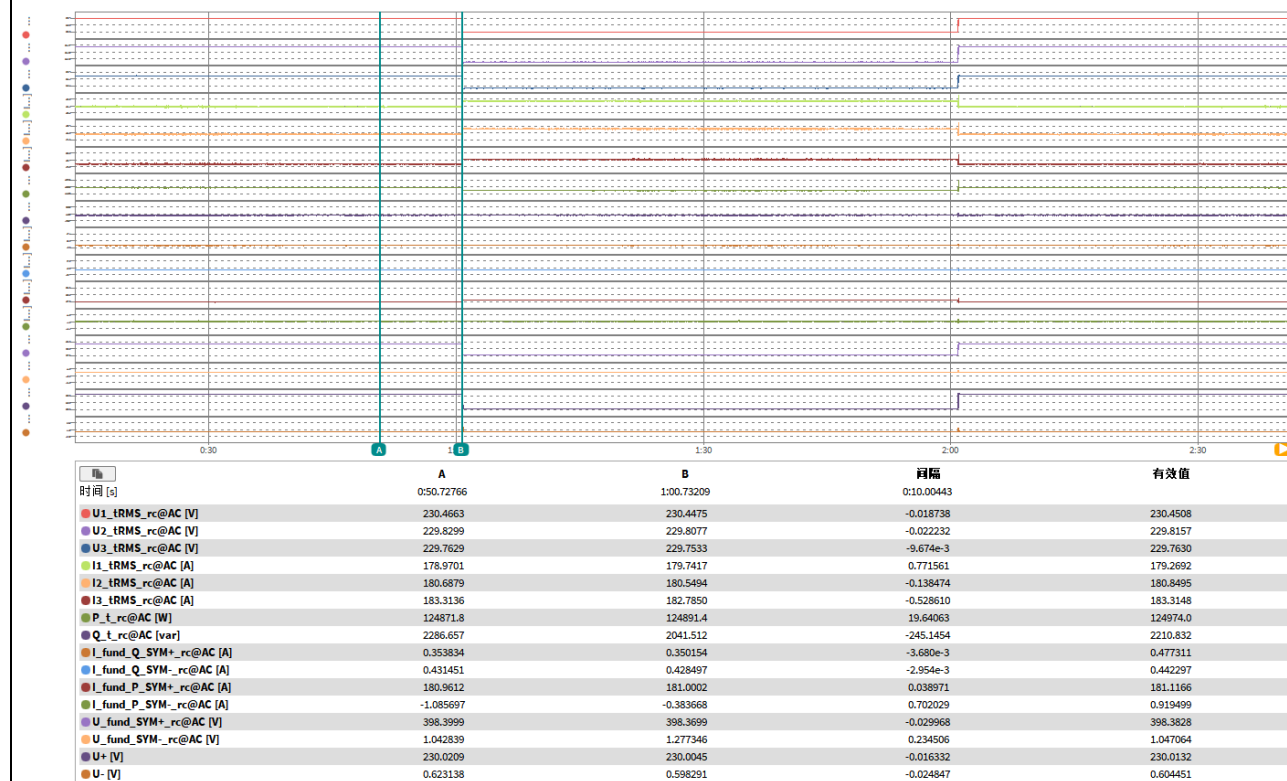


Recover time – Reactive power

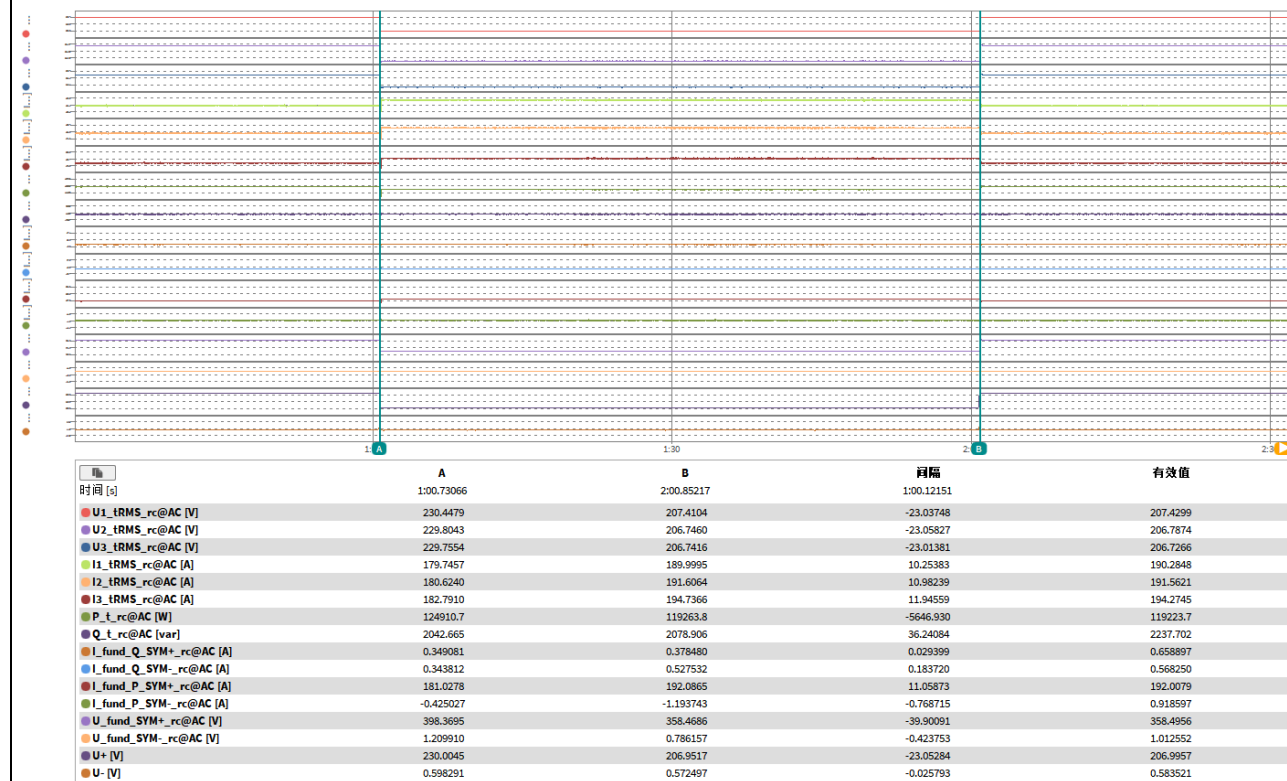


Test – VD5.1

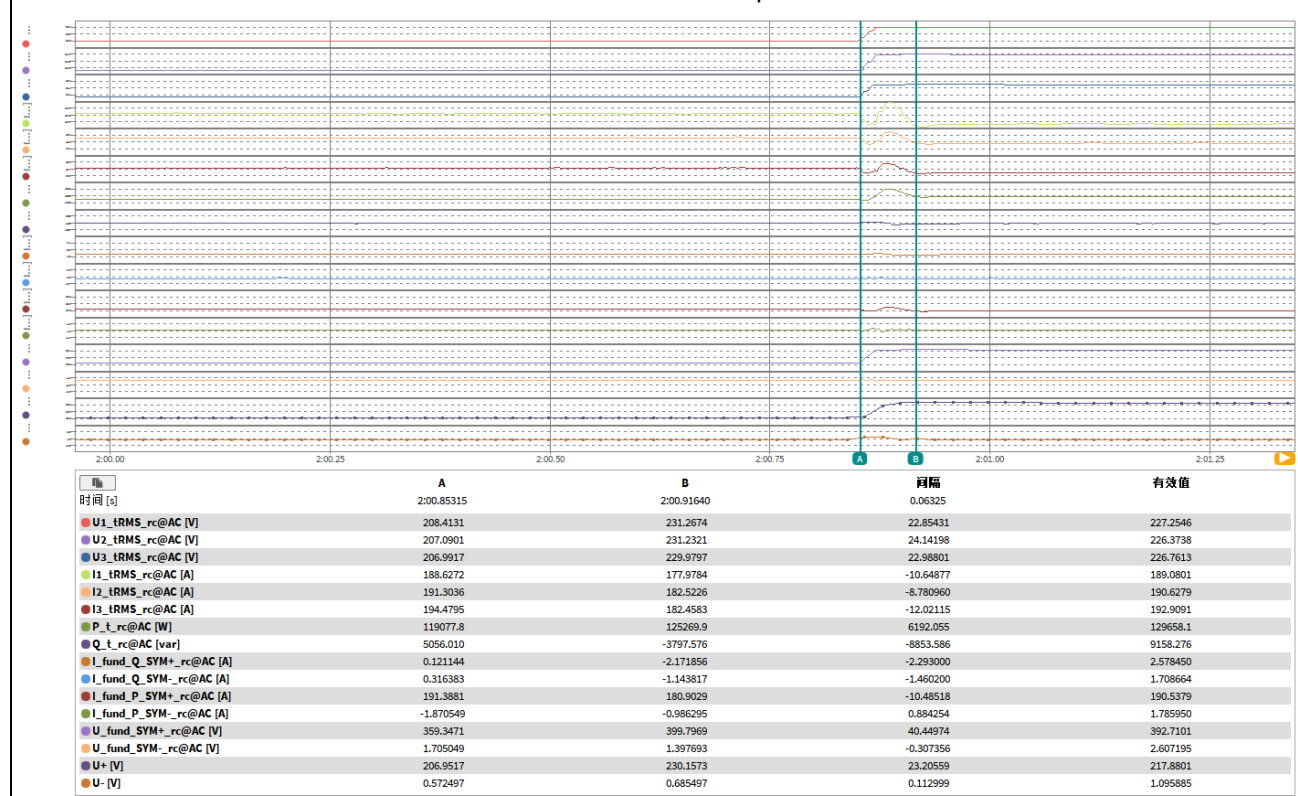
Before fault



Duration fault time

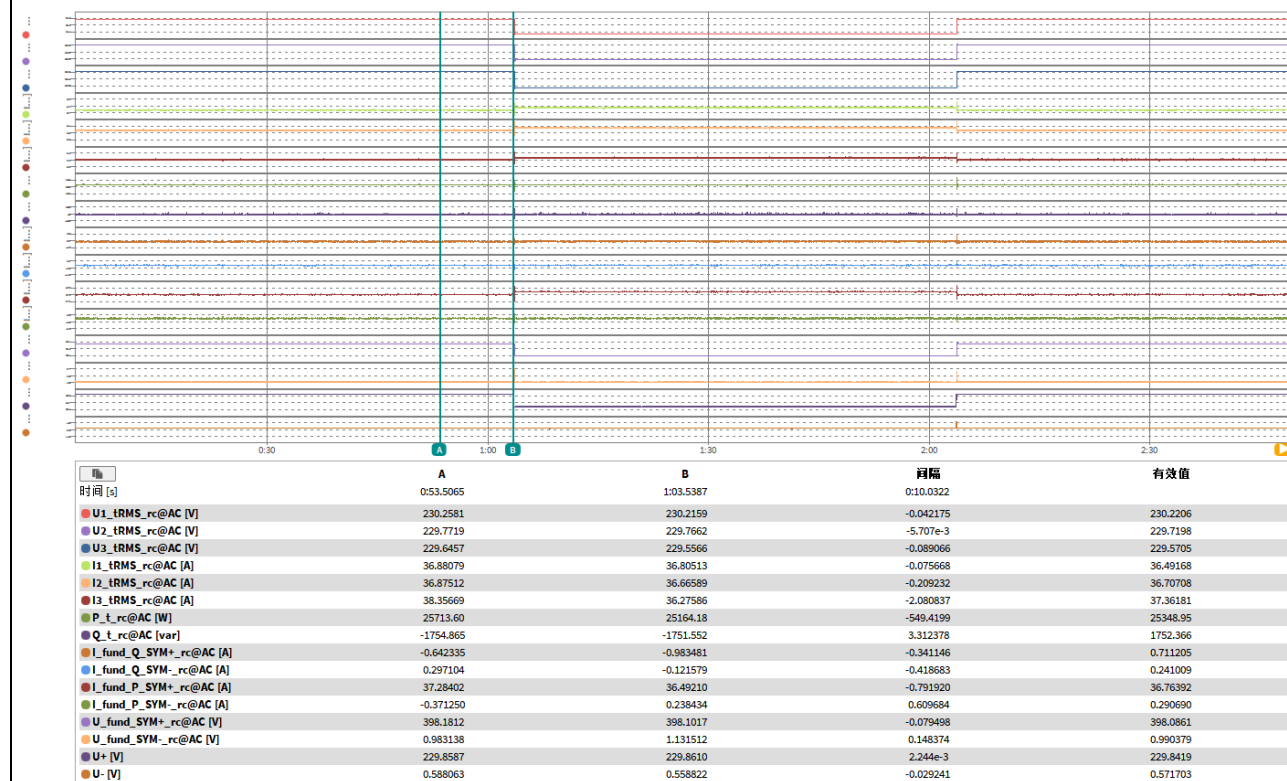


Recover time – Active power

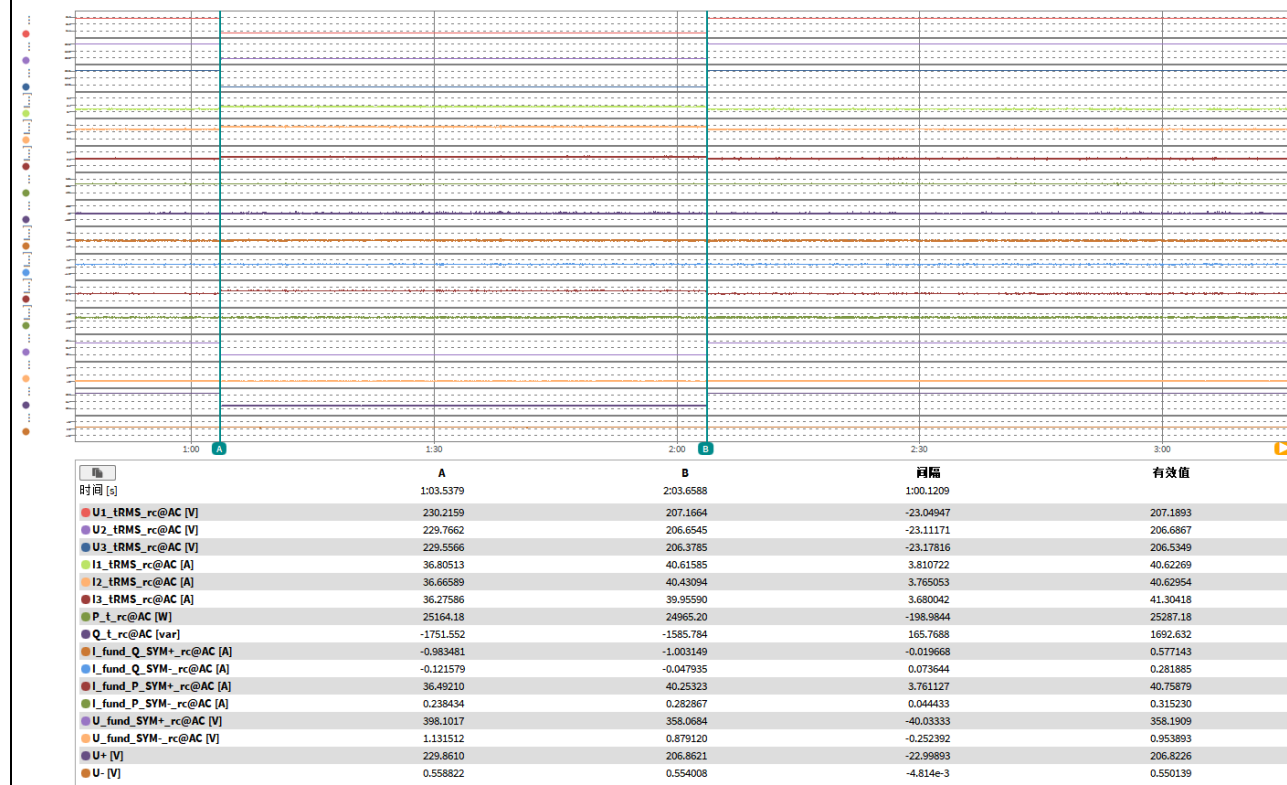


Test – VD5.2

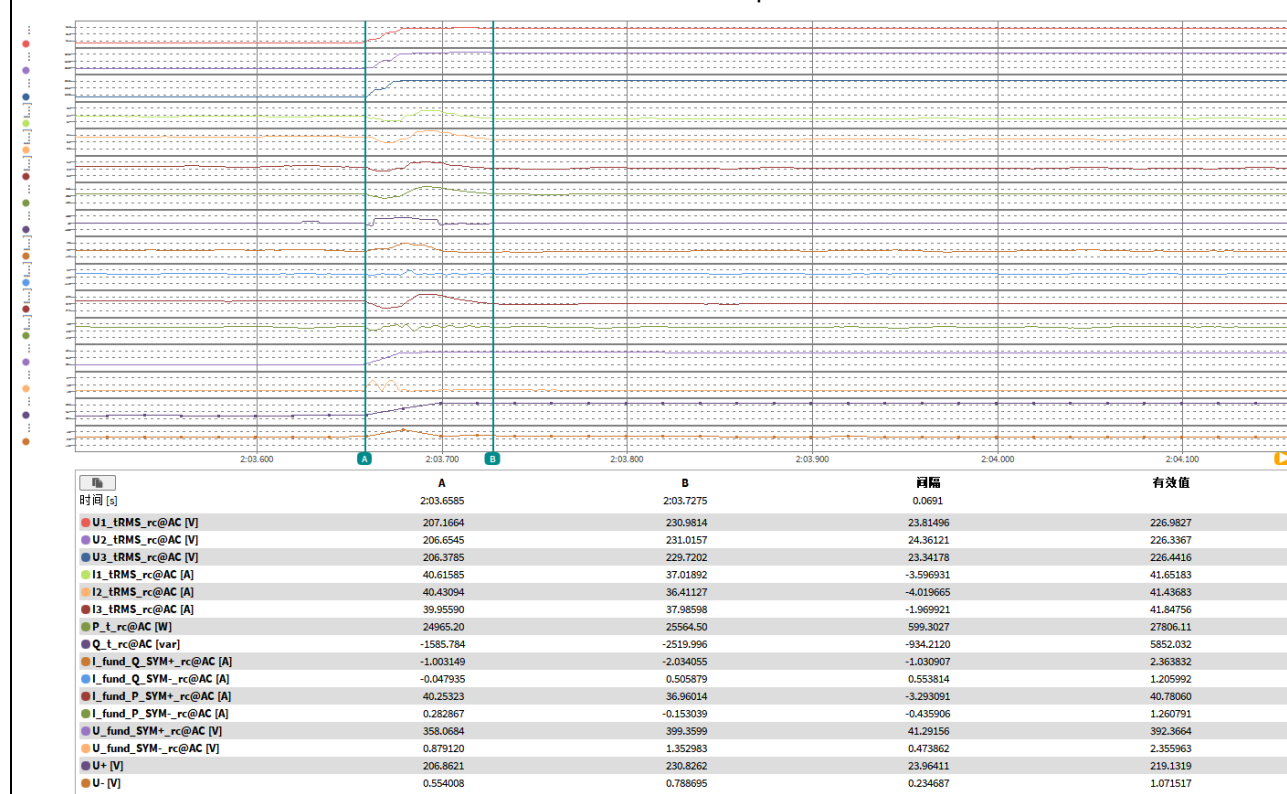
Before fault



Duration fault time

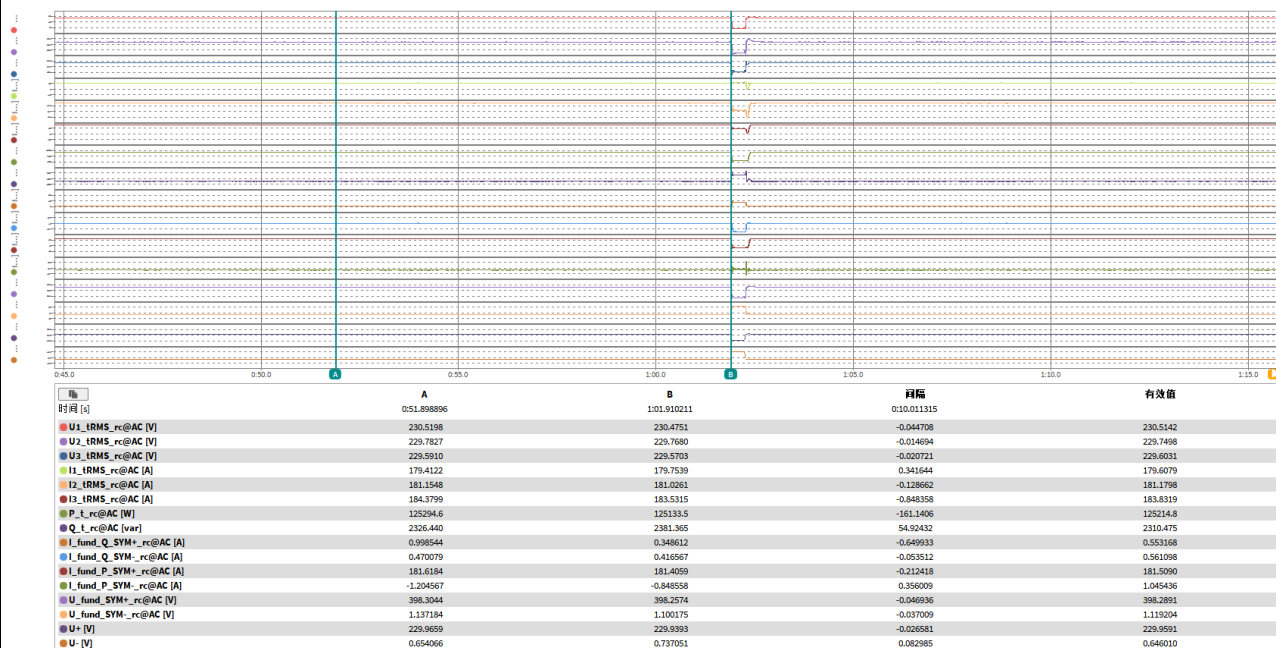


Recover time – Active power

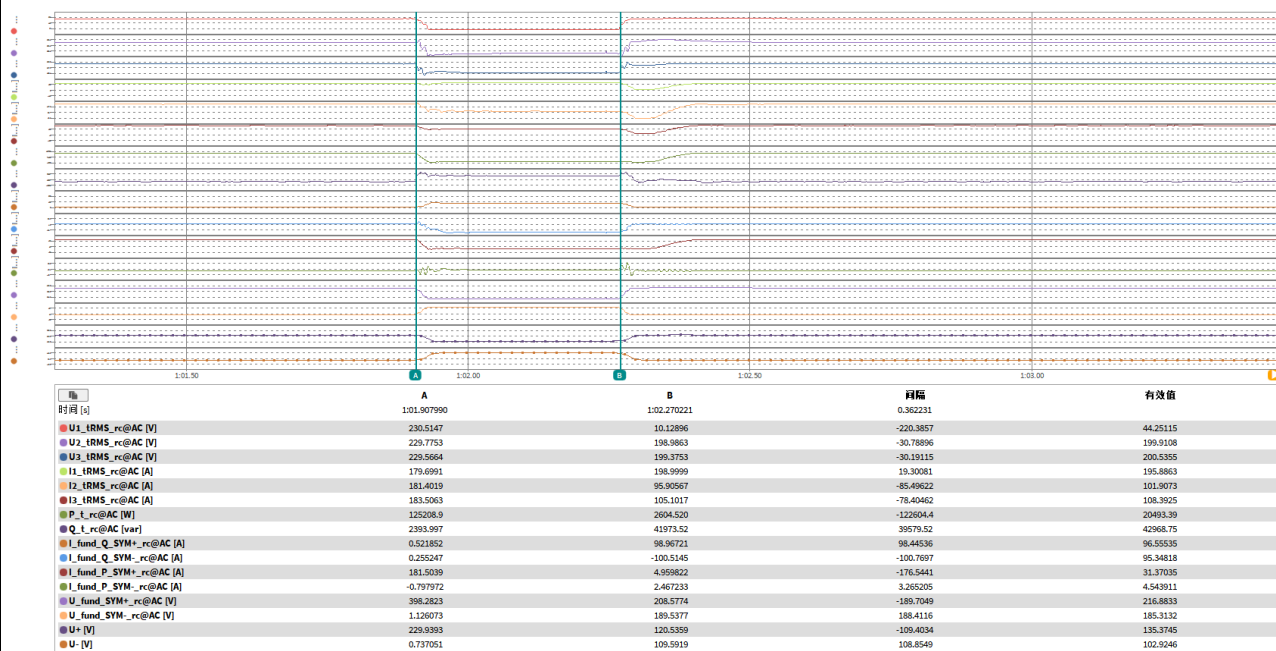


Test – VD6.1

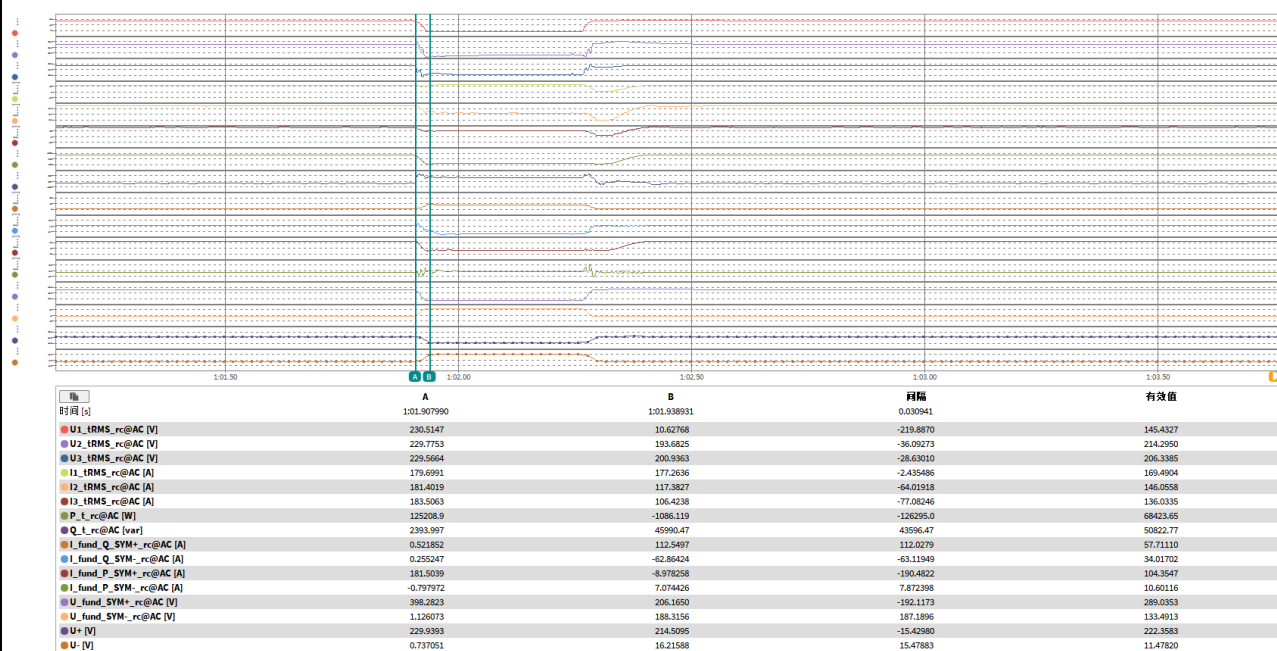
Before fault



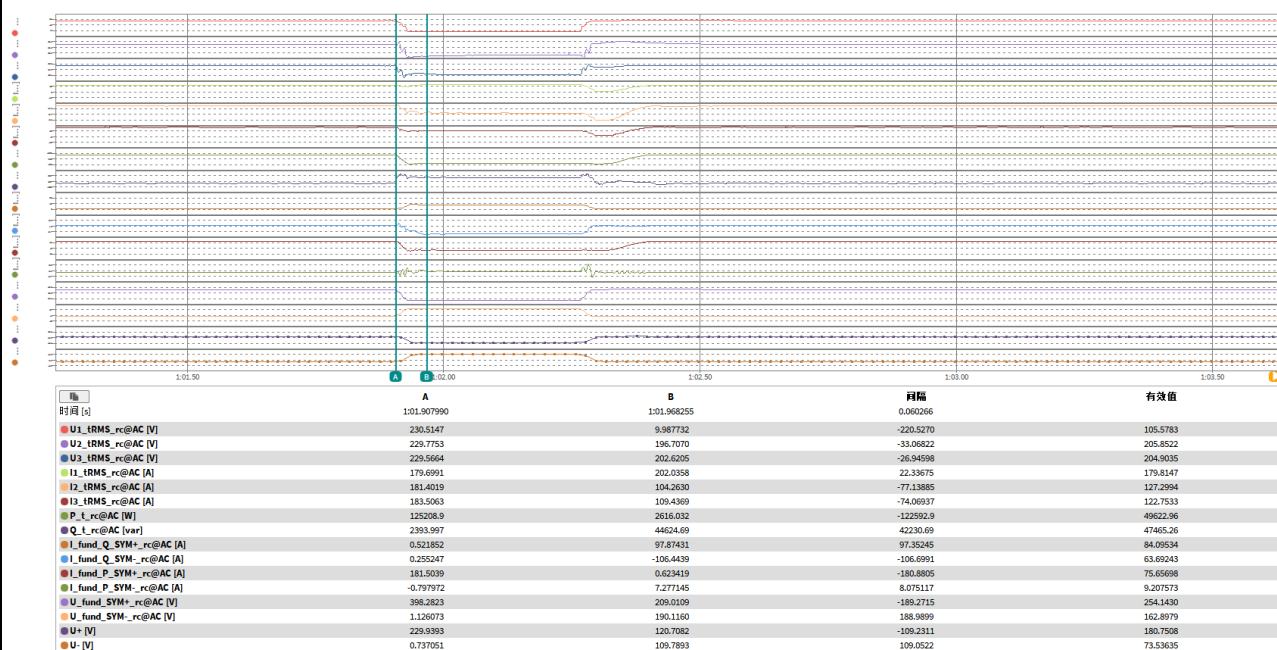
Duration fault time



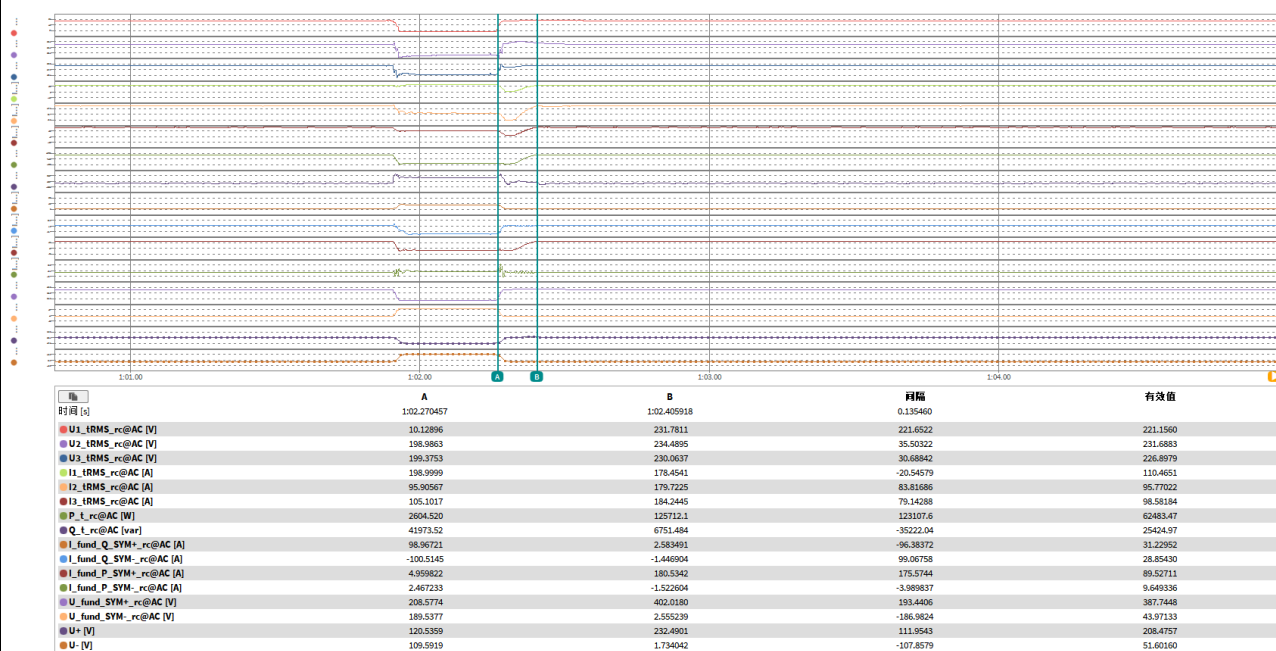
Injected reactive current at 30ms



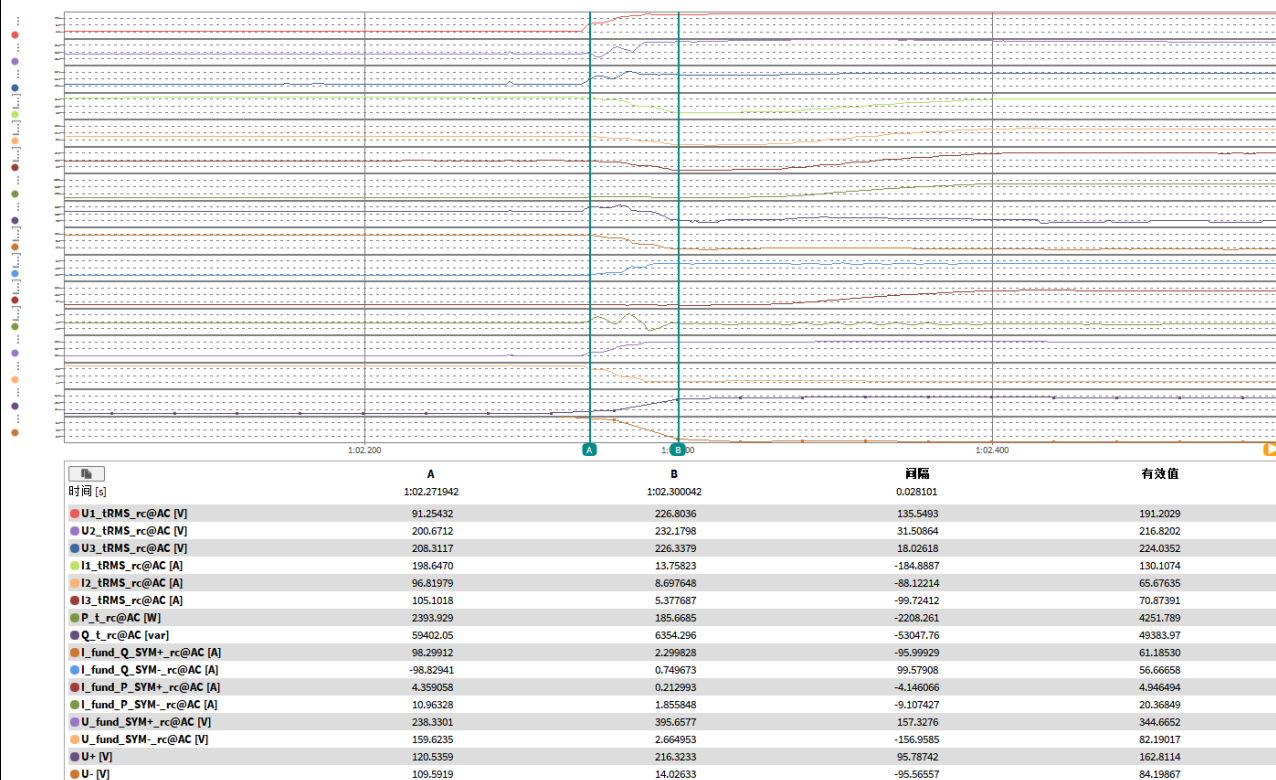
Injected reactive current at 60ms



Recover time – Active power

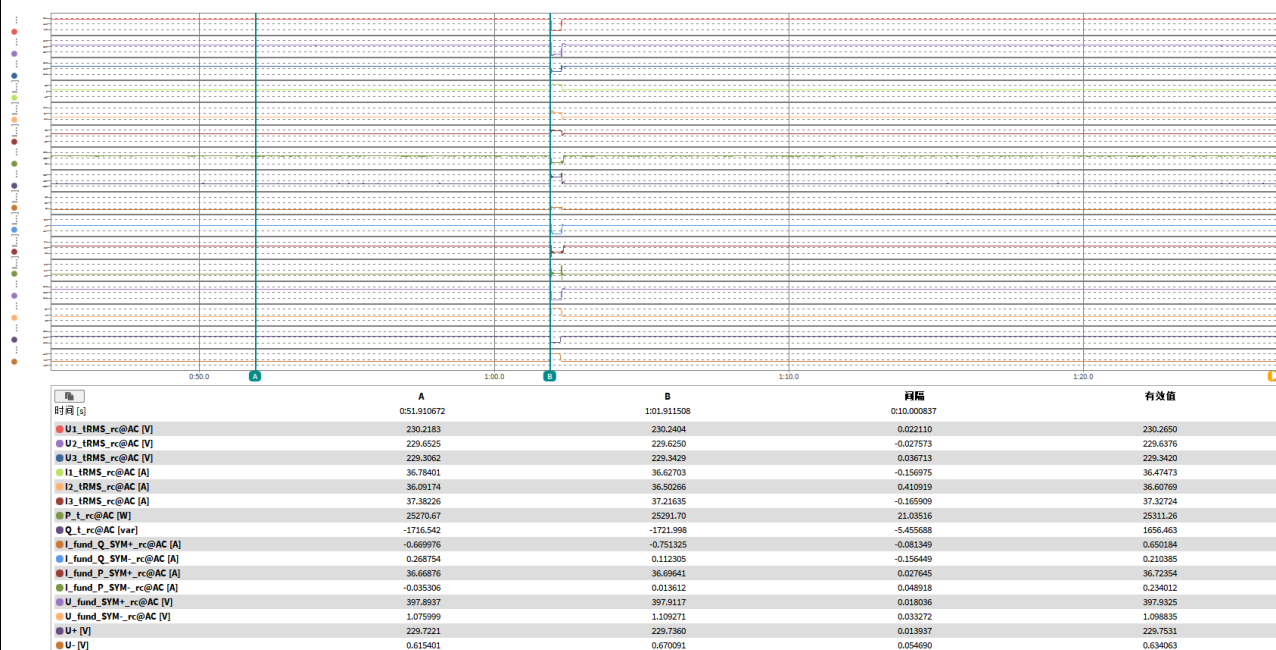


Recover time – Reactive power

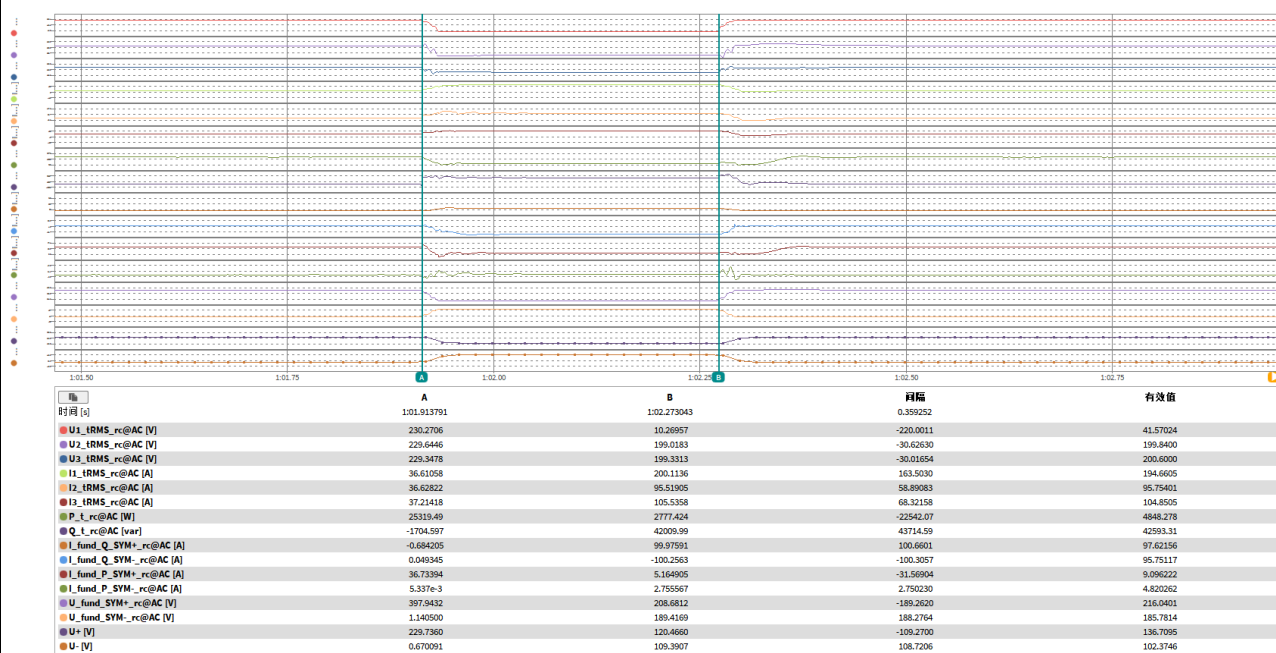


Test – VD6.2

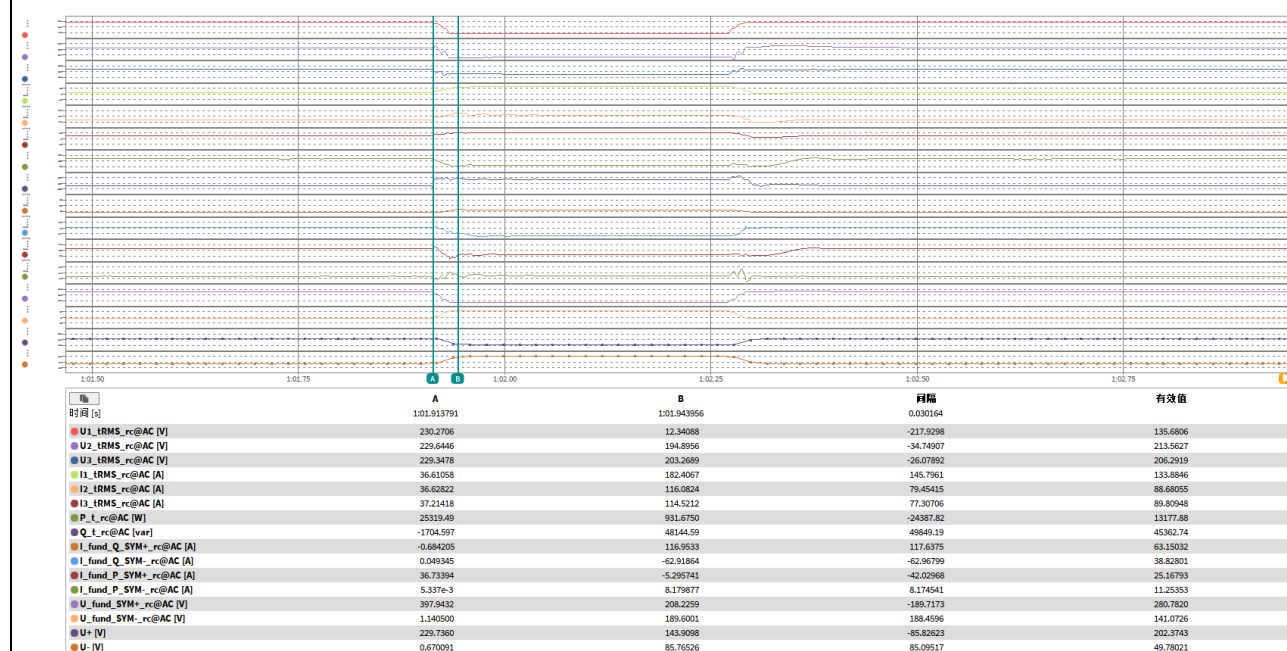
Before fault



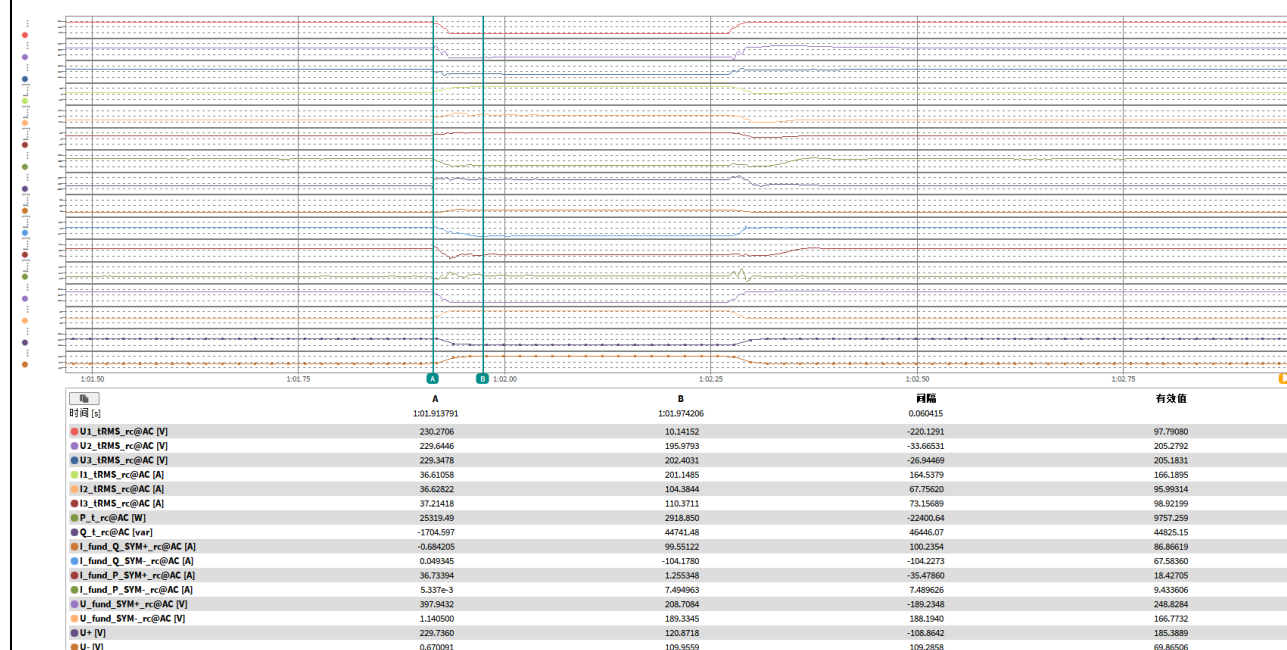
Duration fault time



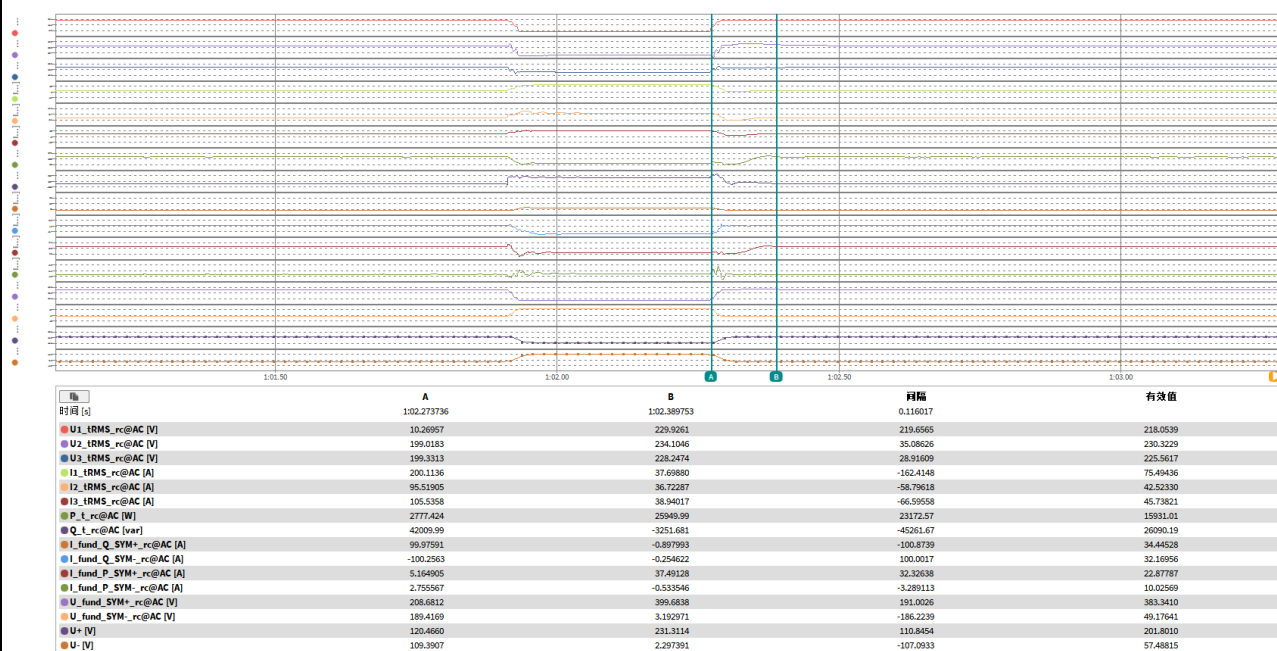
Injected reactive current at 30ms



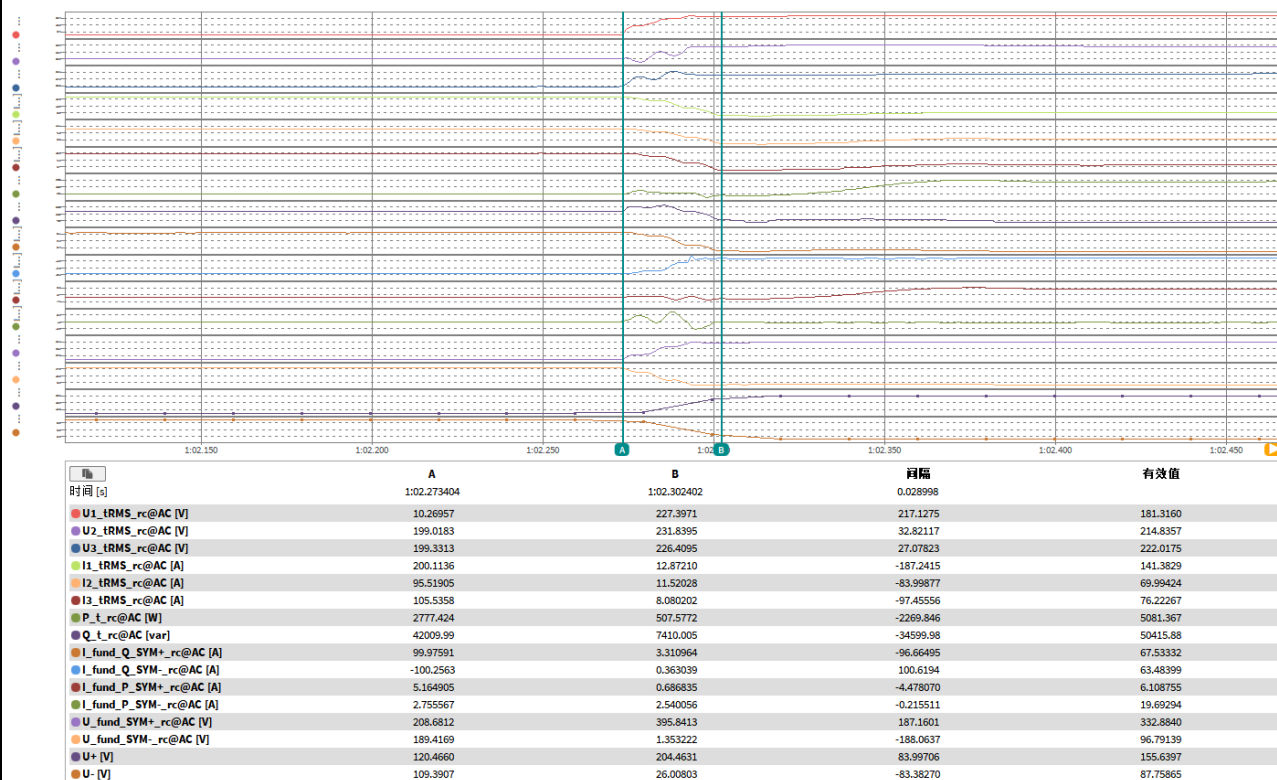
Injected reactive current at 60ms



Recover time – Active power

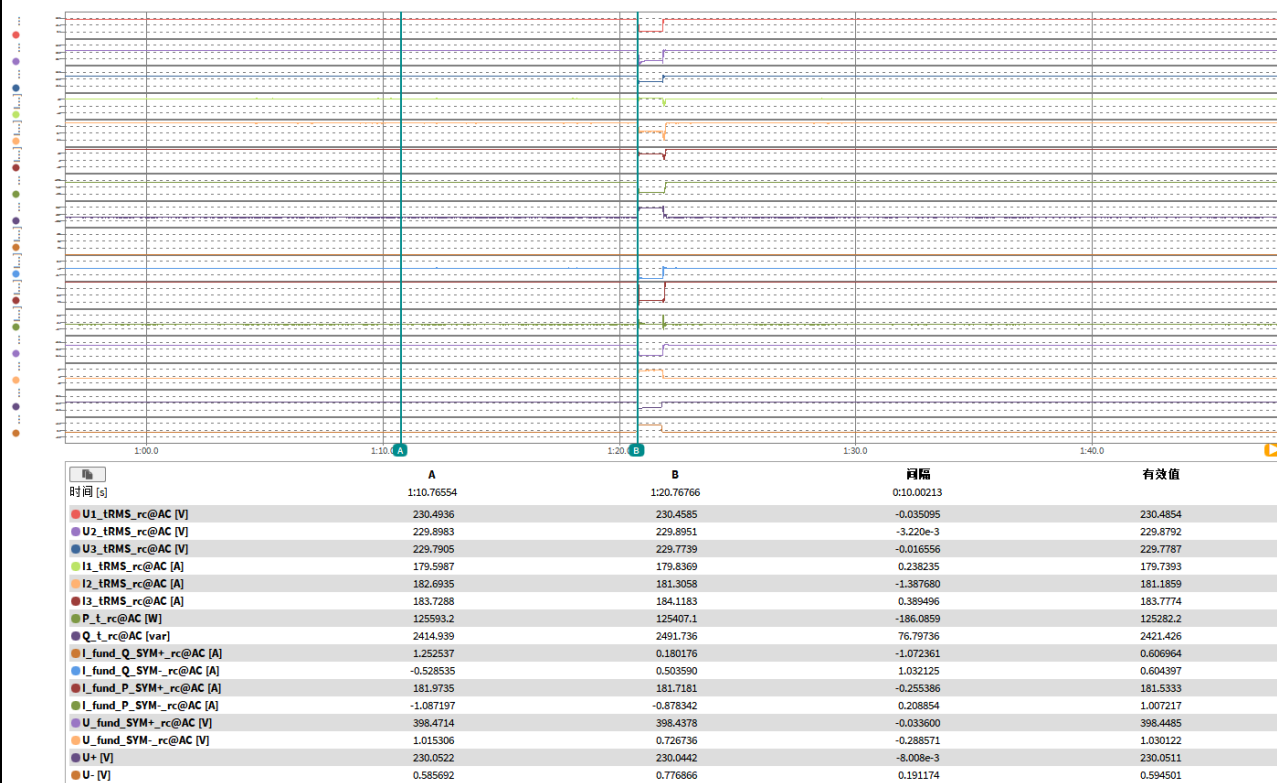


Recover time – Reactive power

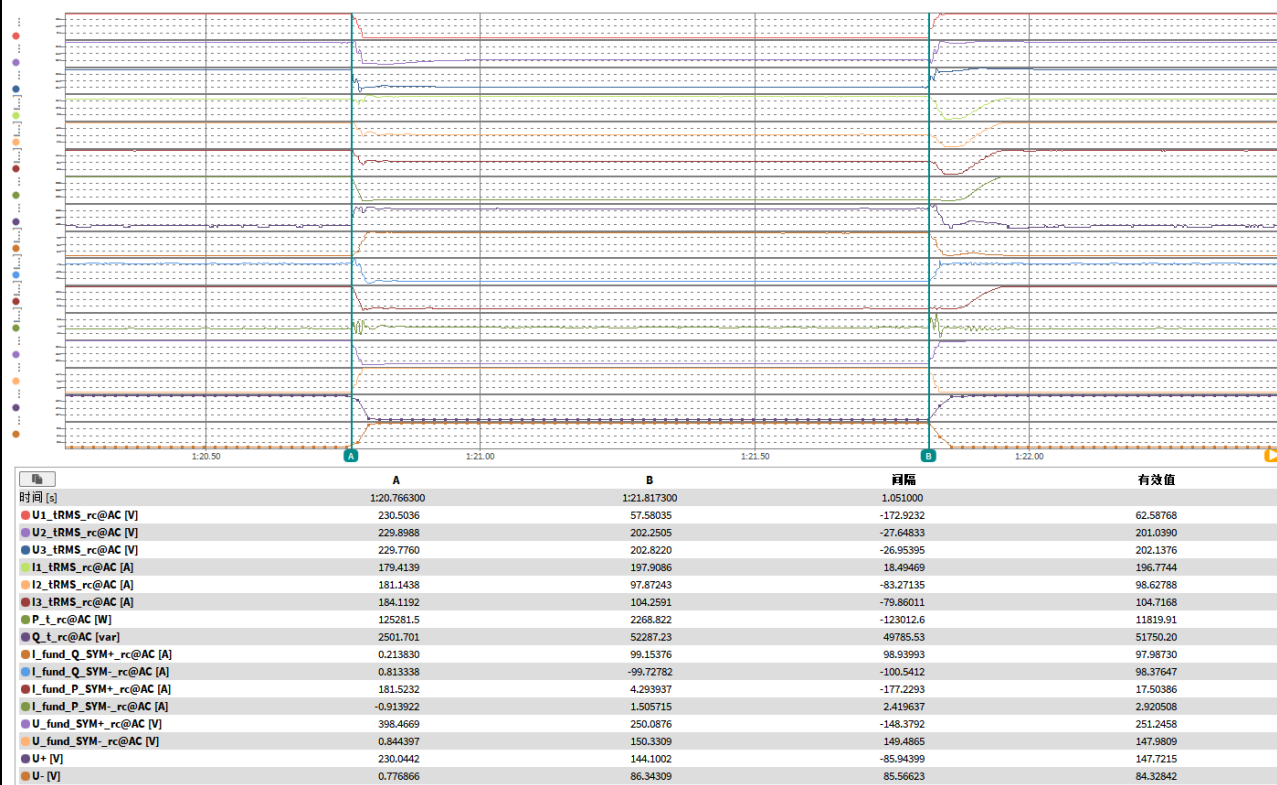


Test – VD7.1

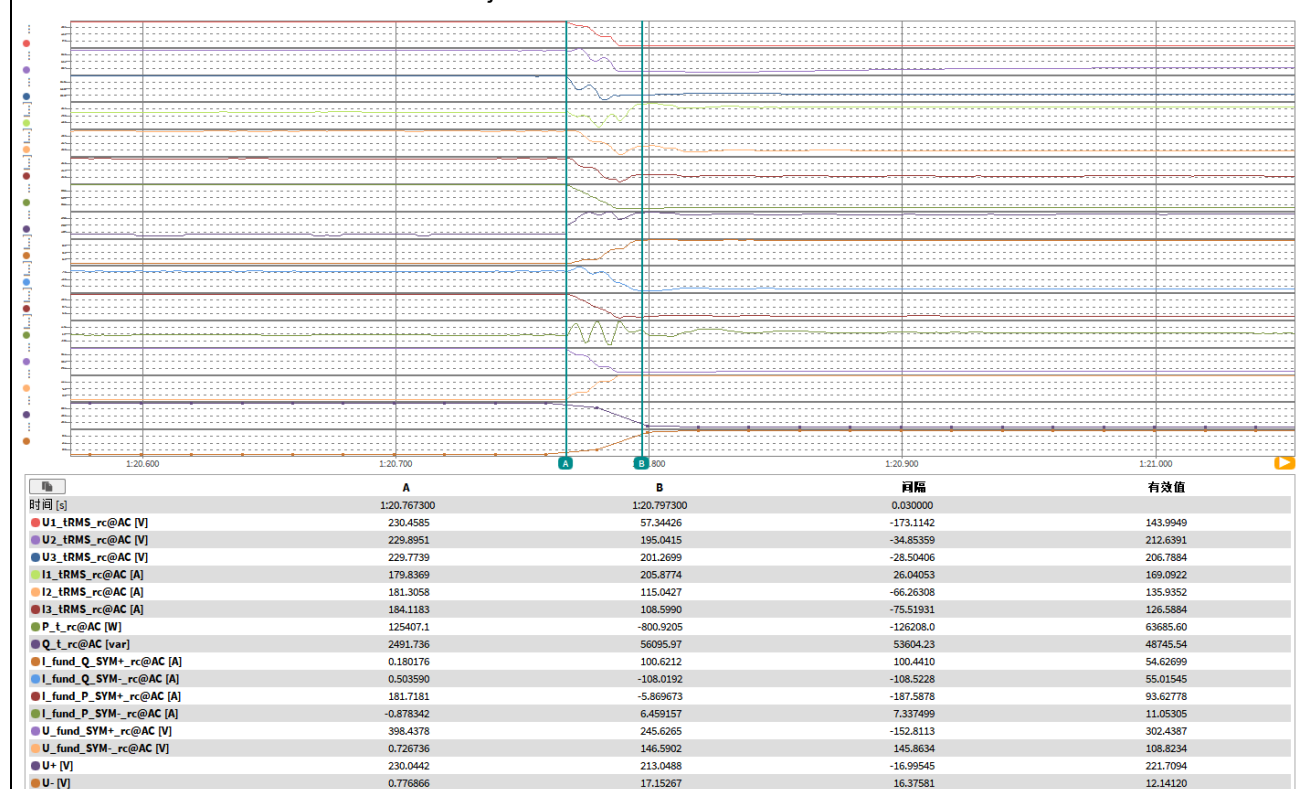
Before fault



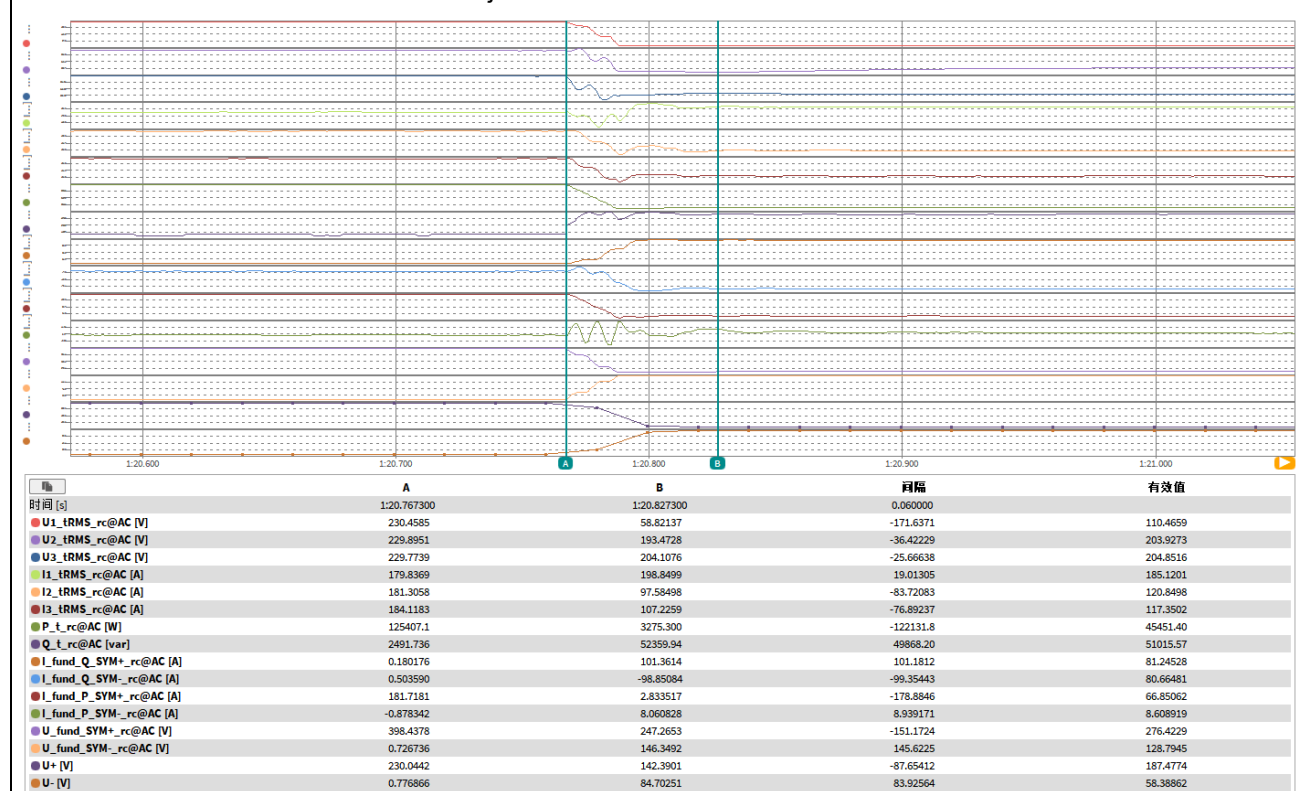
Duration fault time



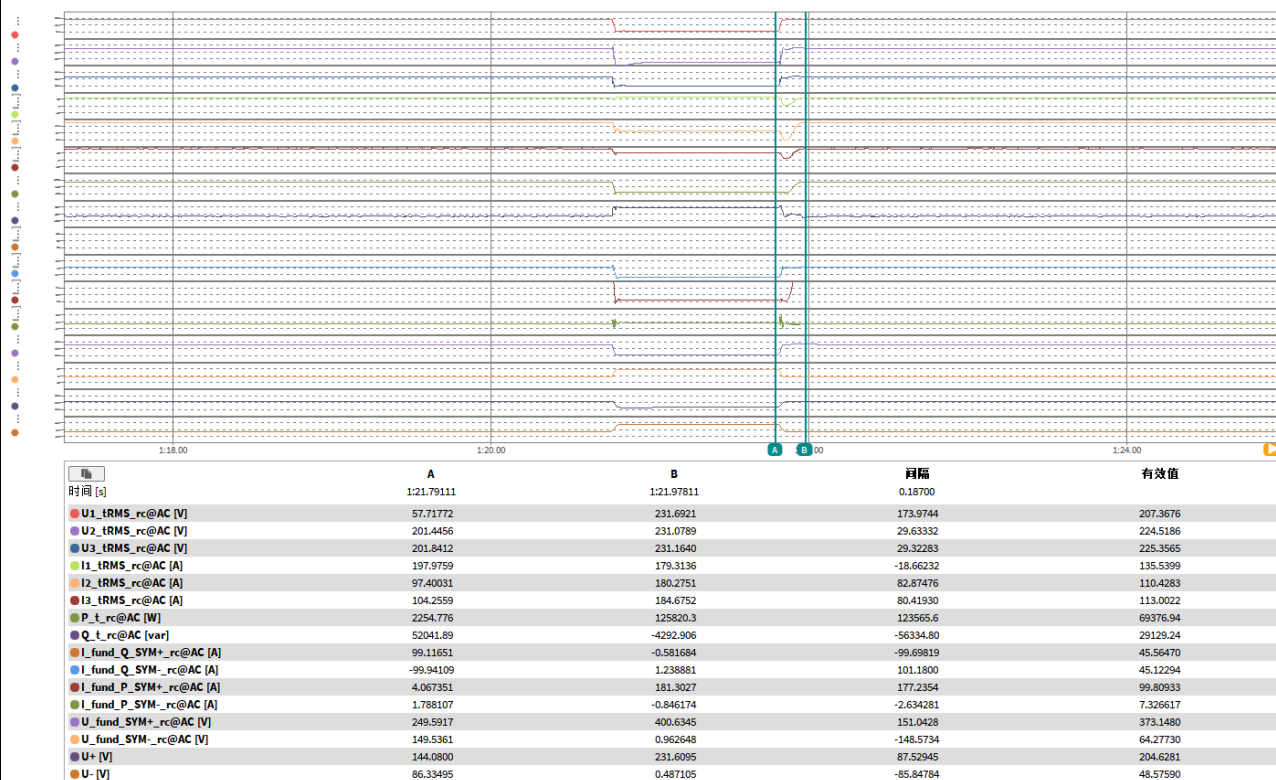
Injected reactive current at 30ms



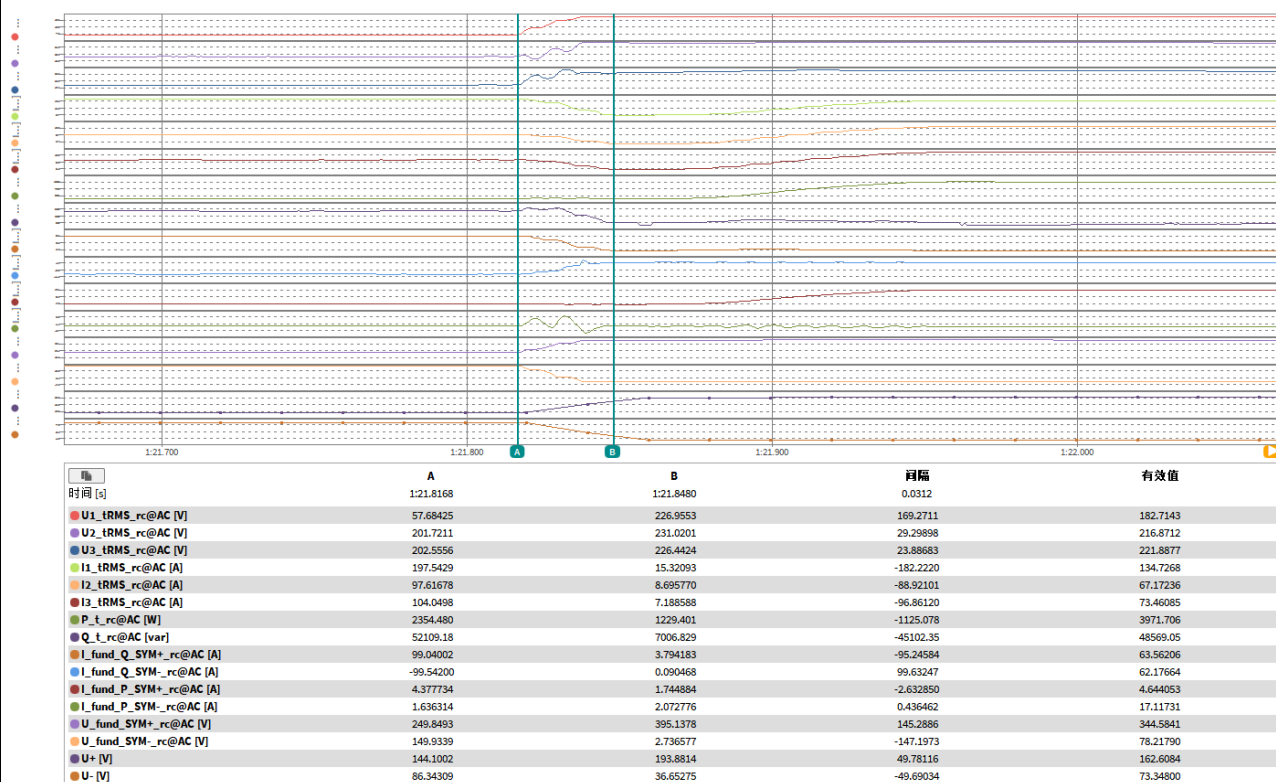
Injected reactive current at 60ms



Recover time – Active power

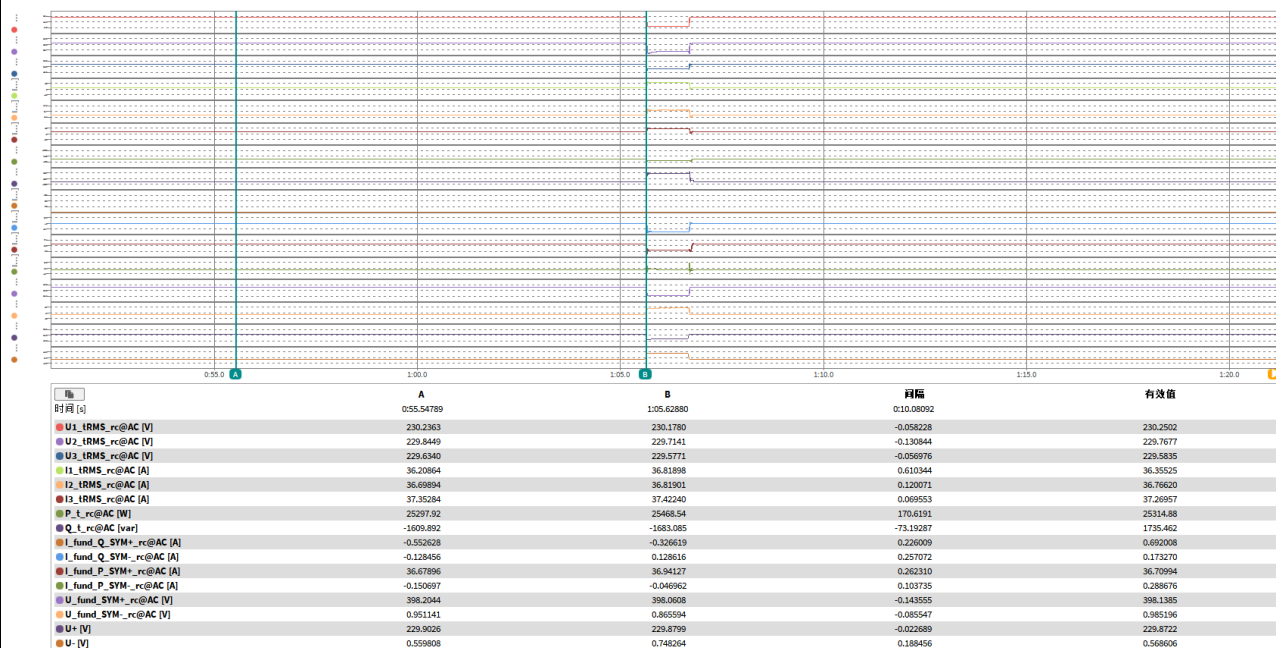


Recover time – Reactive power

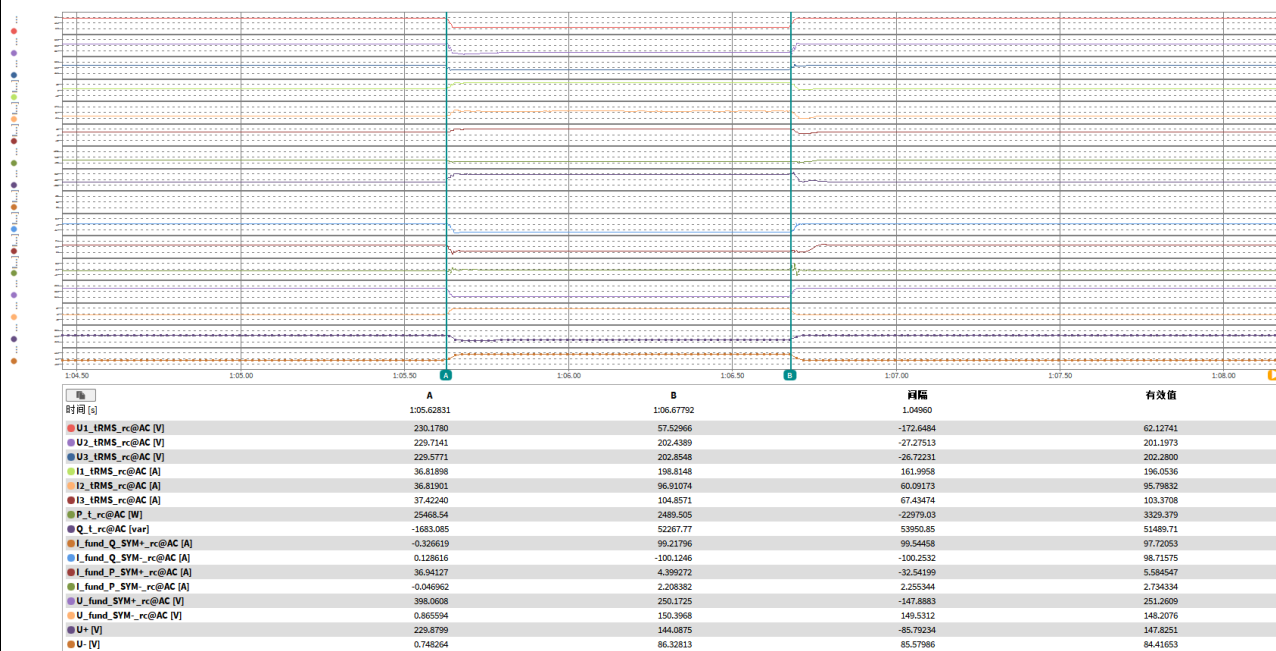


Test – VD7.2

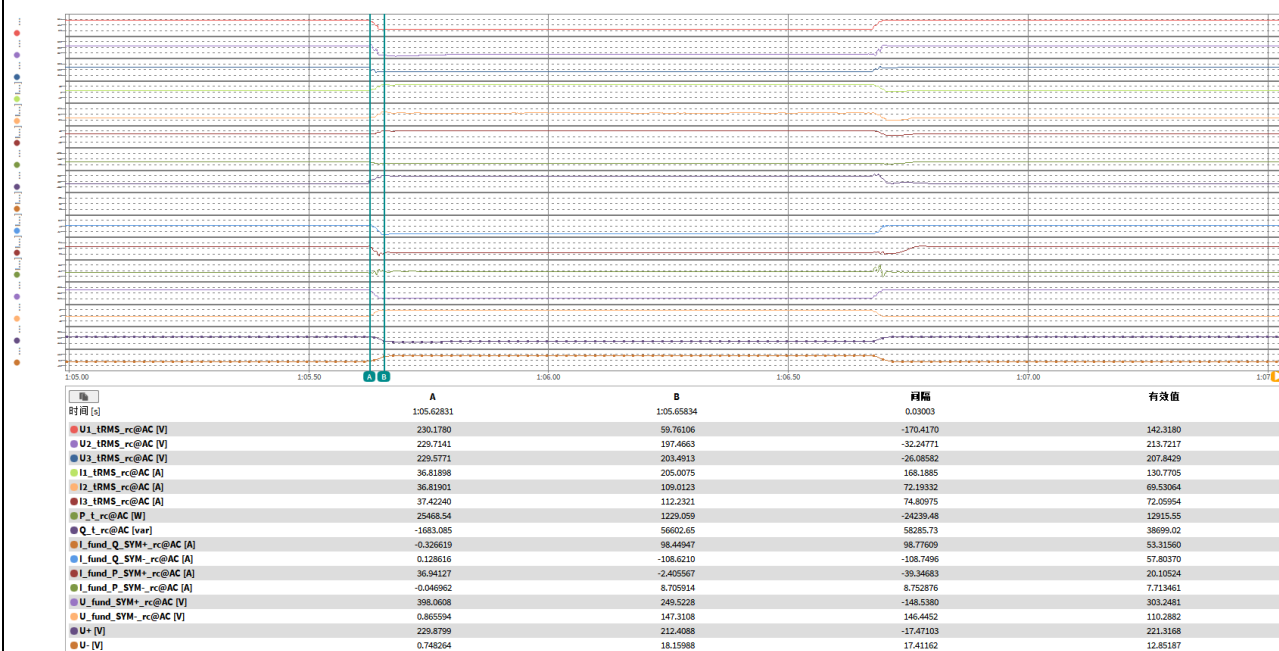
Before fault



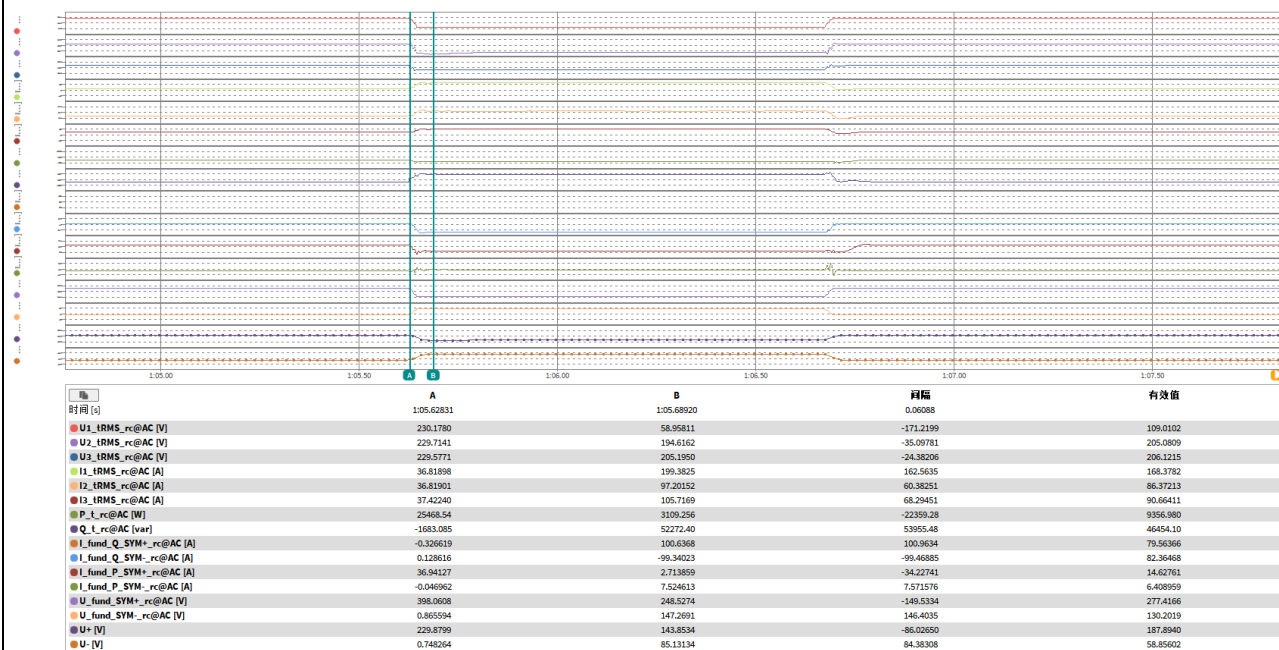
Duration fault time



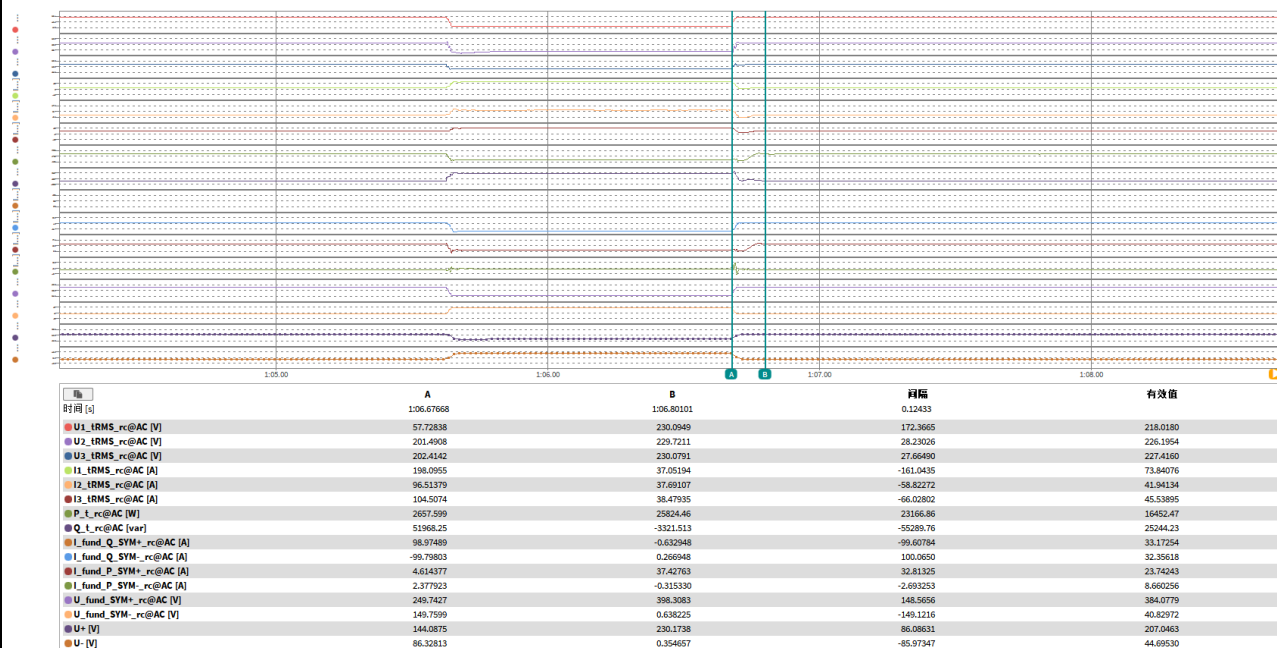
Injected reactive current at 30ms



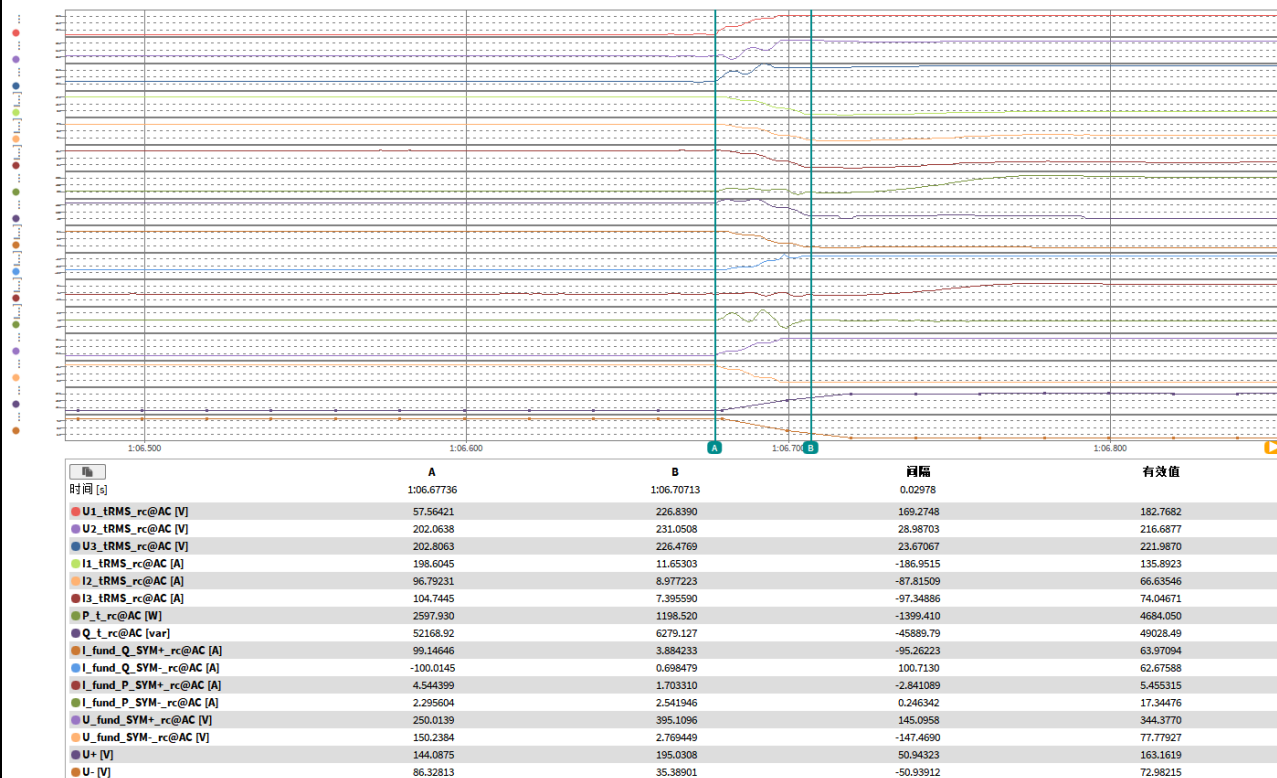
Injected reactive current at 60ms



Recover time – Active power

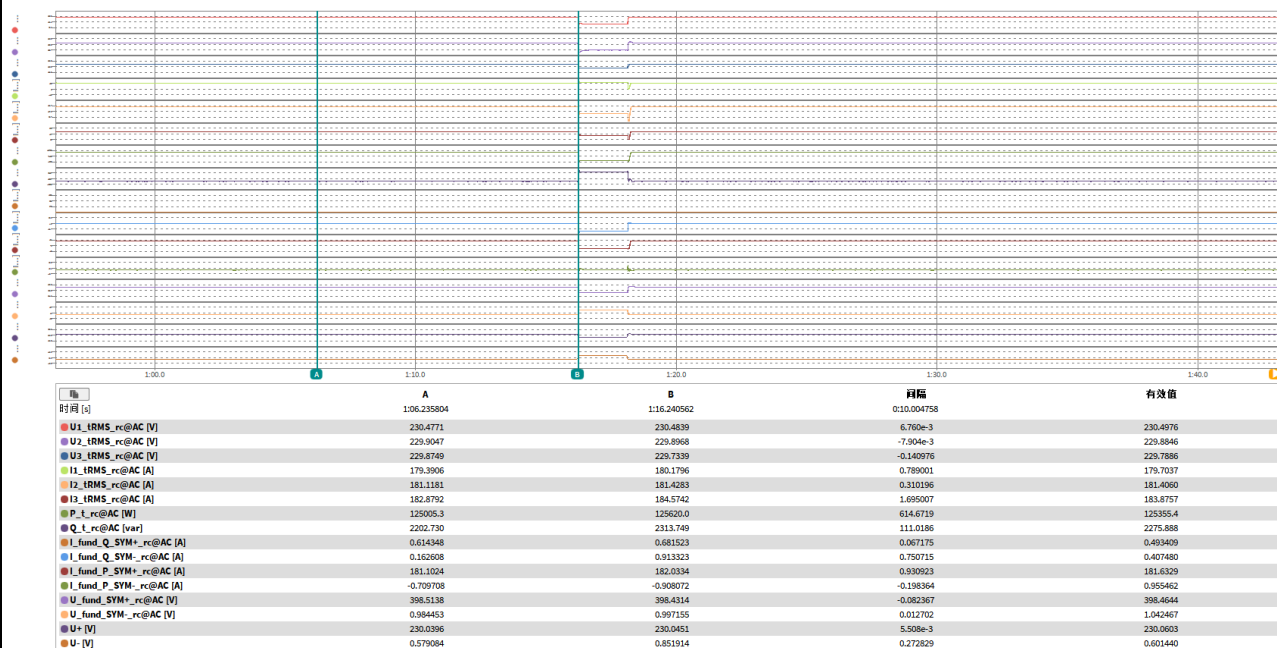


Recover time – Reactive power

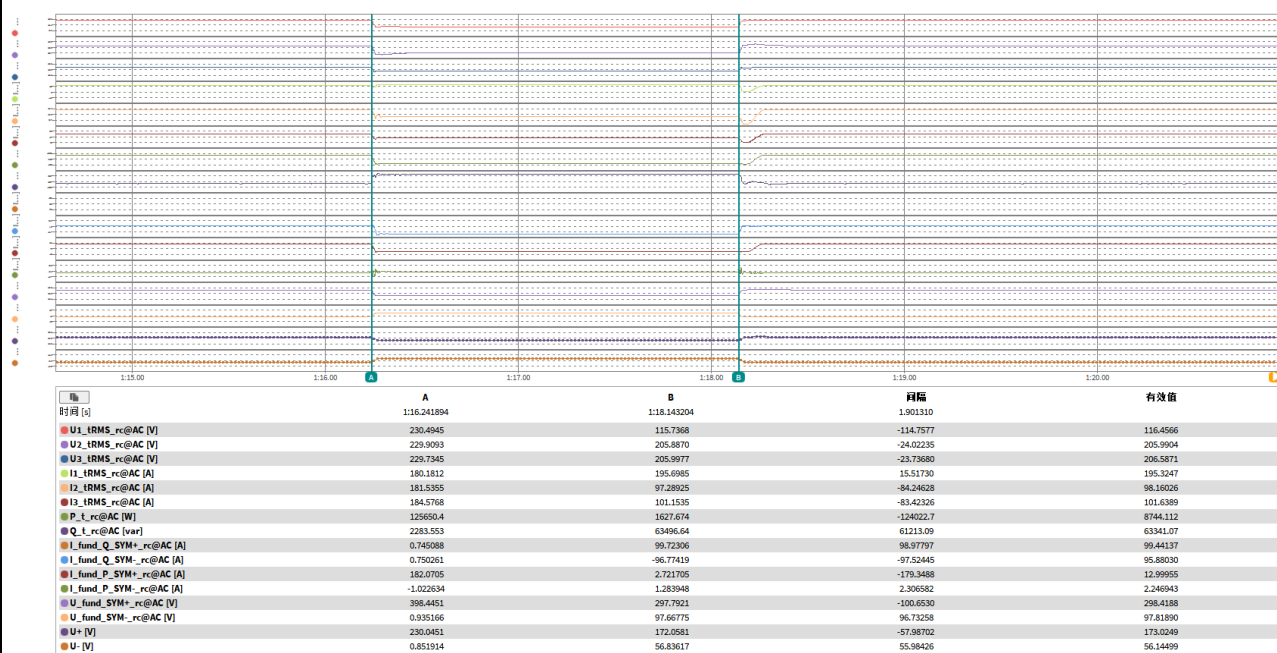


Test – VD8.1

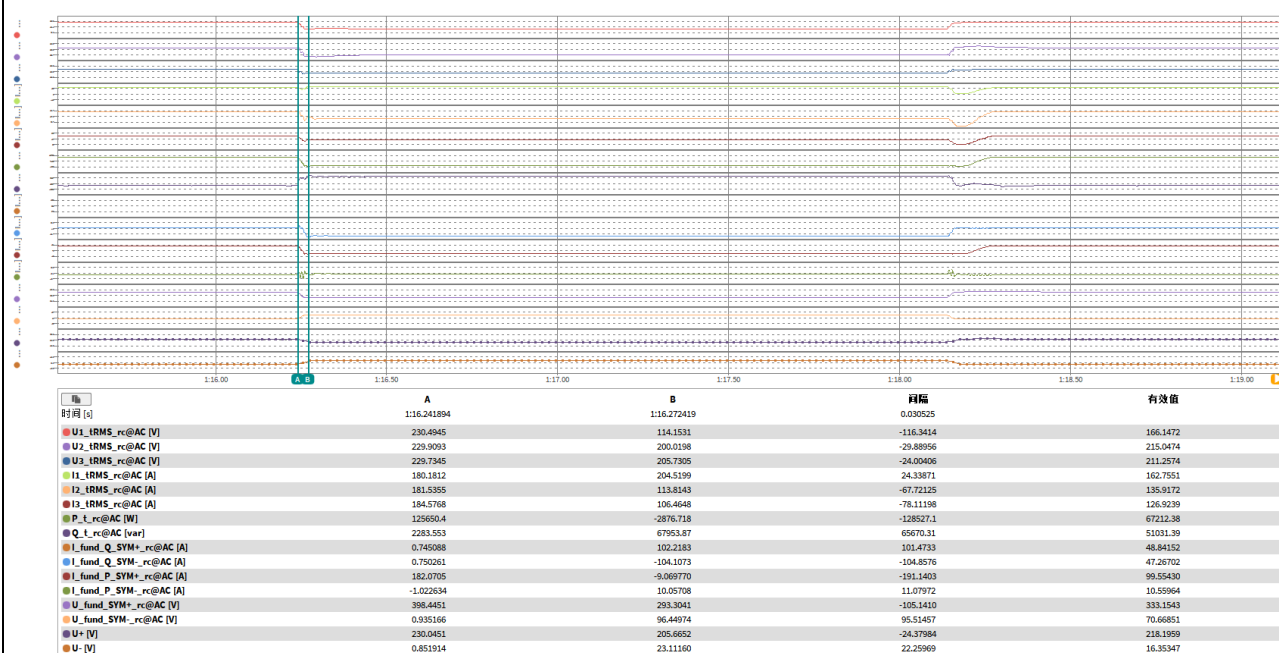
Before fault



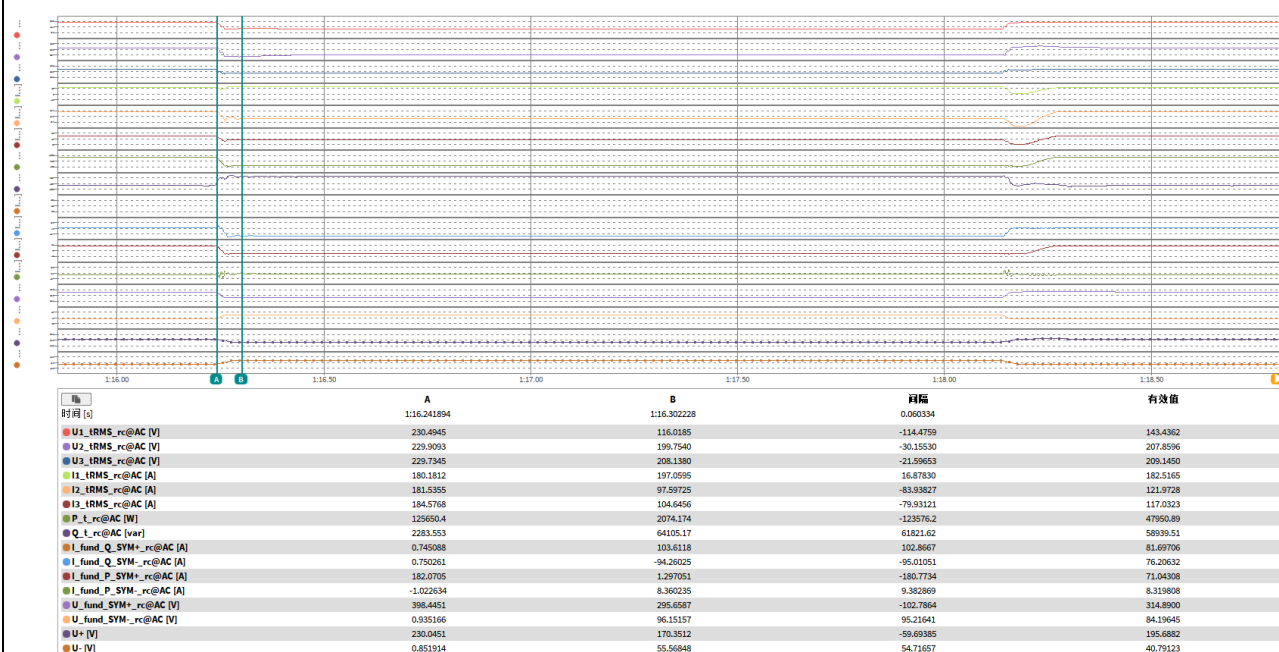
Duration fault time



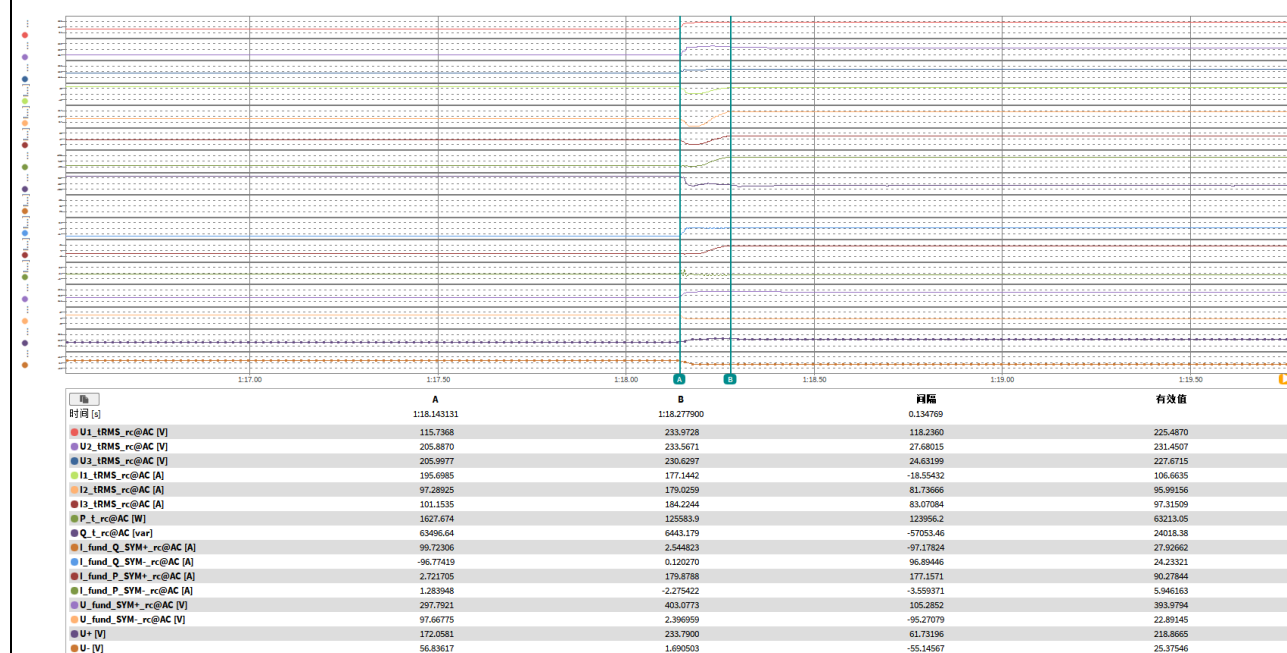
Injected reactive current at 30ms



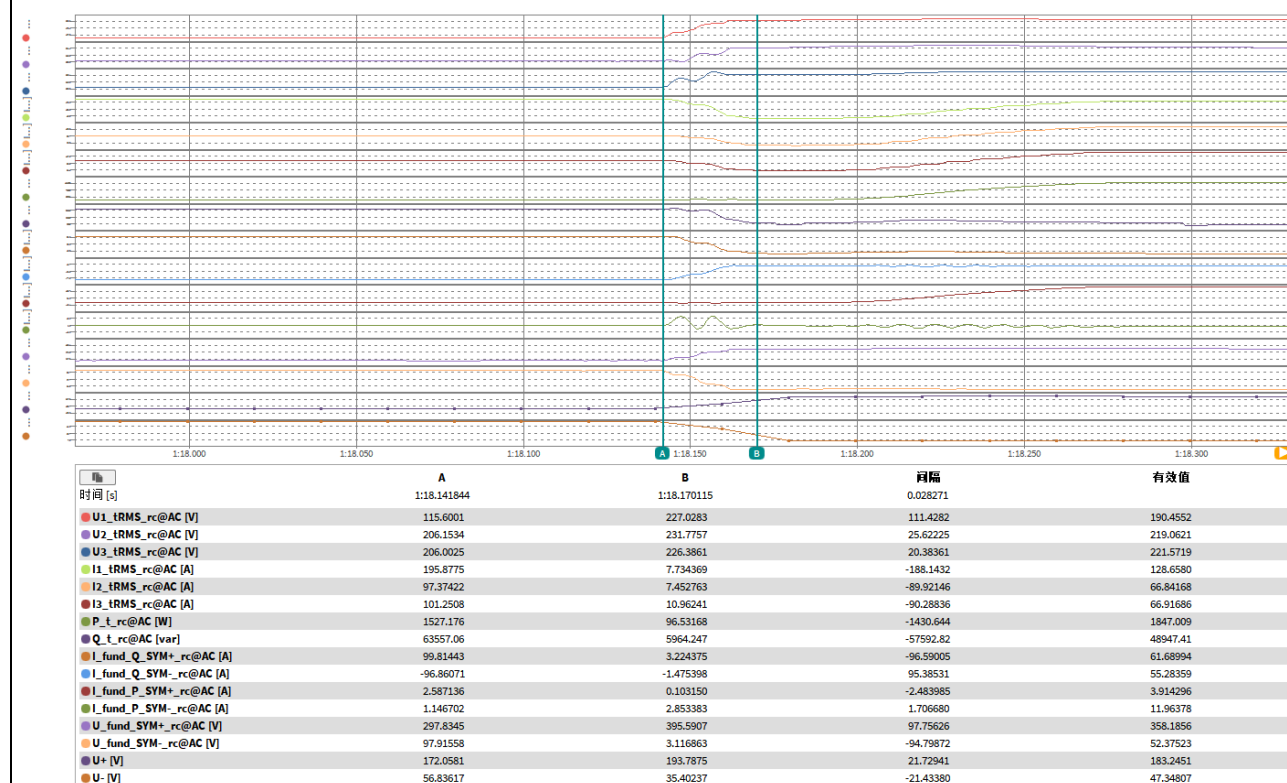
Injected reactive current at 60ms



Recover time – Active power

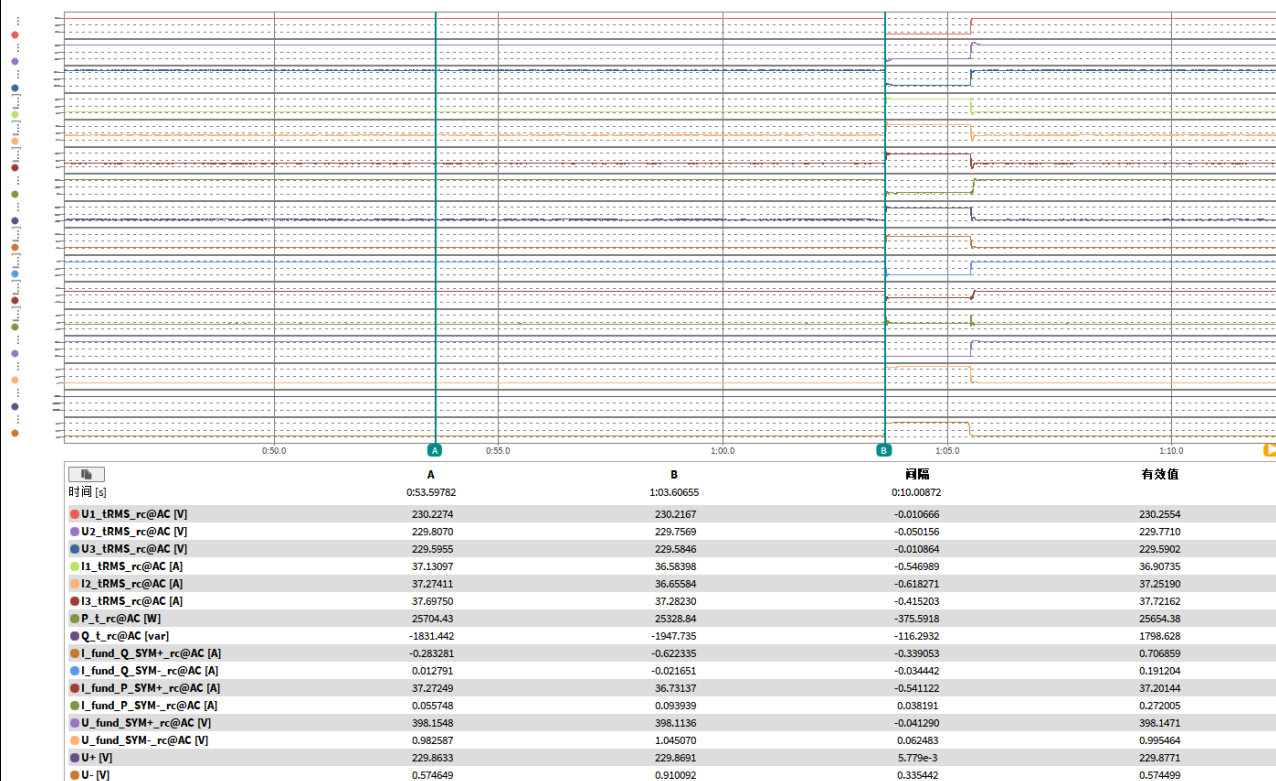


Recover time – Reactive power



Test – VD8.2

Before fault



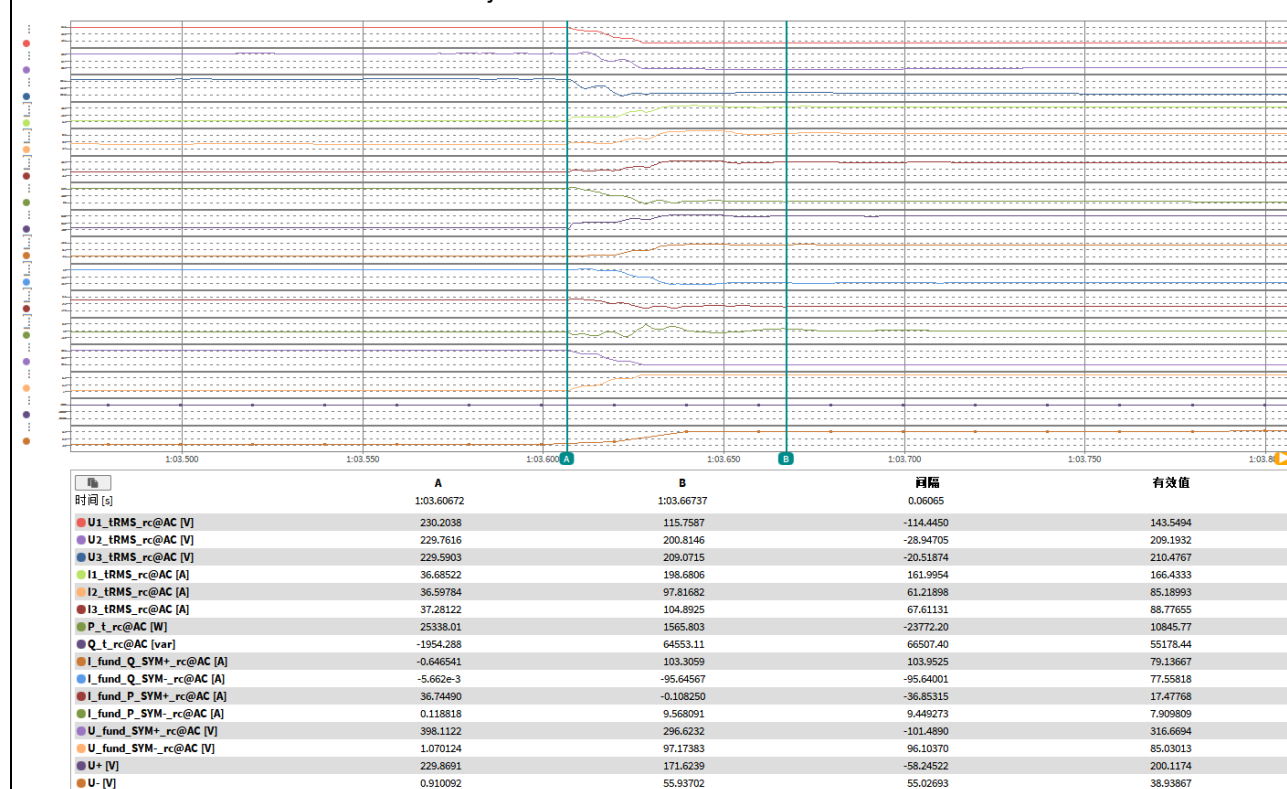
Duration fault time



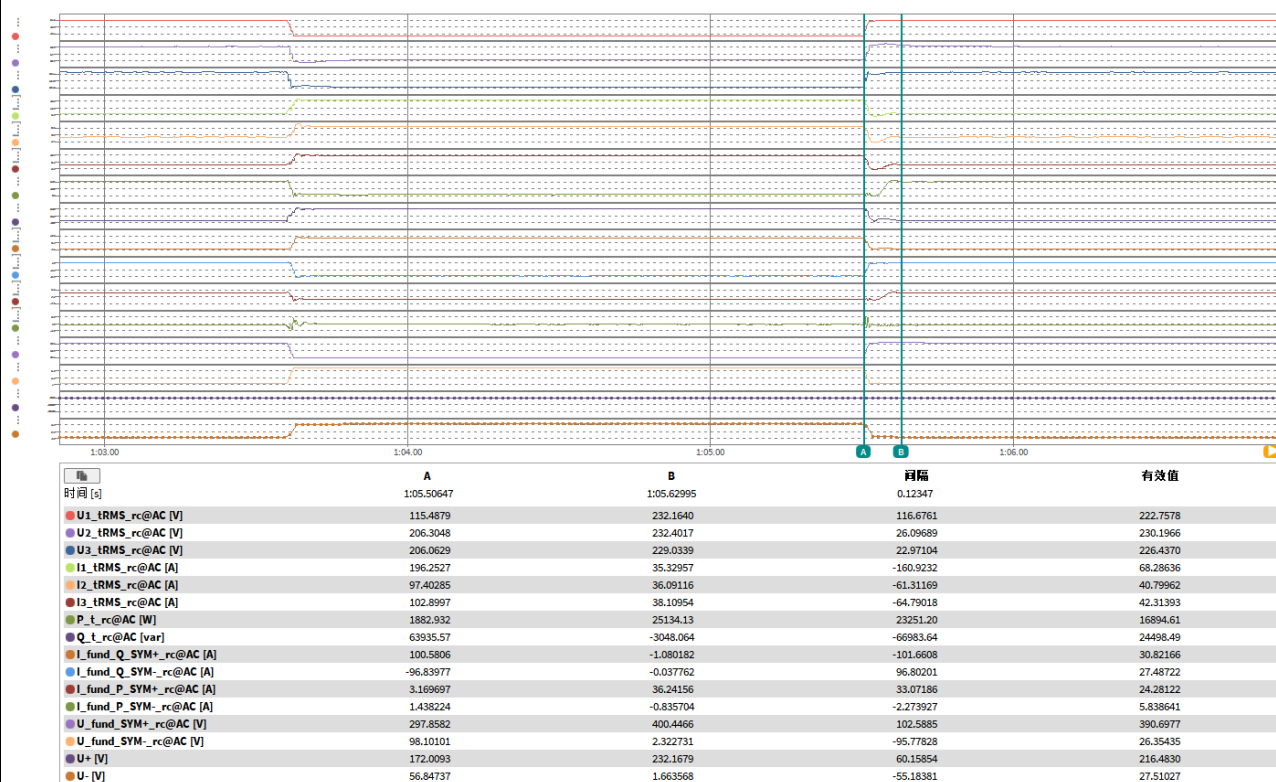
Injected reactive current at 30ms



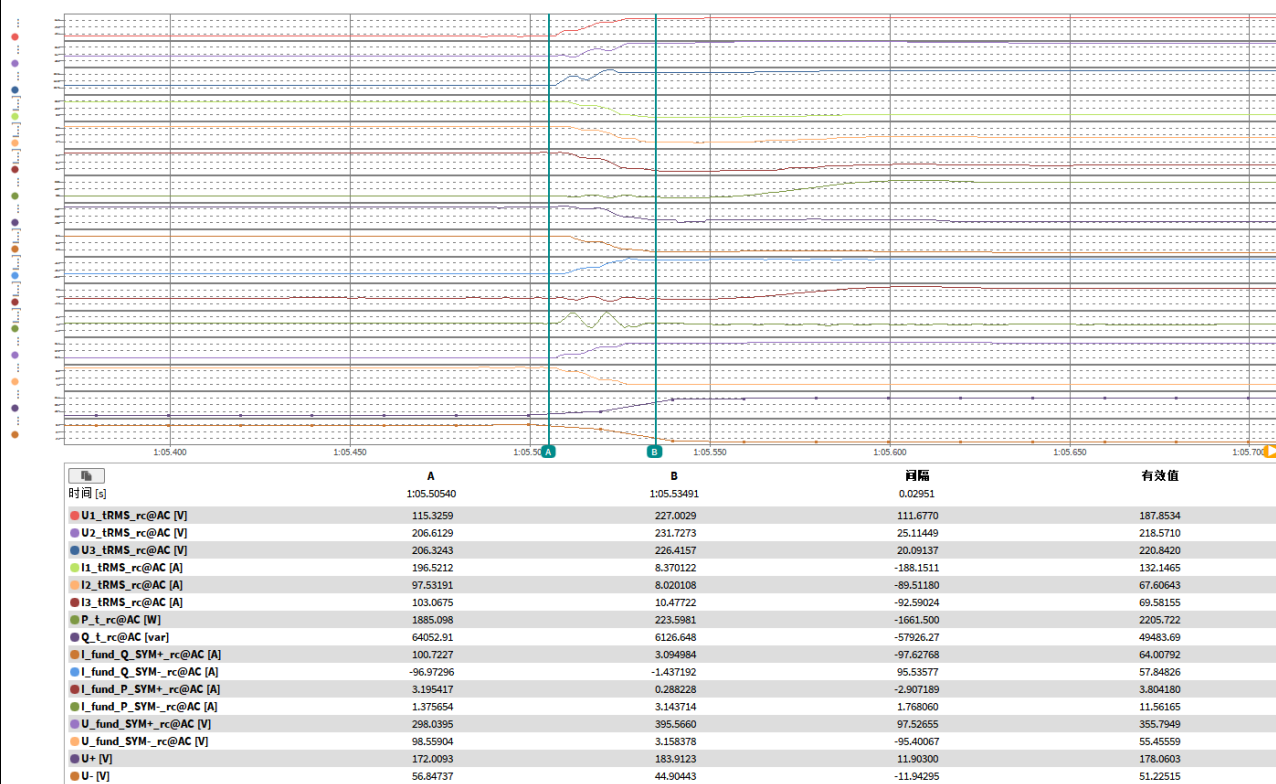
Injected reactive current at 60ms



Recover time – Active power

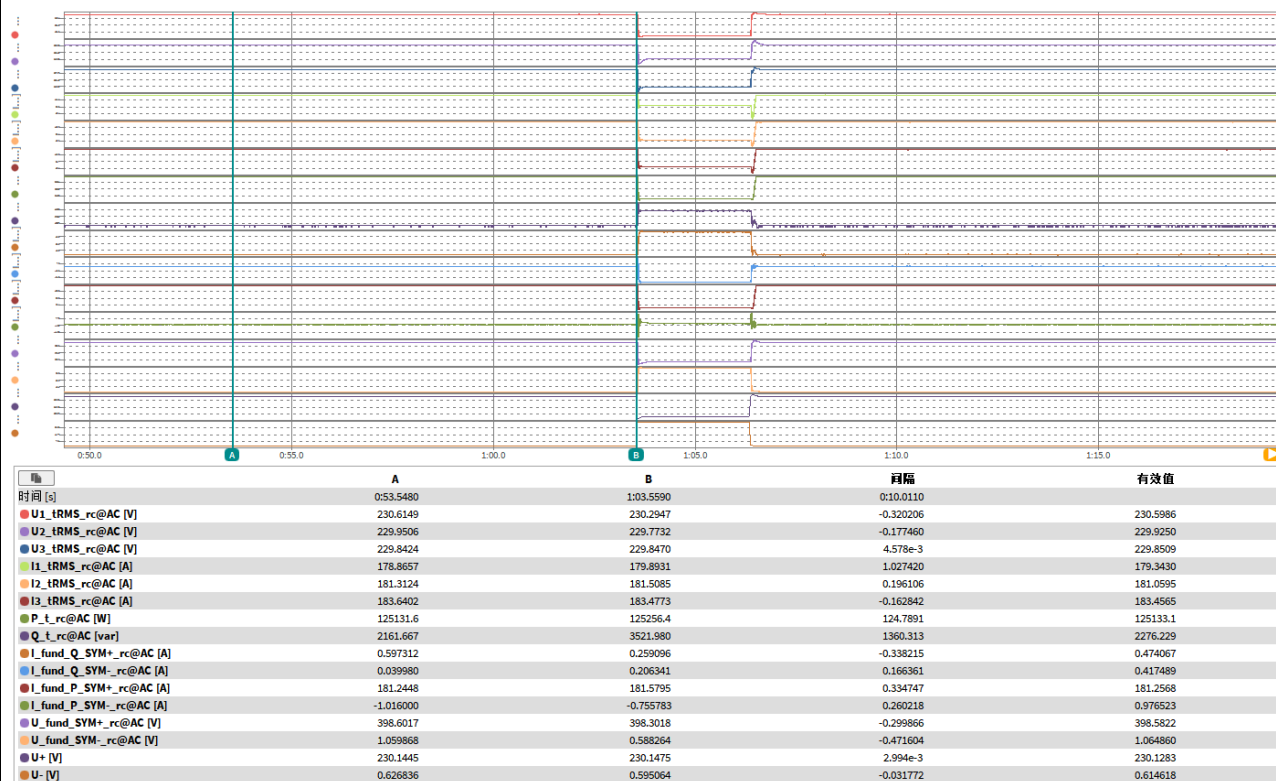


Recover time – Reactive power

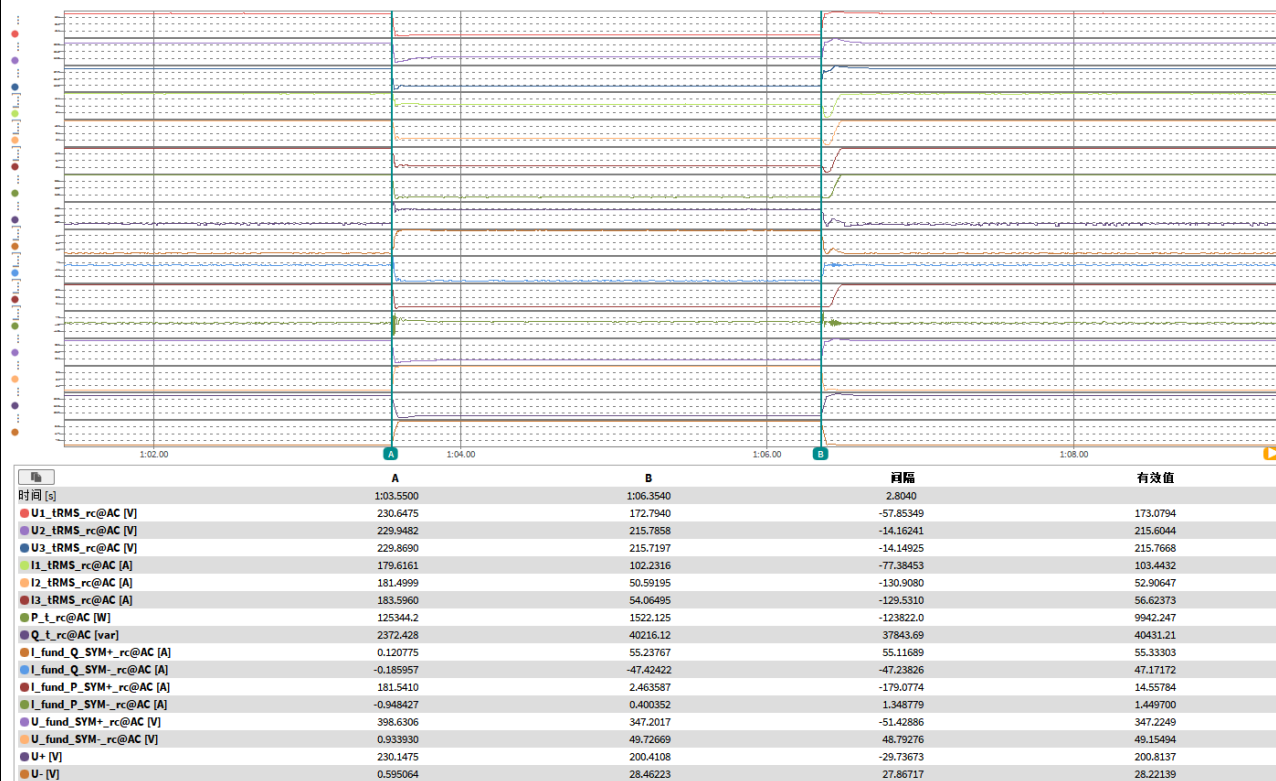


Test – VD9.1

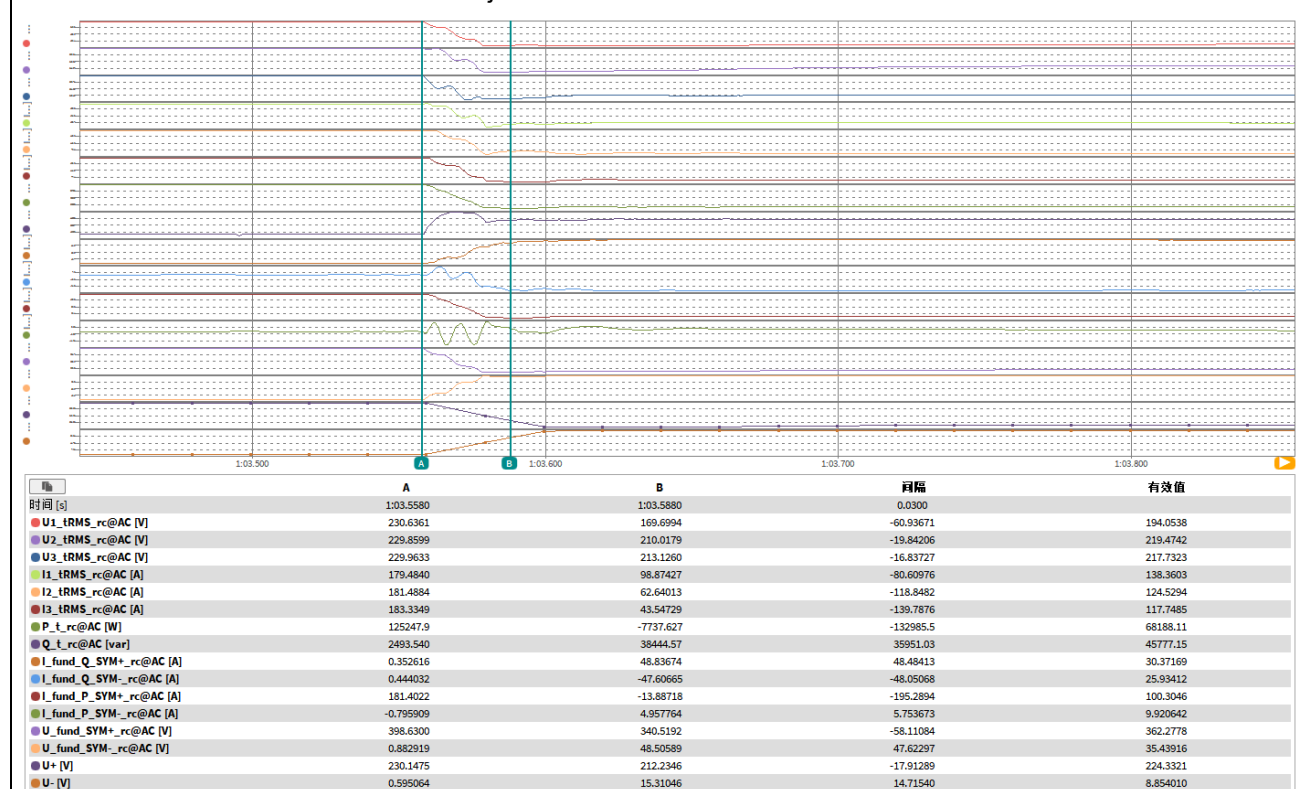
Before fault



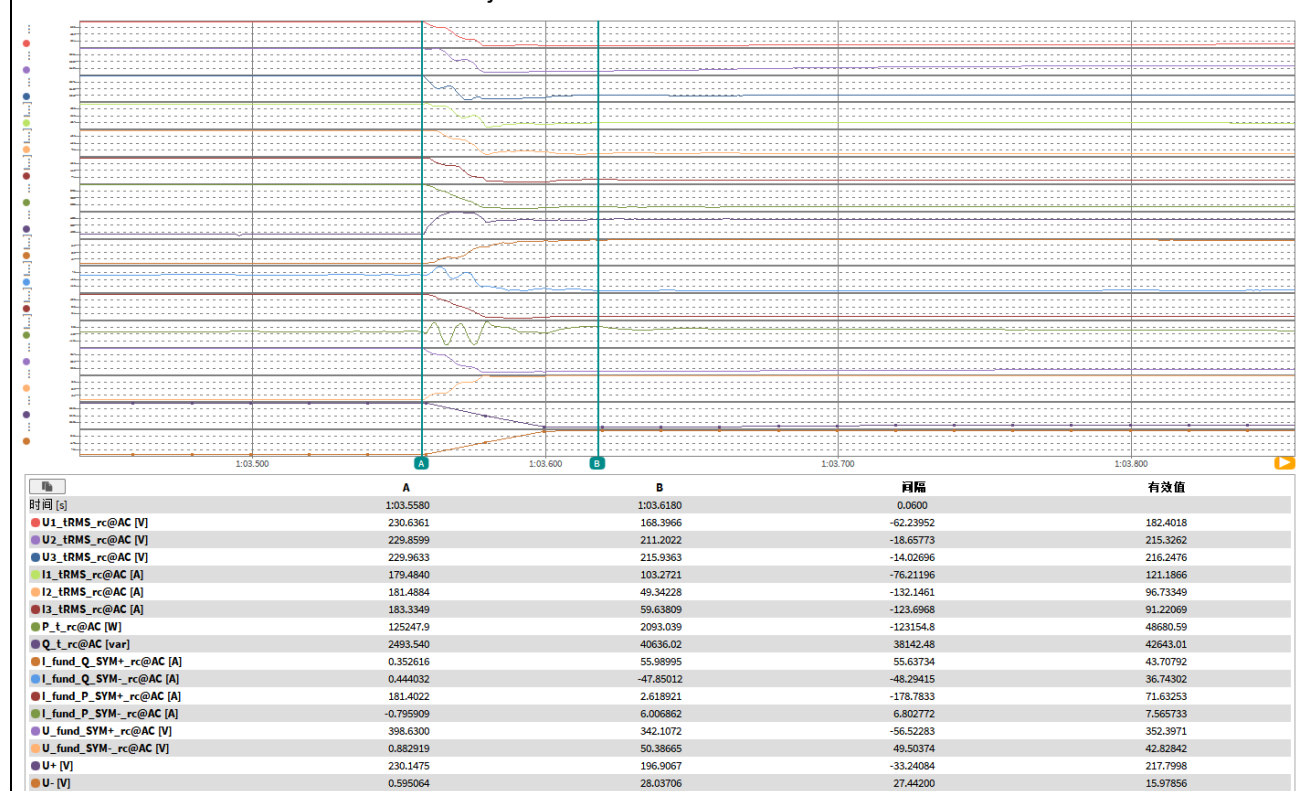
Duration fault time



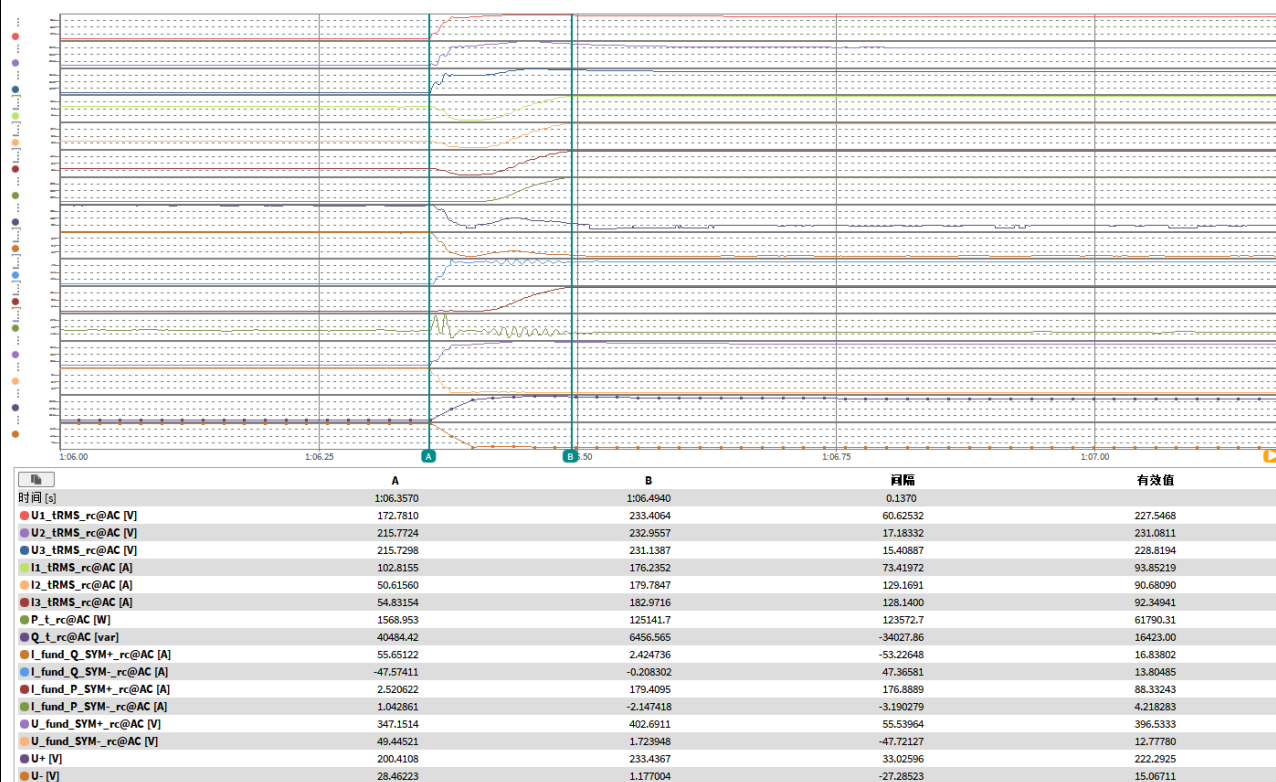
Injected reactive current at 30ms



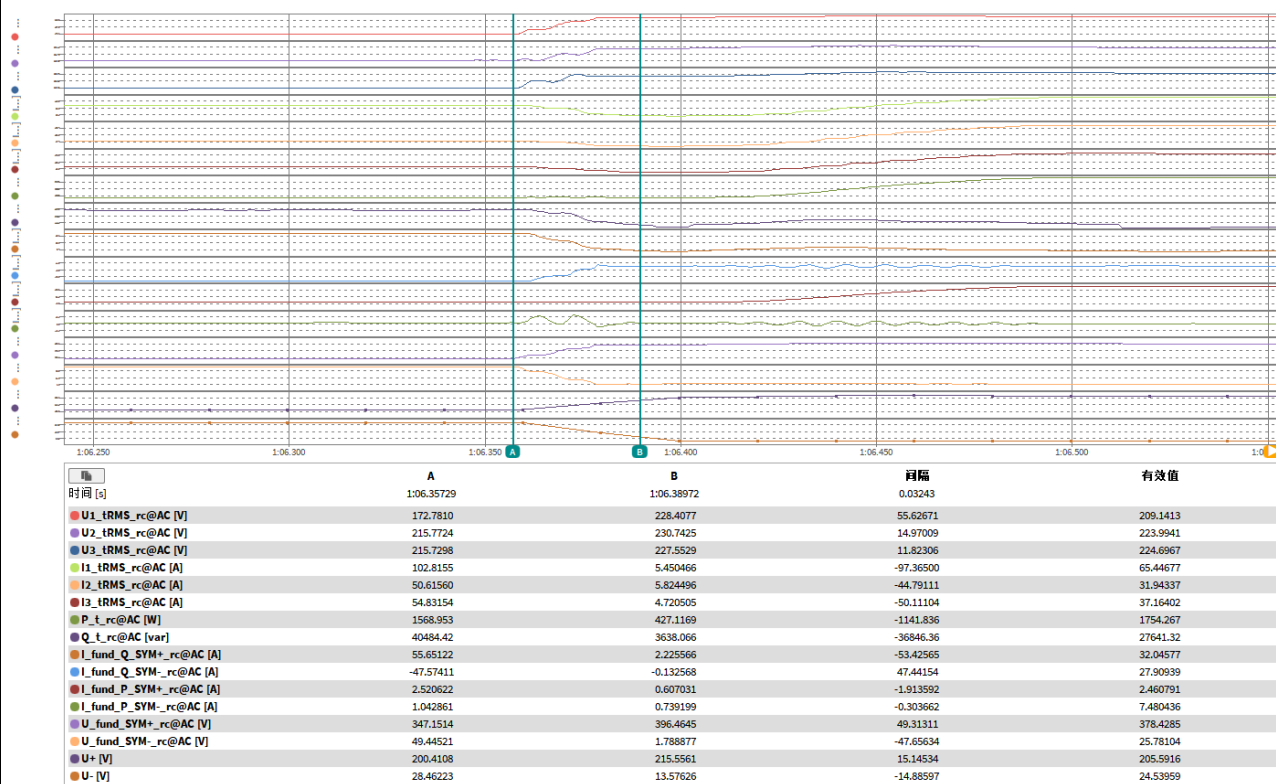
Injected reactive current at 60ms



Recover time – Active power

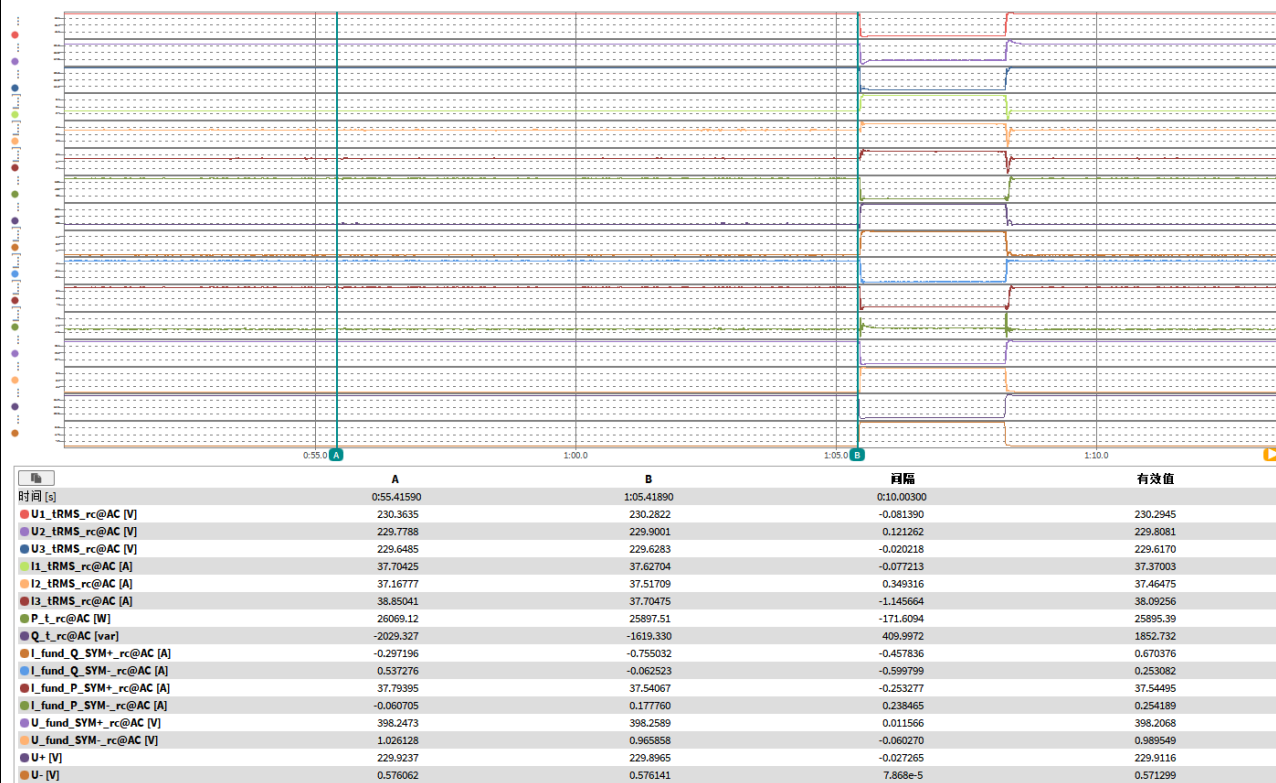


Recover time – Reactive power

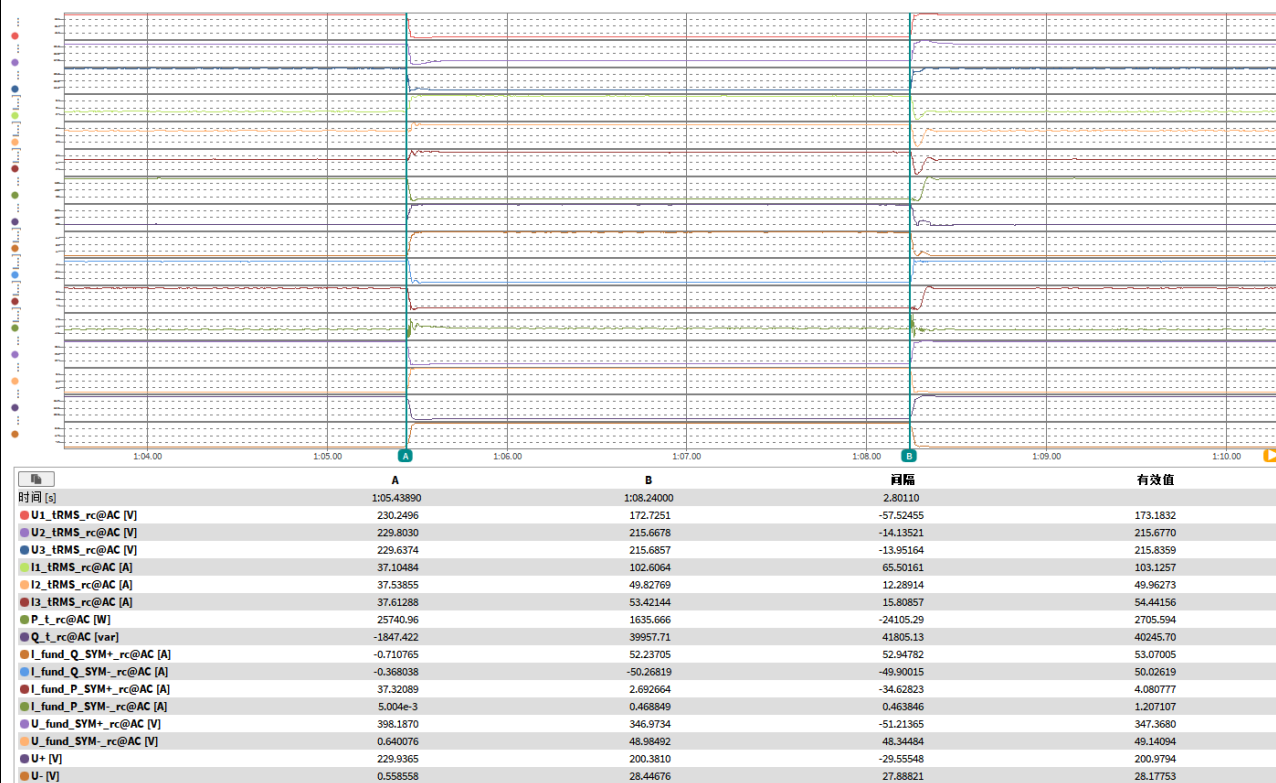


Test – VD9.2.a

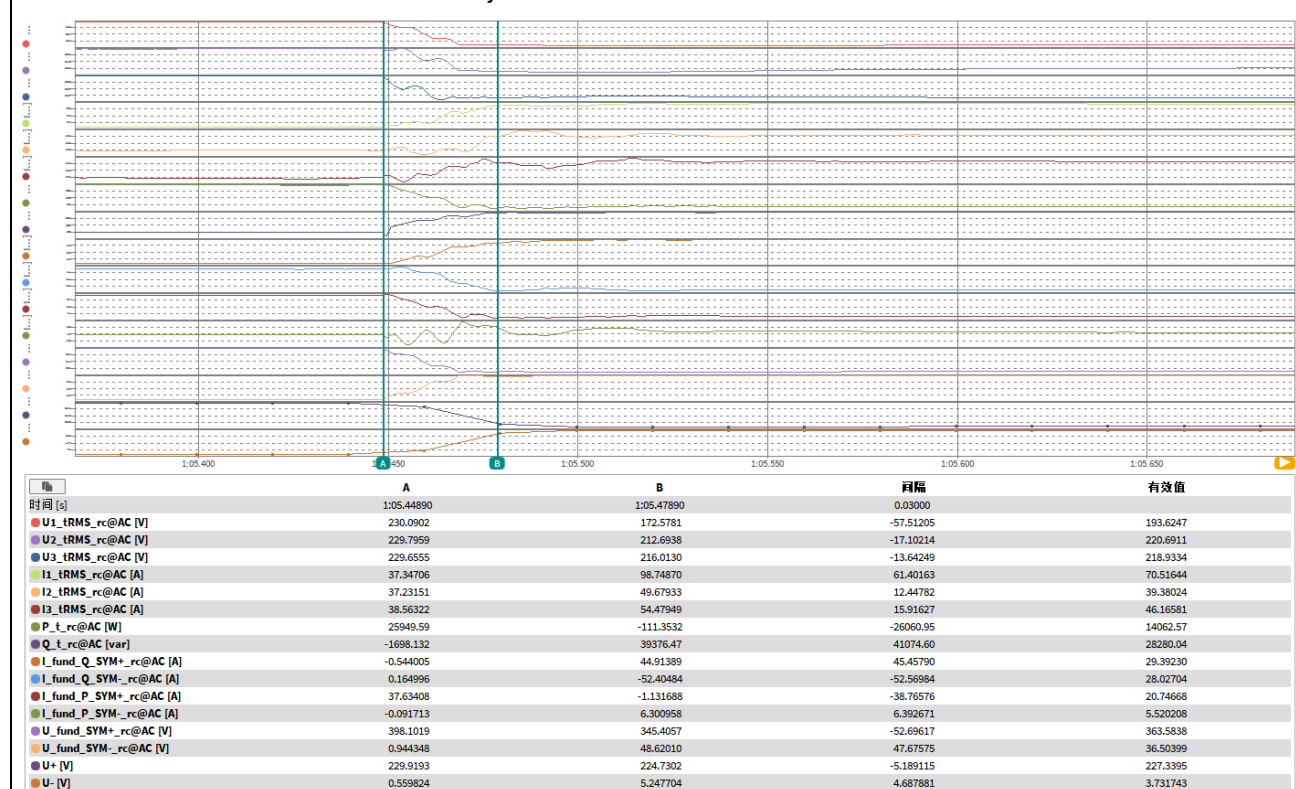
Before fault



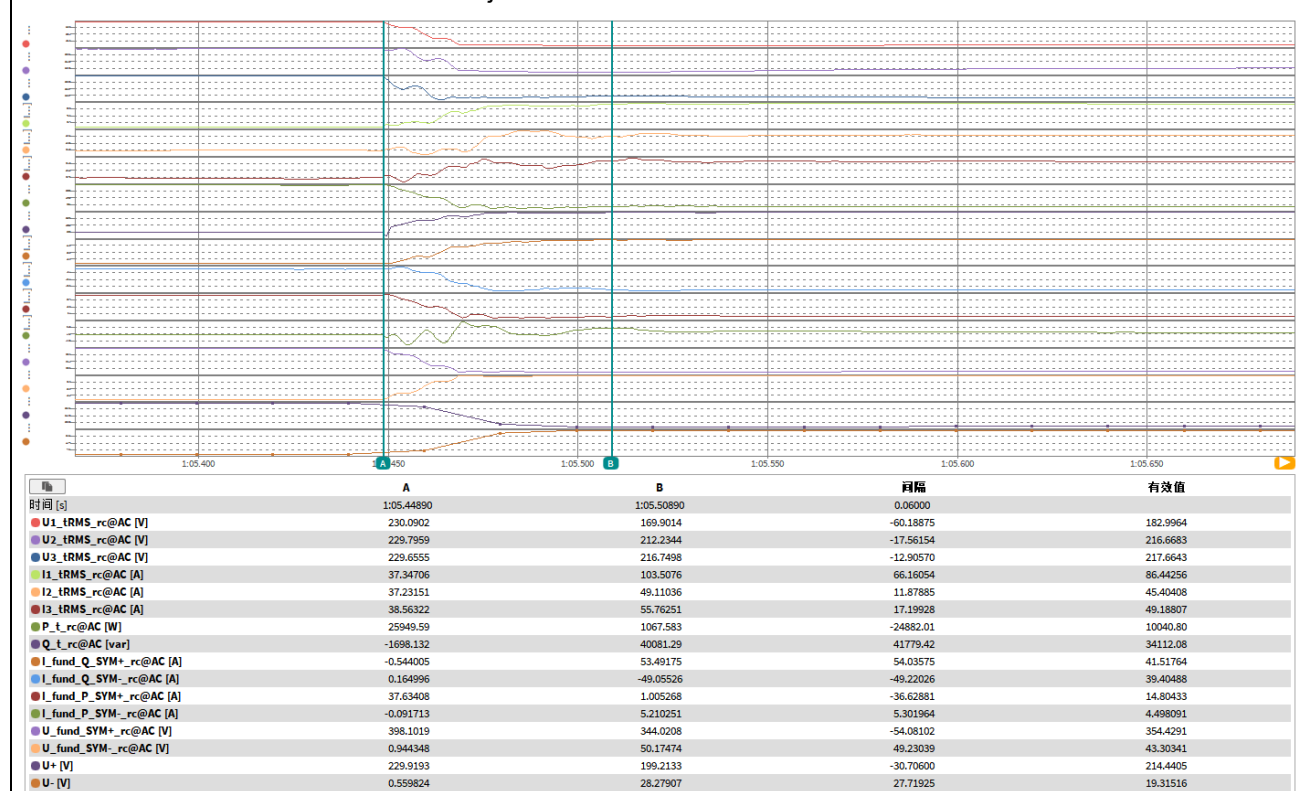
Duration fault time



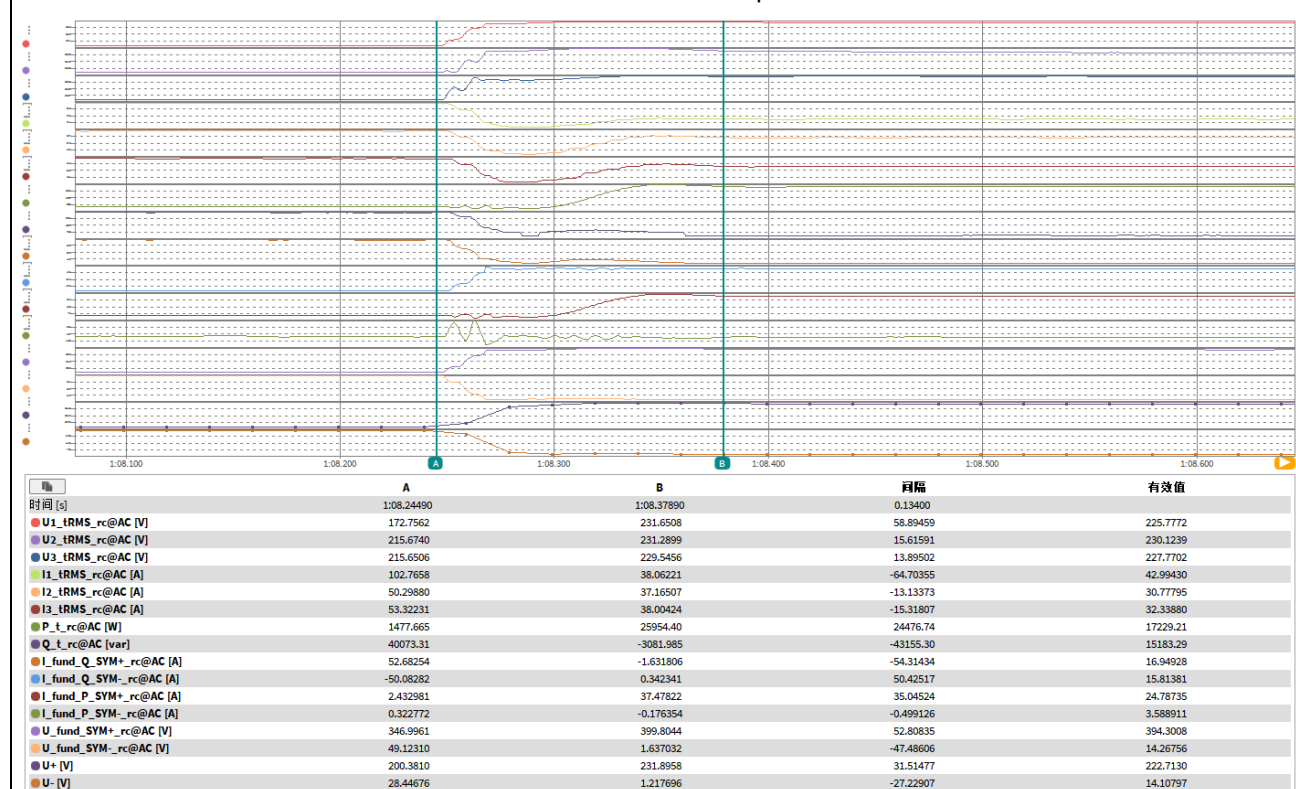
Injected reactive current at 30ms



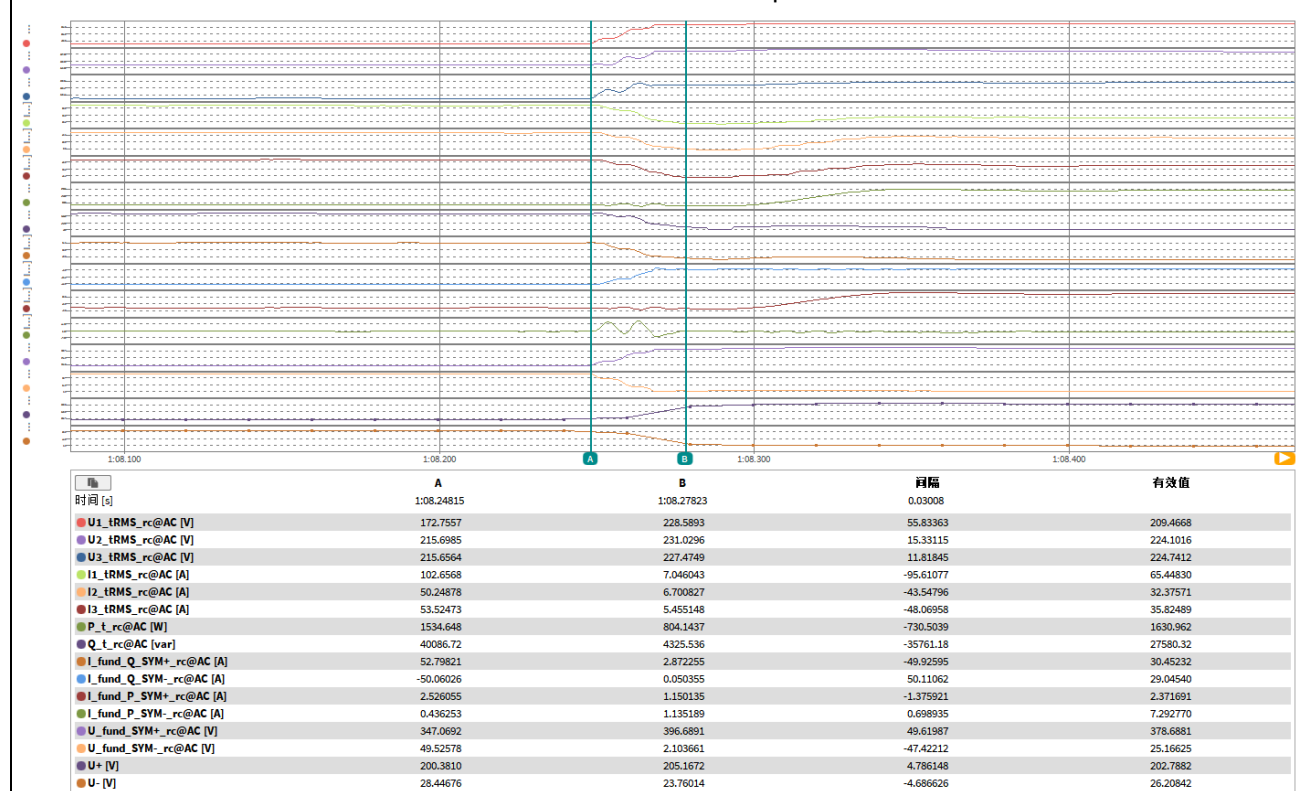
Injected reactive current at 60ms



Recover time – Active power

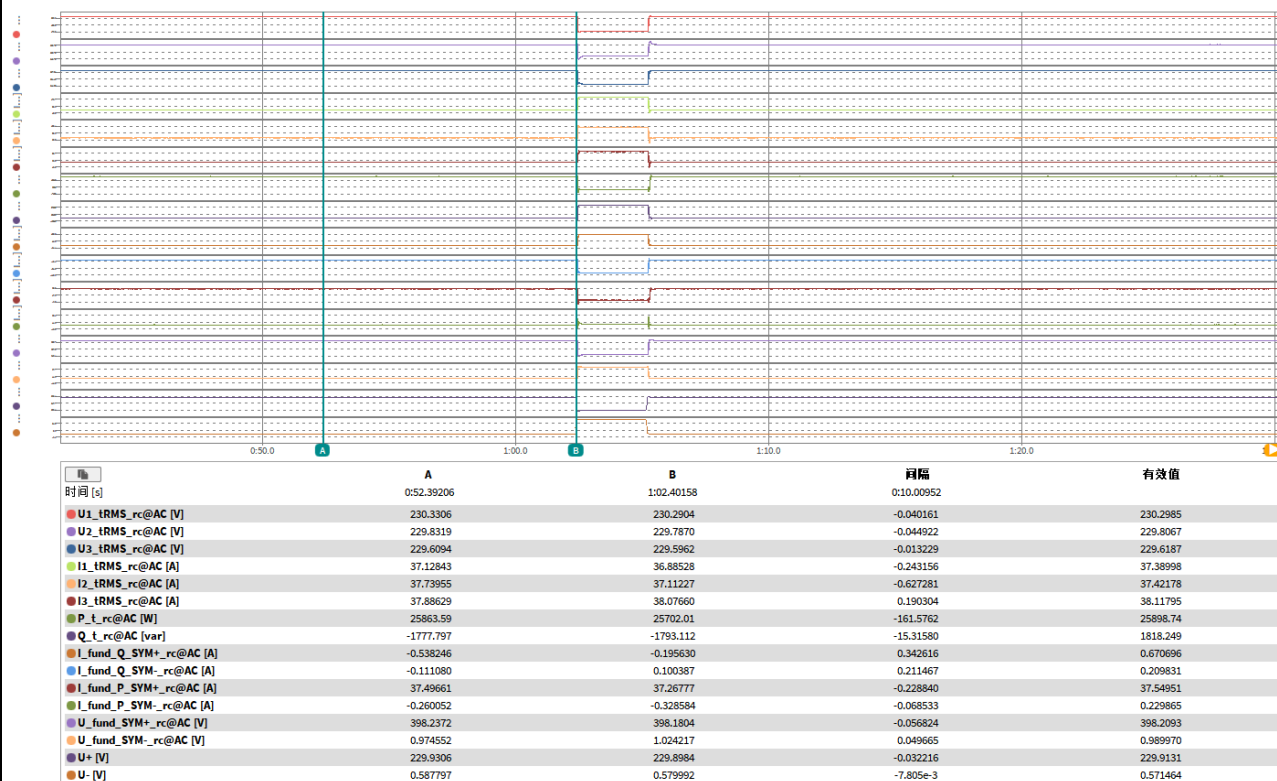


Recover time – Reactive power

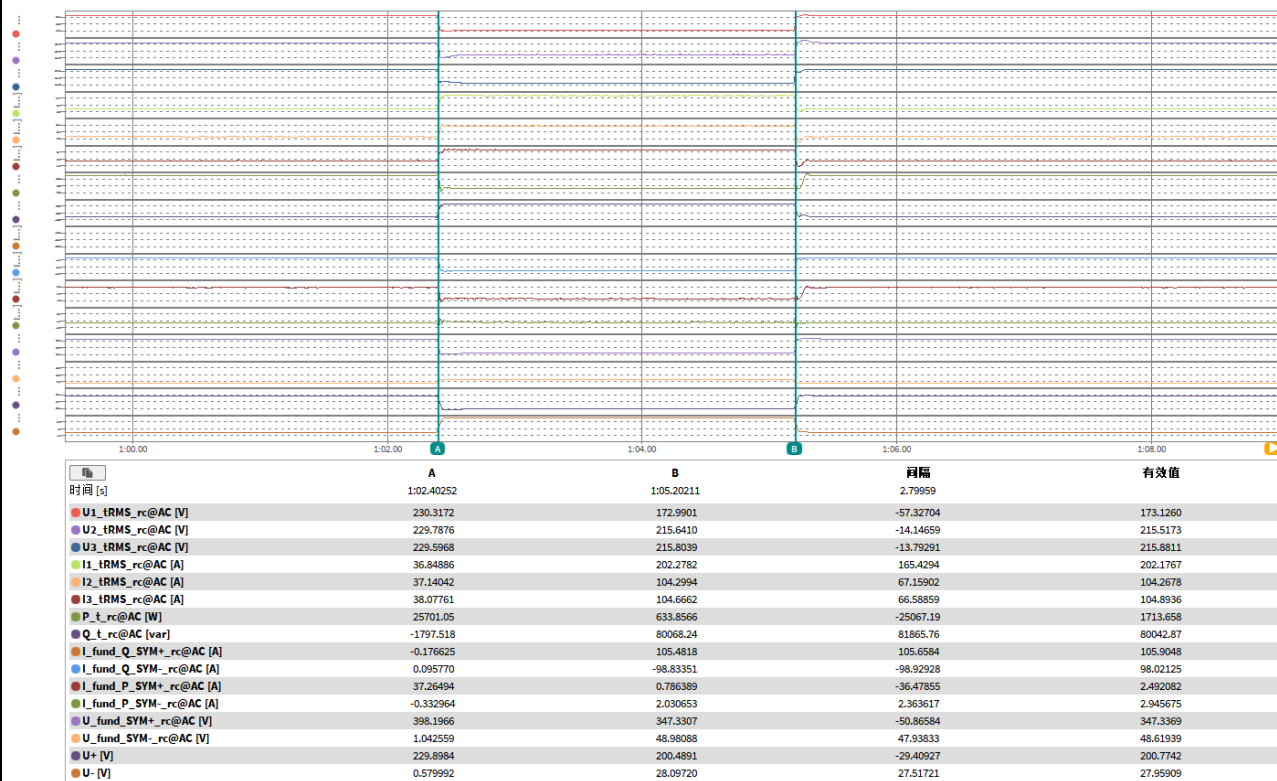


Test – VD9.2.b

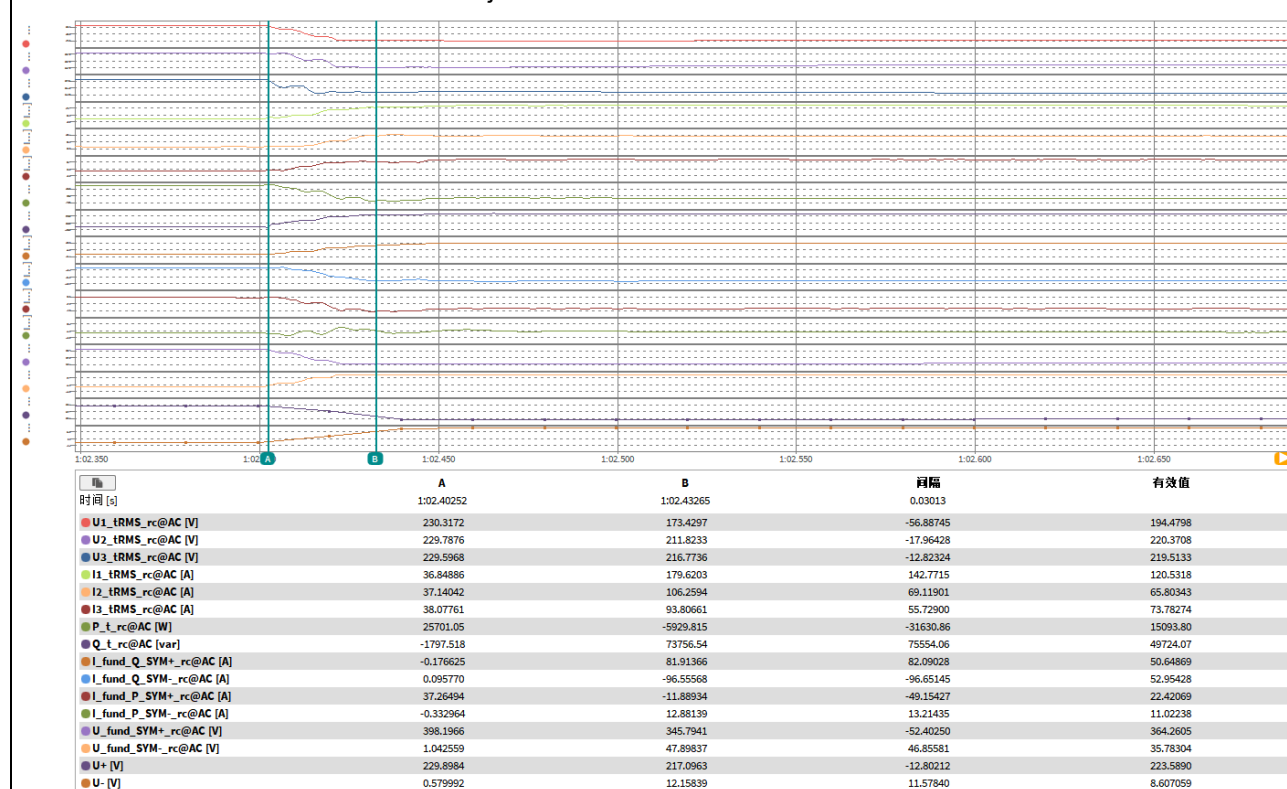
Before fault



Duration fault time



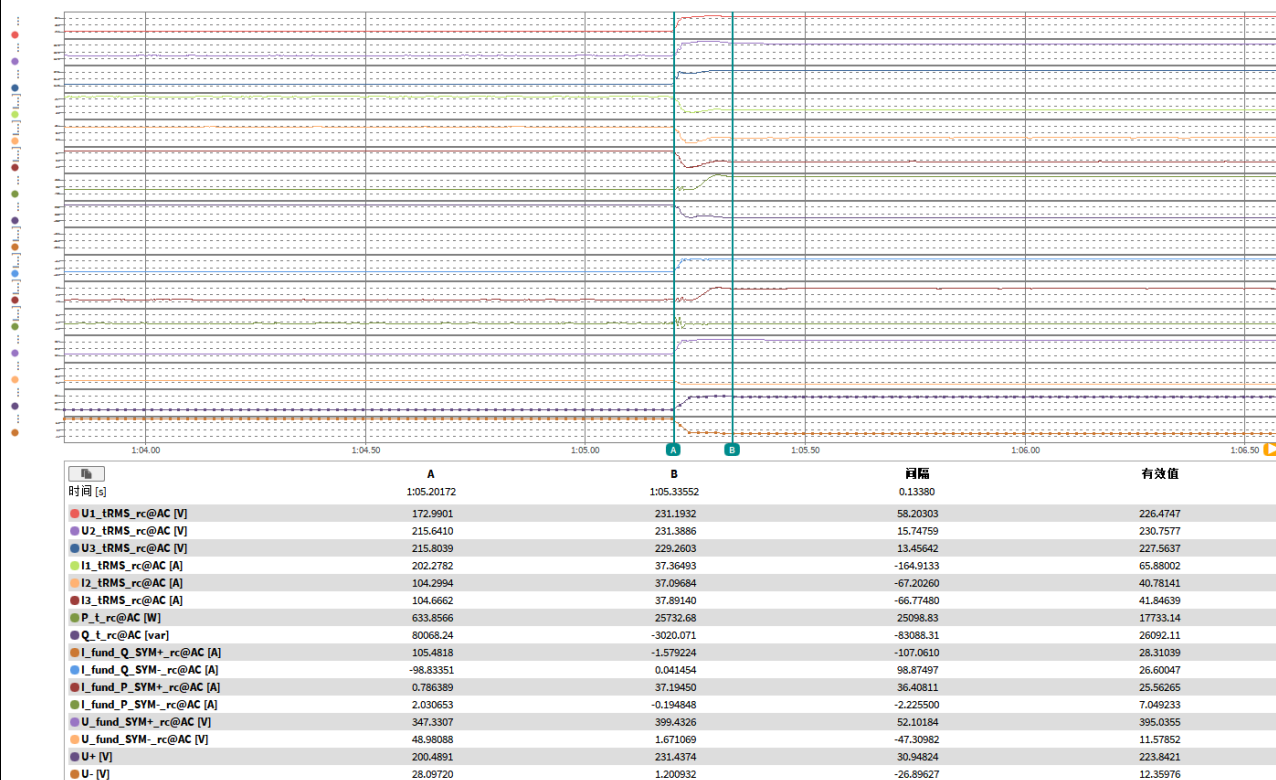
Injected reactive current at 30ms



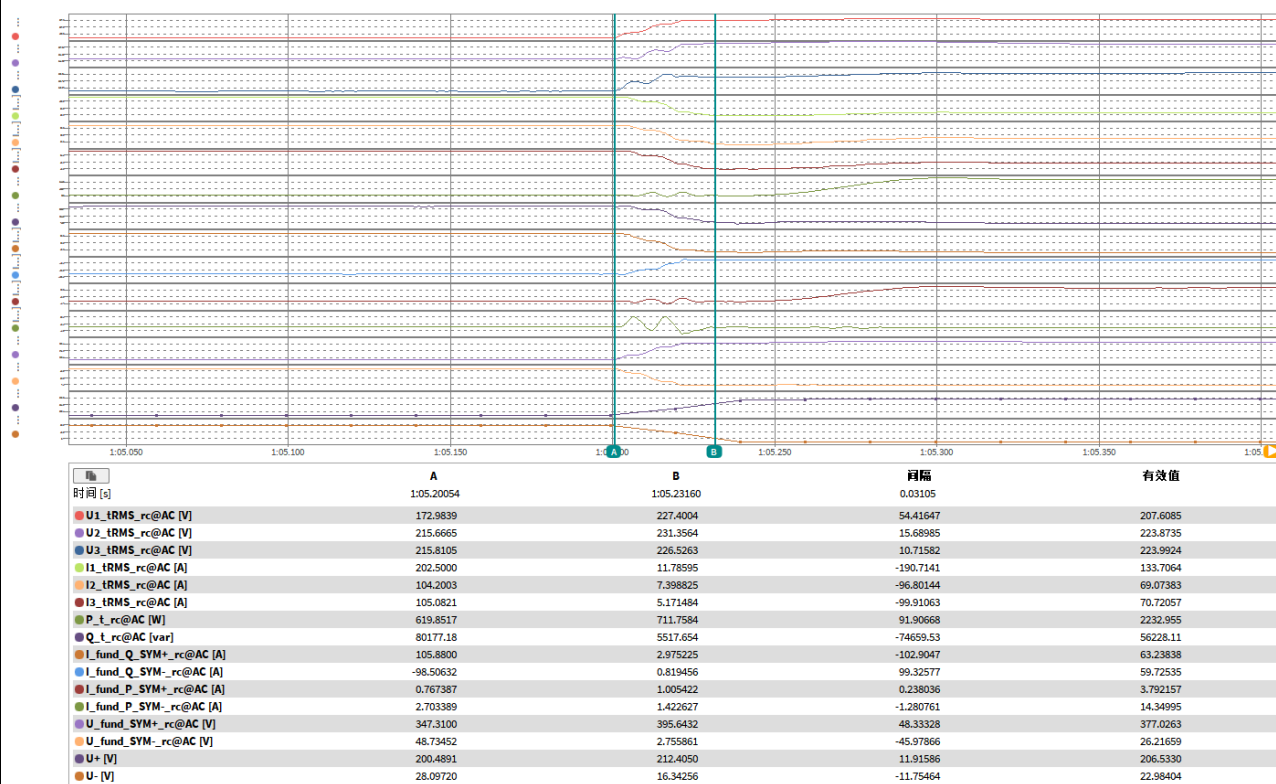
Injected reactive current at 60ms



Recover time – Active power

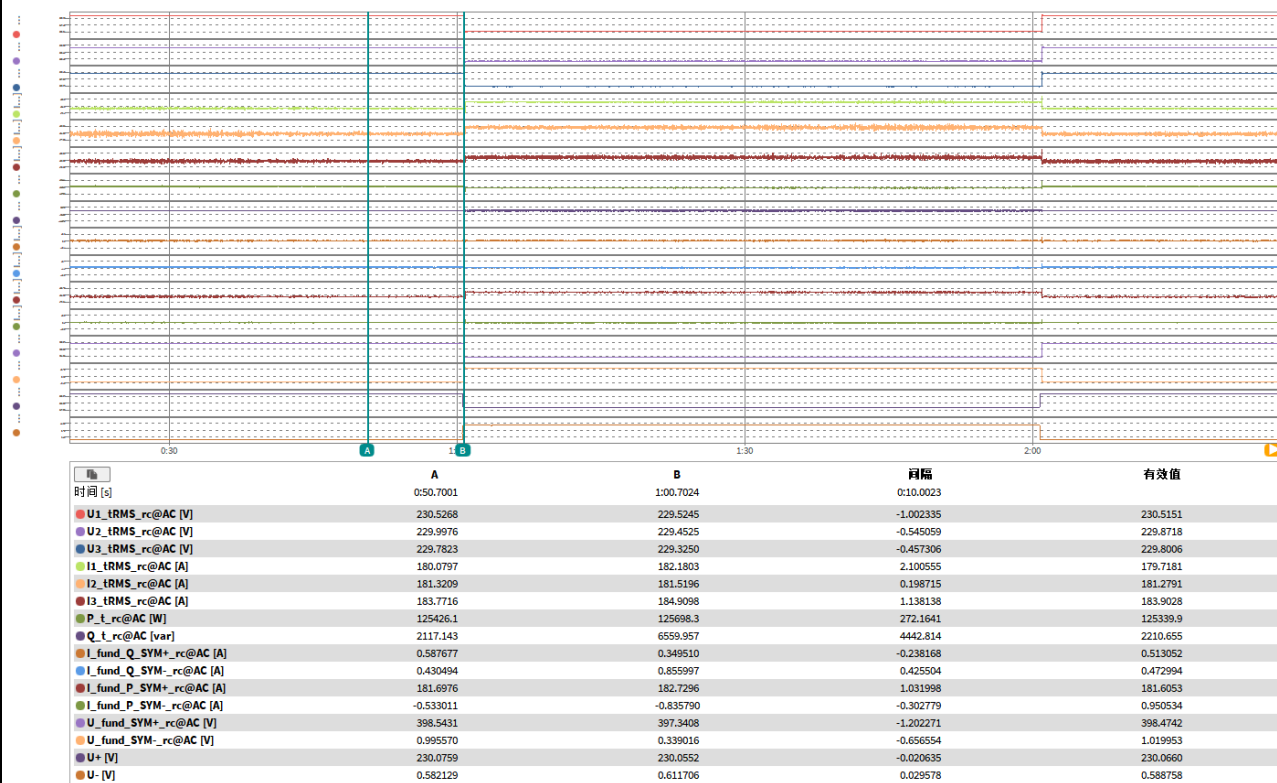


Recover time – Reactive power

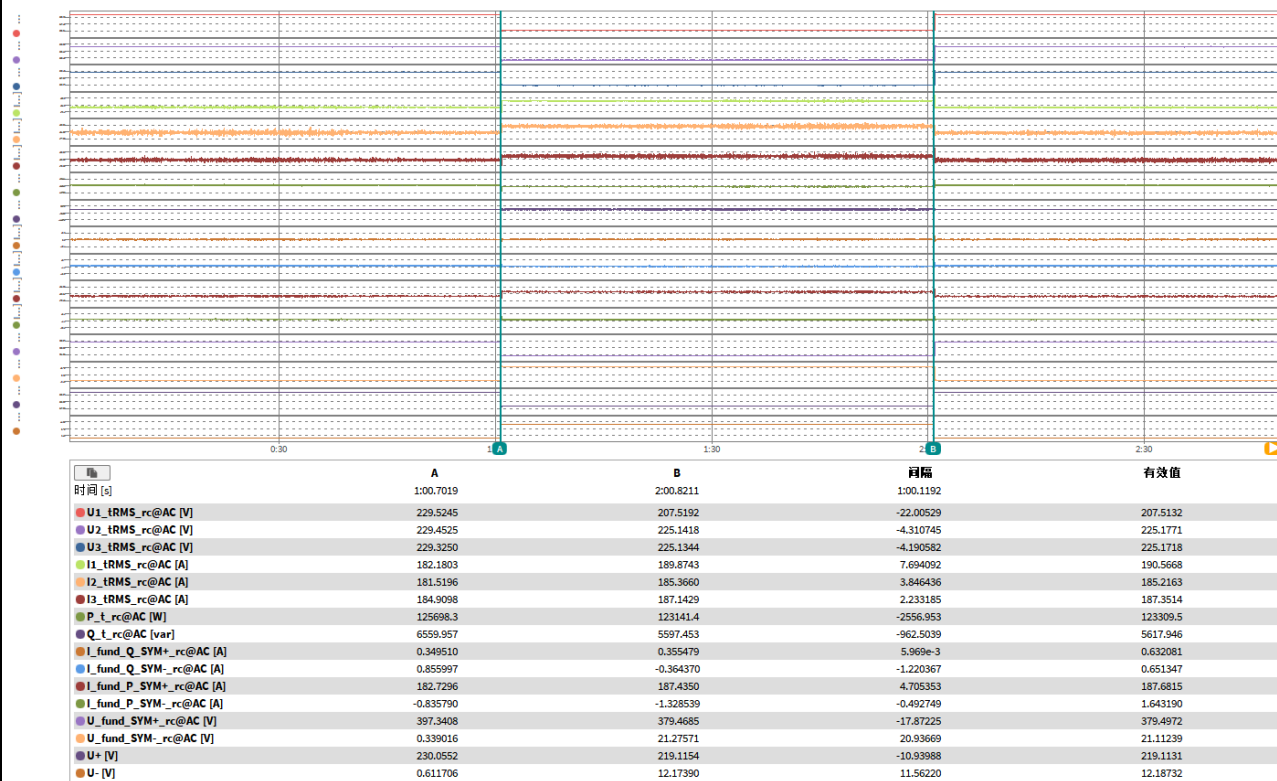


Test – VD10.1

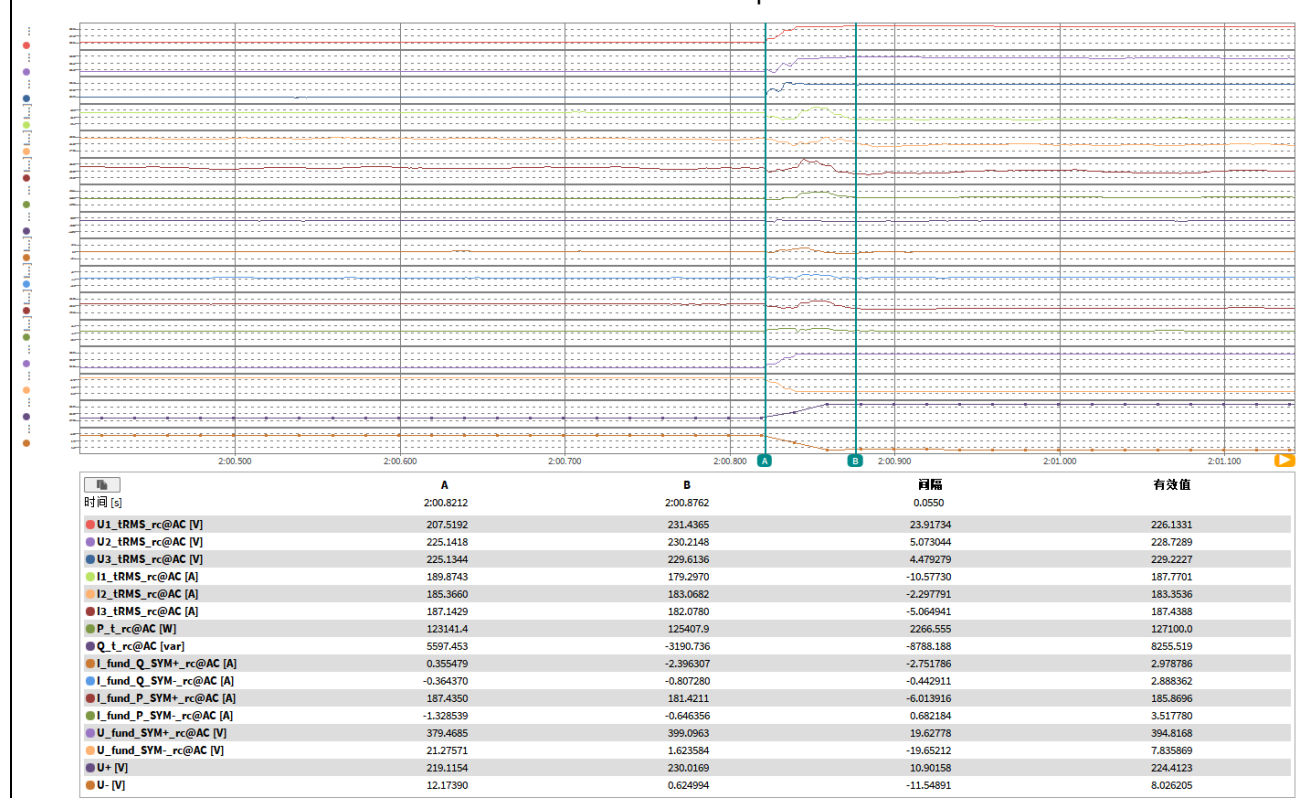
Before fault



Duration fault time

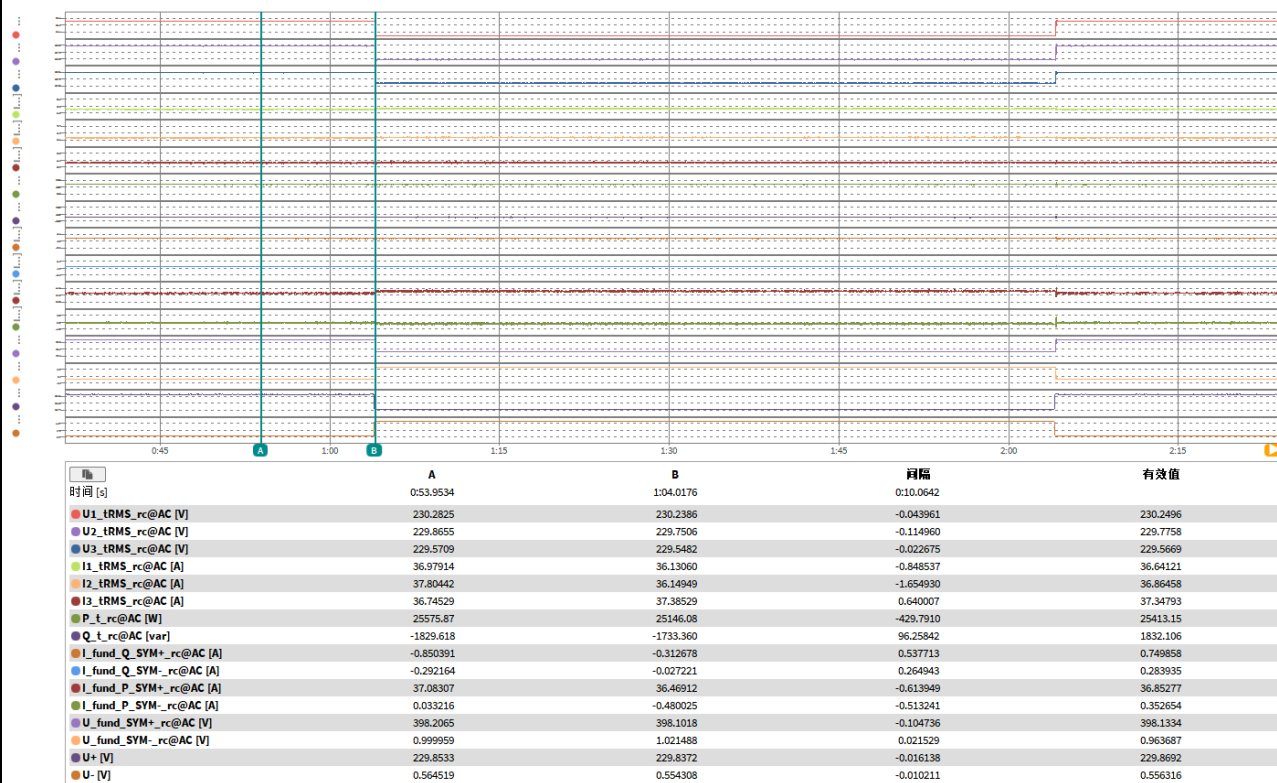


Recover time – Active power

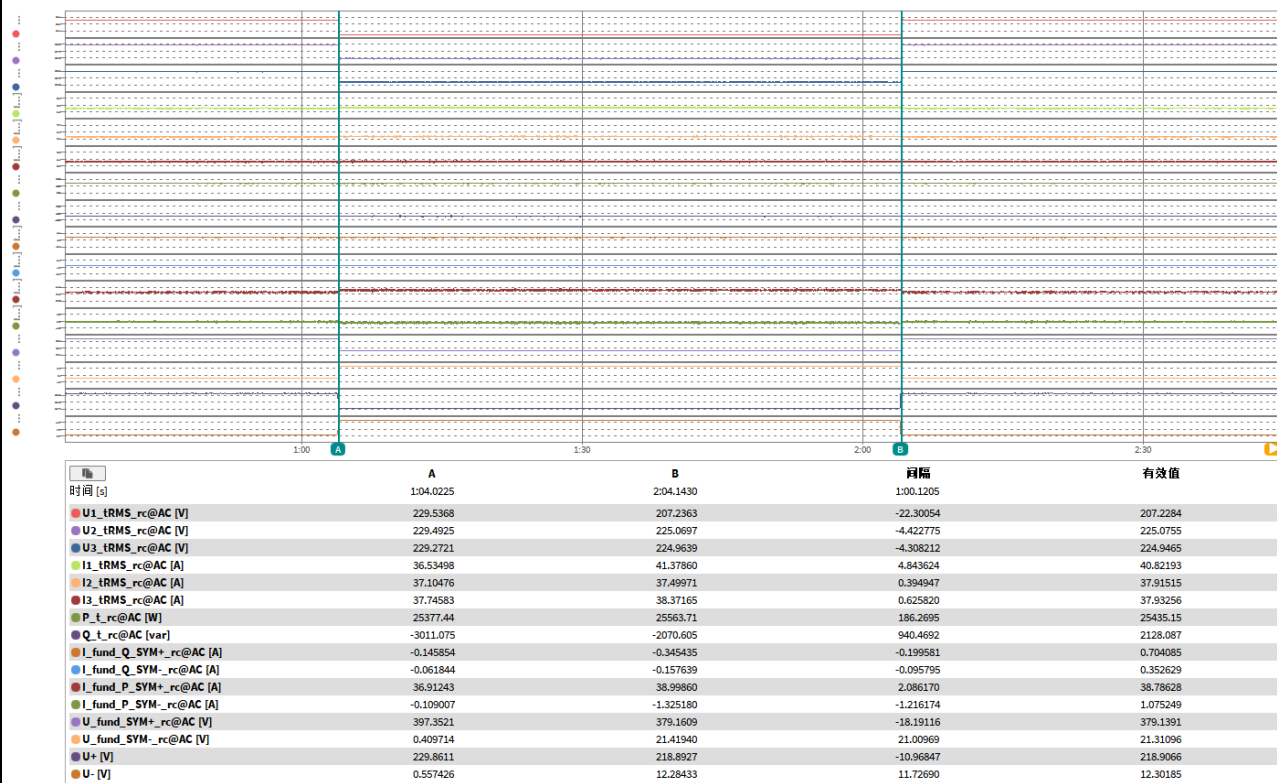


Test – VD10.2

Before fault



Duration fault time

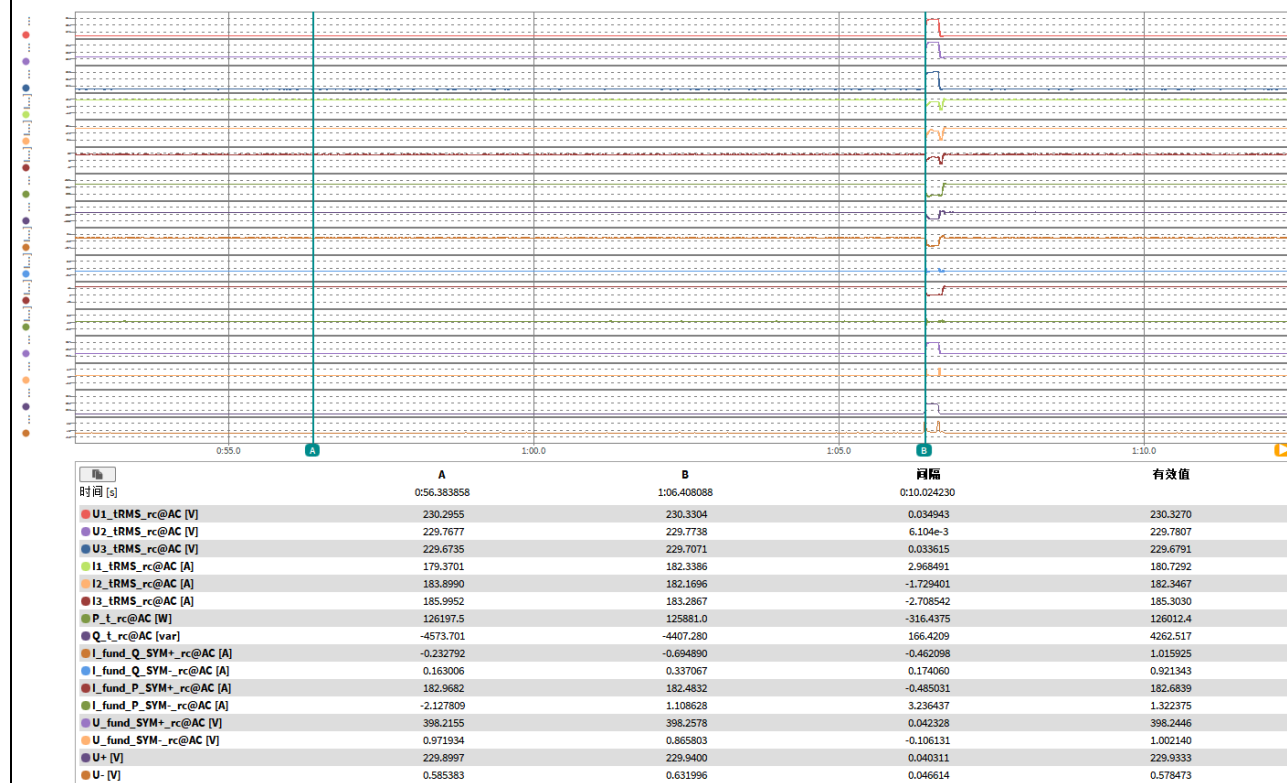


Recover time – Active power

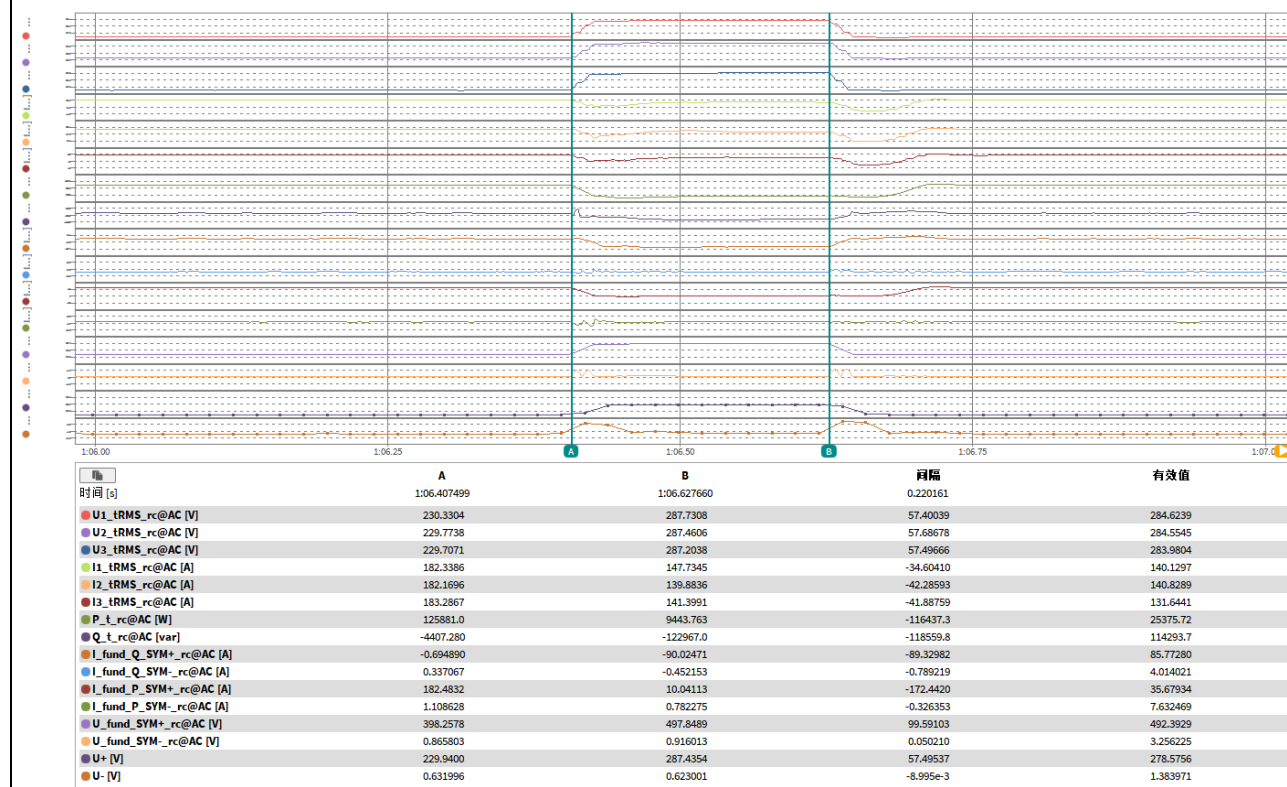


Test – VS1.1

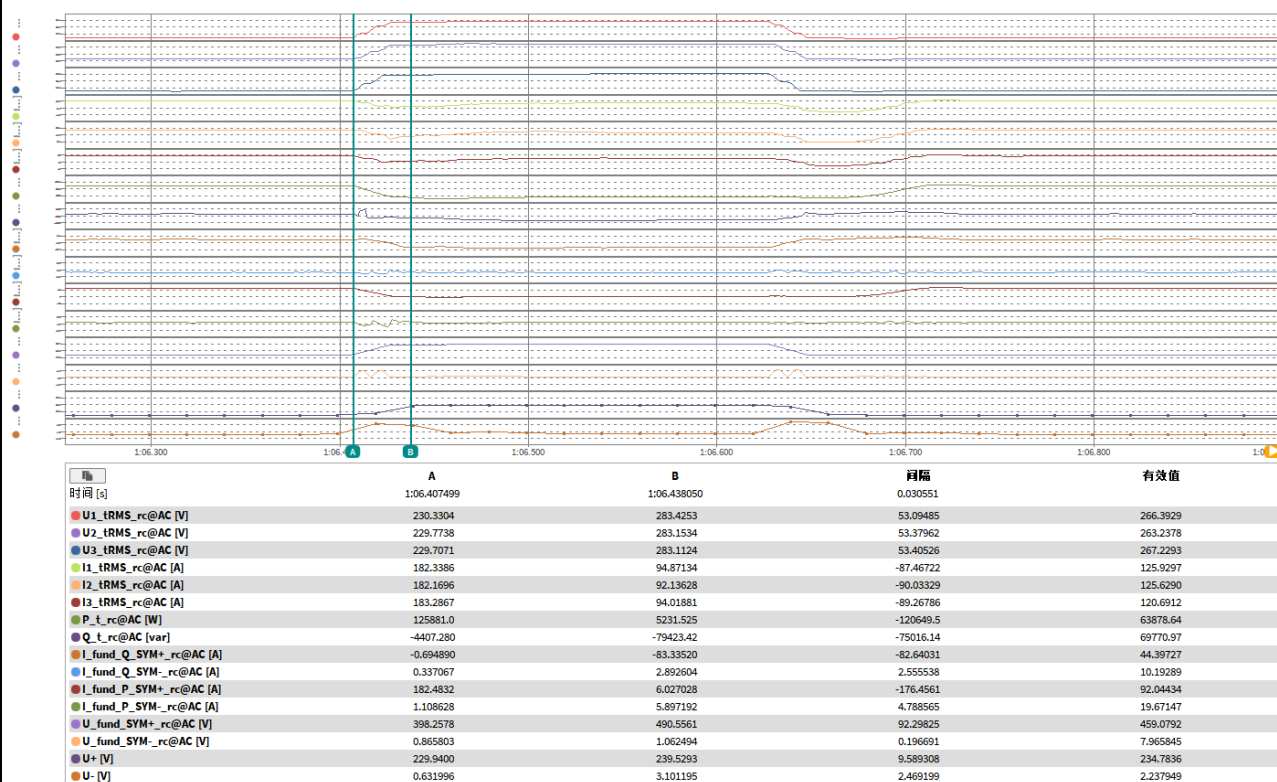
Before fault



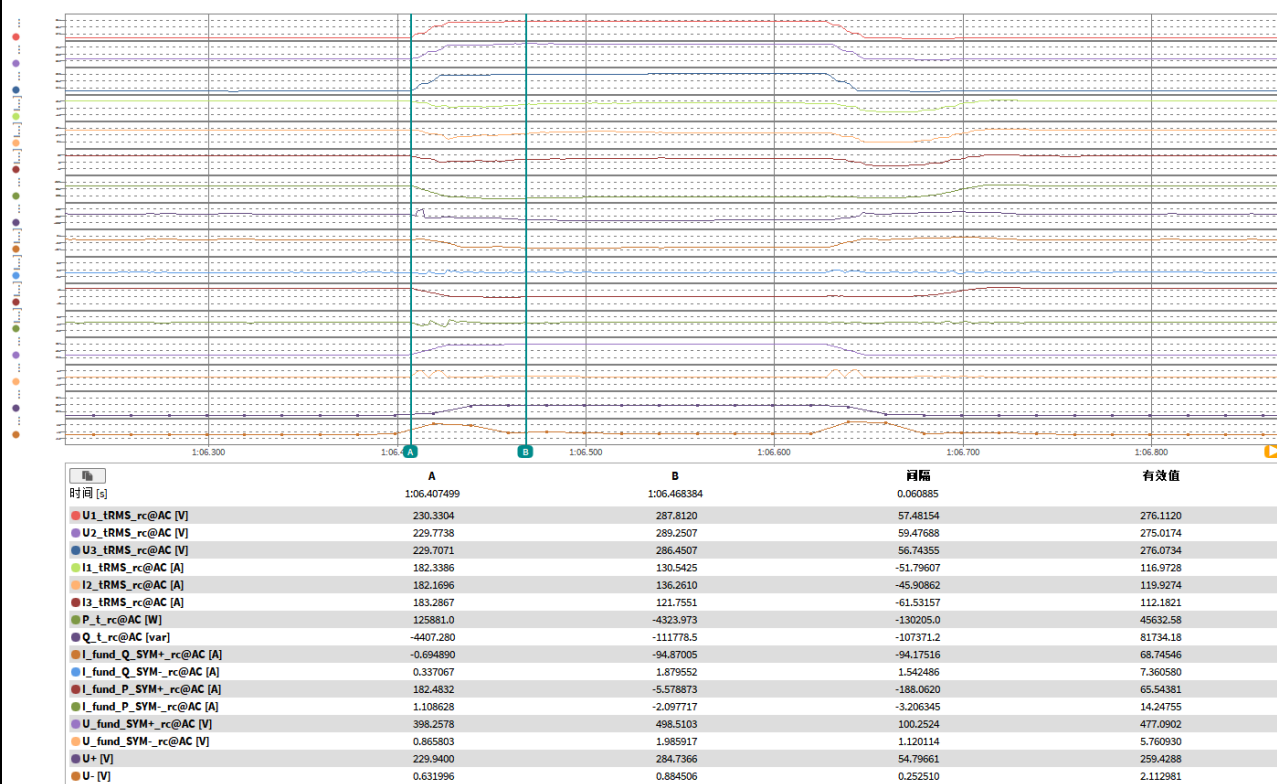
Duration fault time



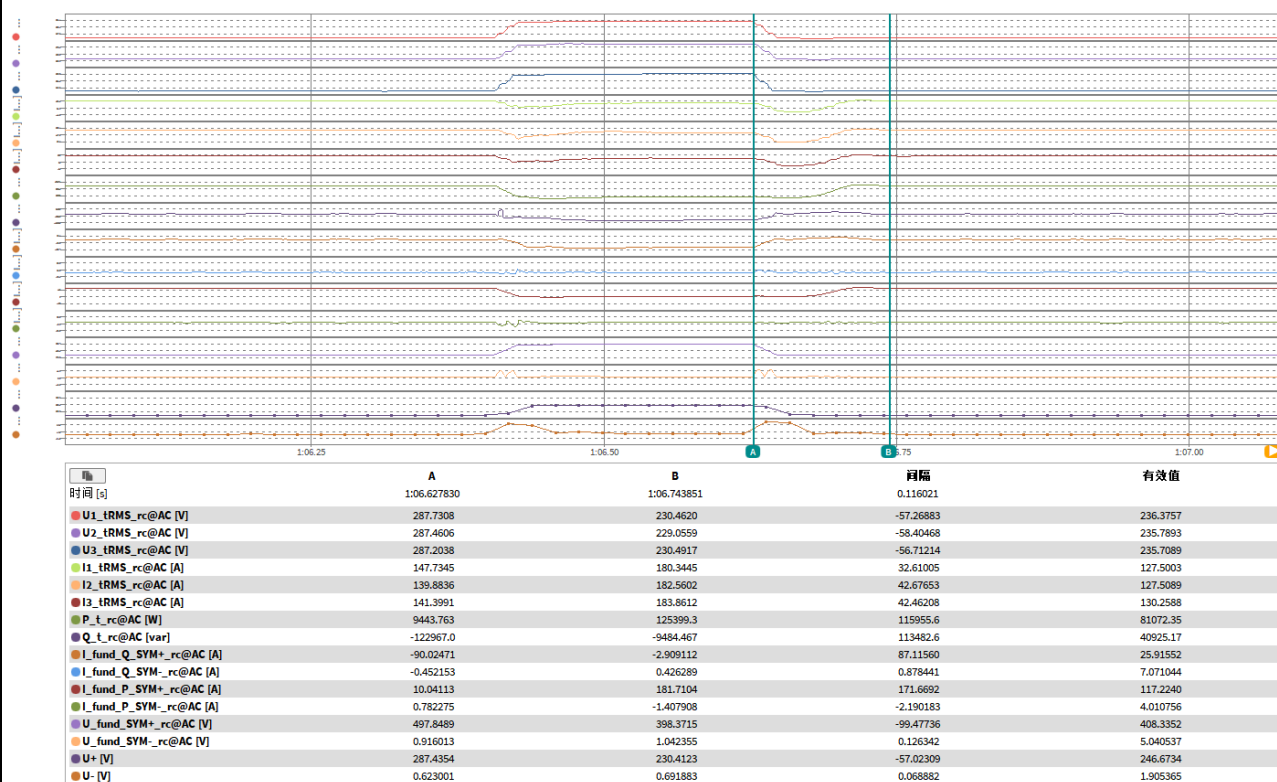
Injected reactive current at 30ms



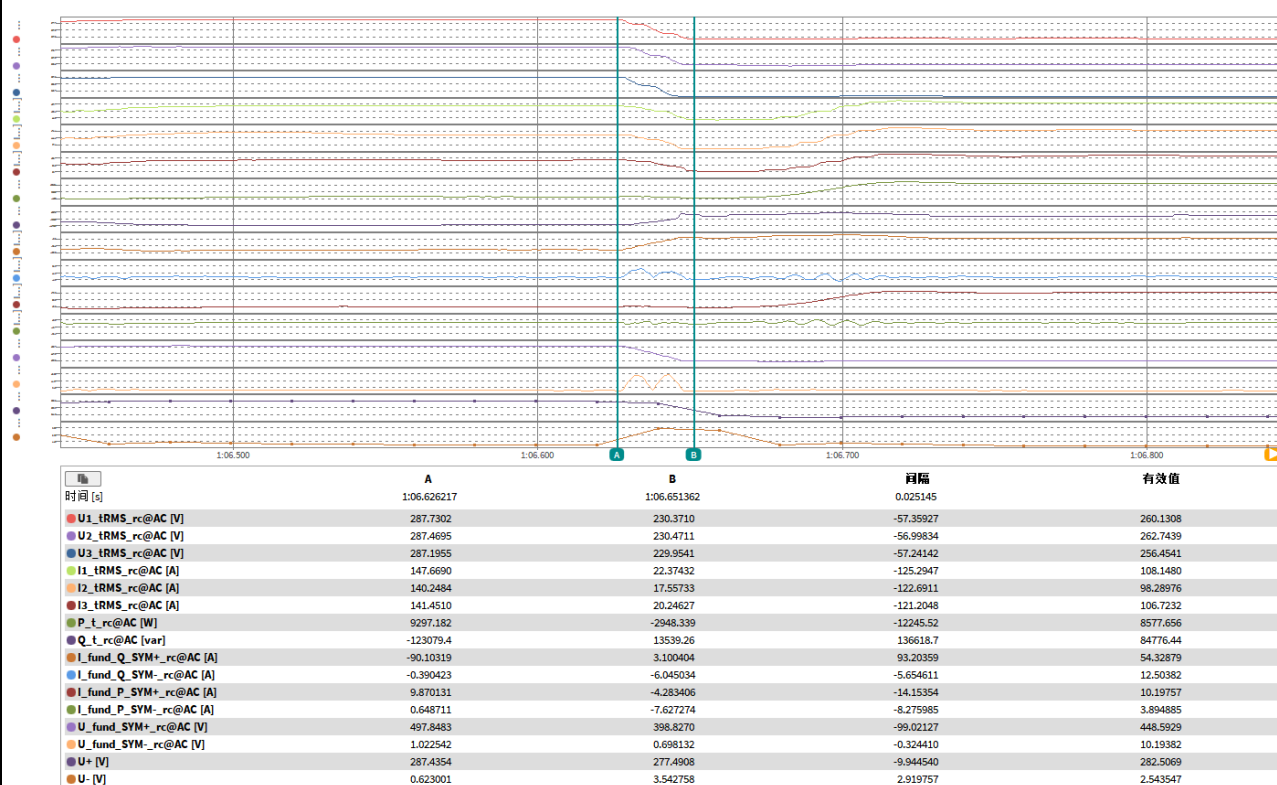
Injected reactive current at 60ms



Recover time – Active power

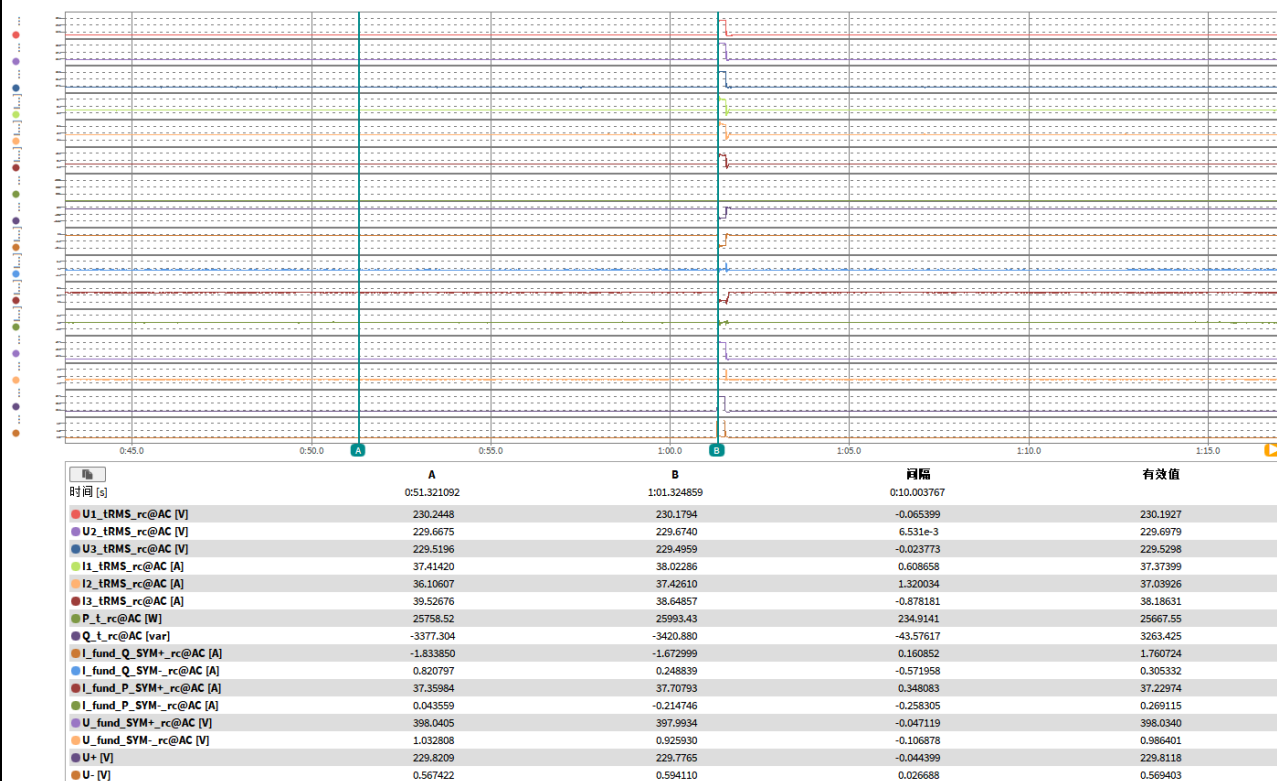


Recover time – Reactive power

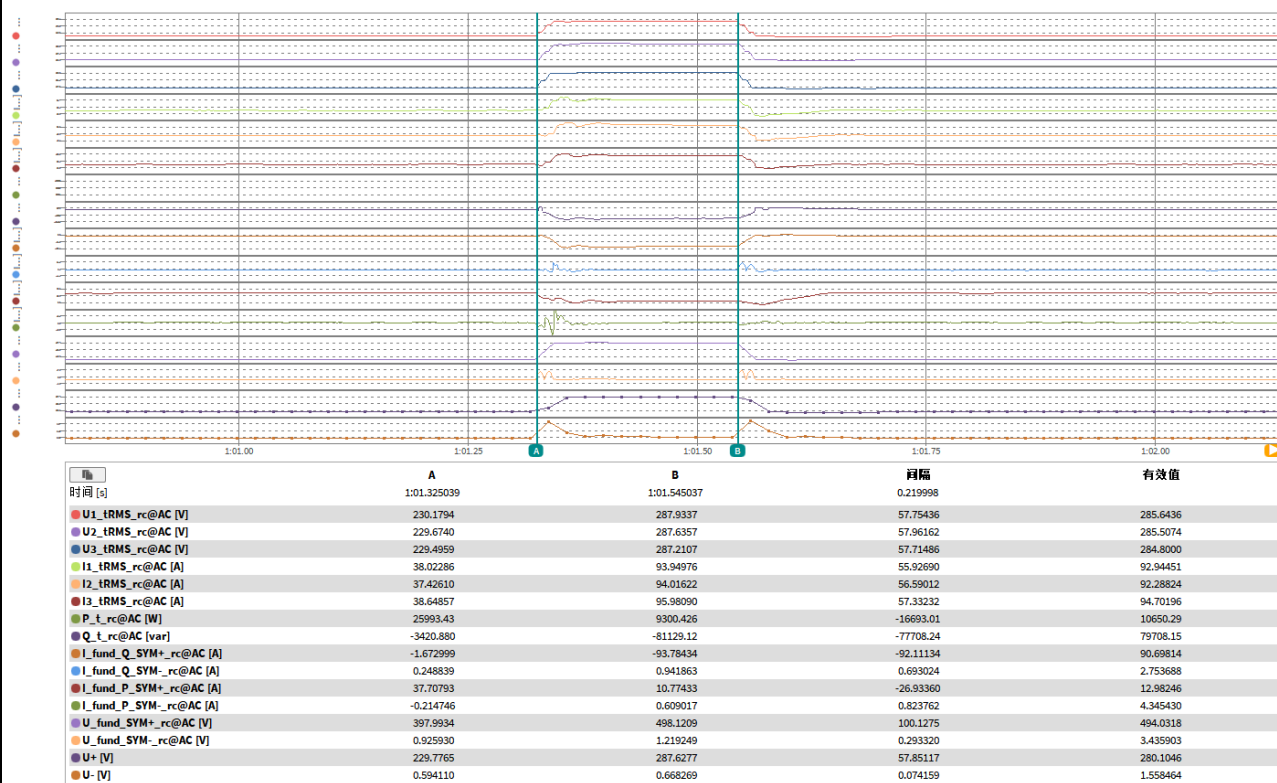


Test – VS1.2

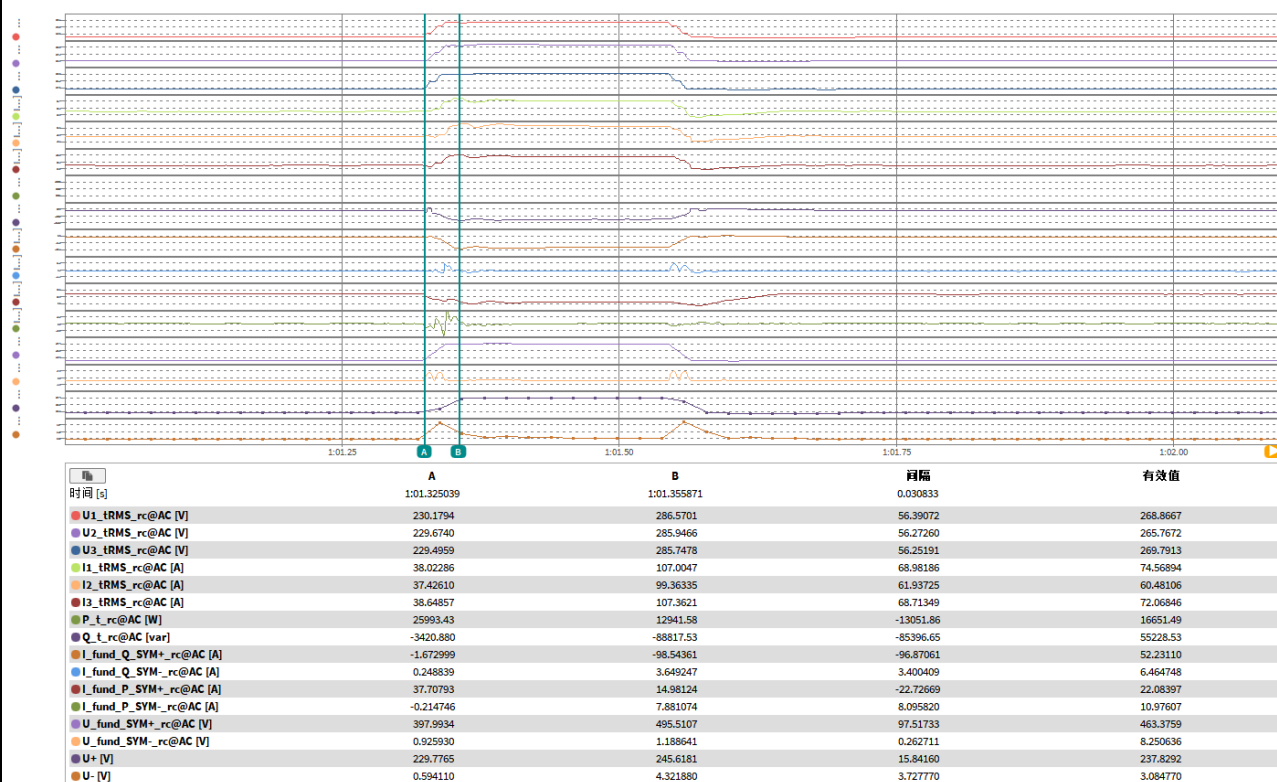
Before fault



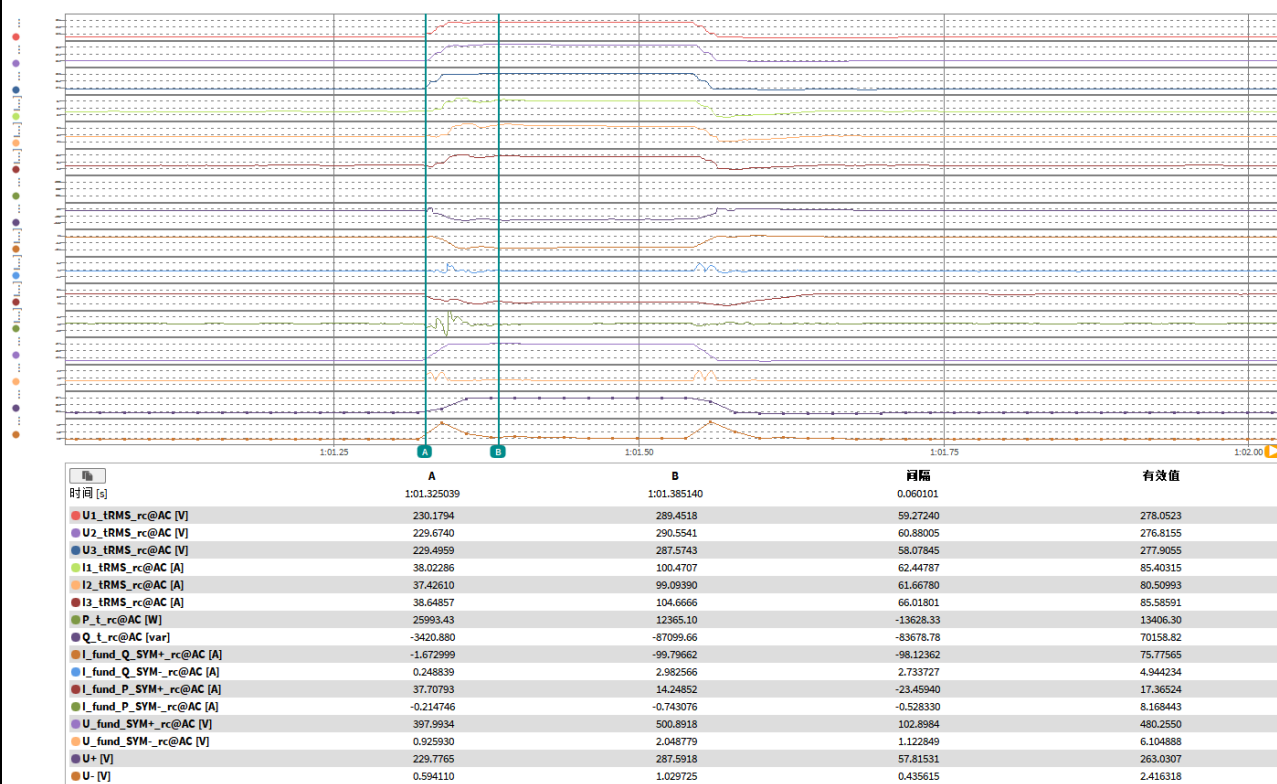
Duration fault time



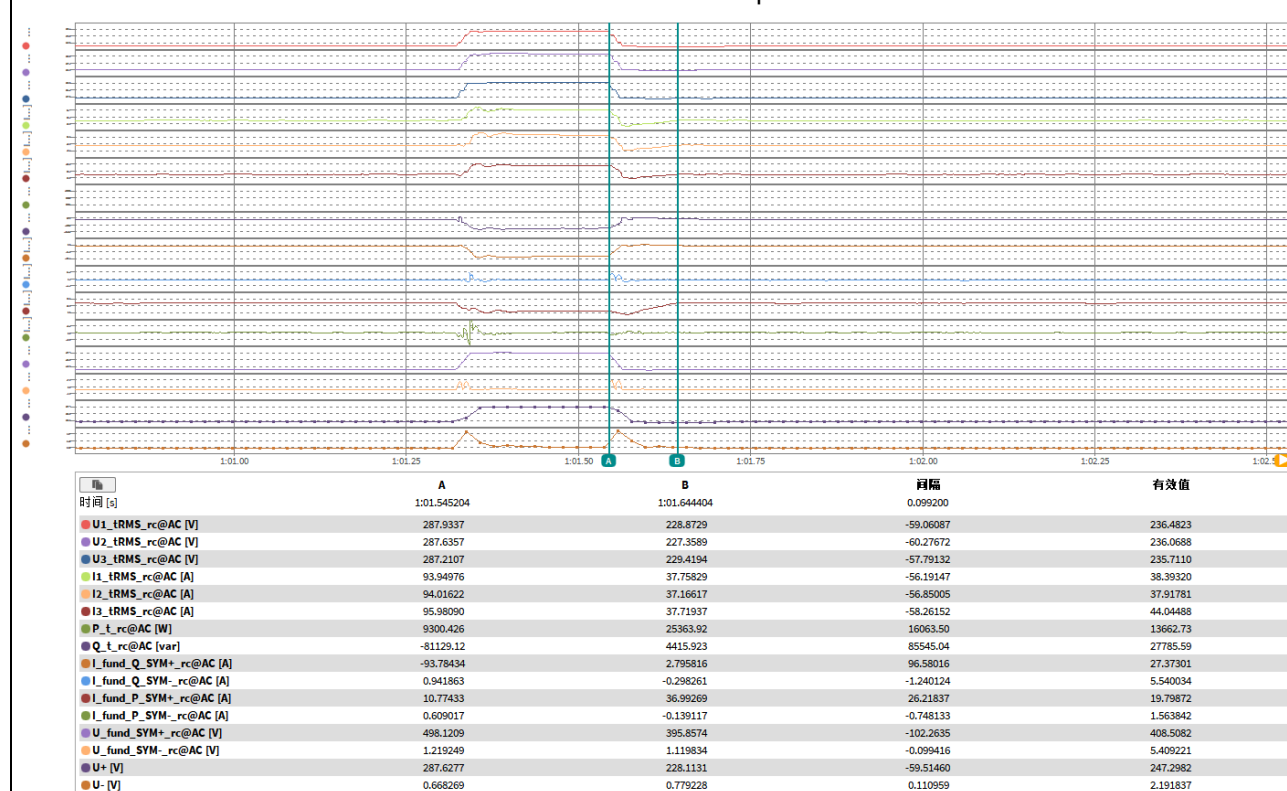
Injected reactive current at 30ms



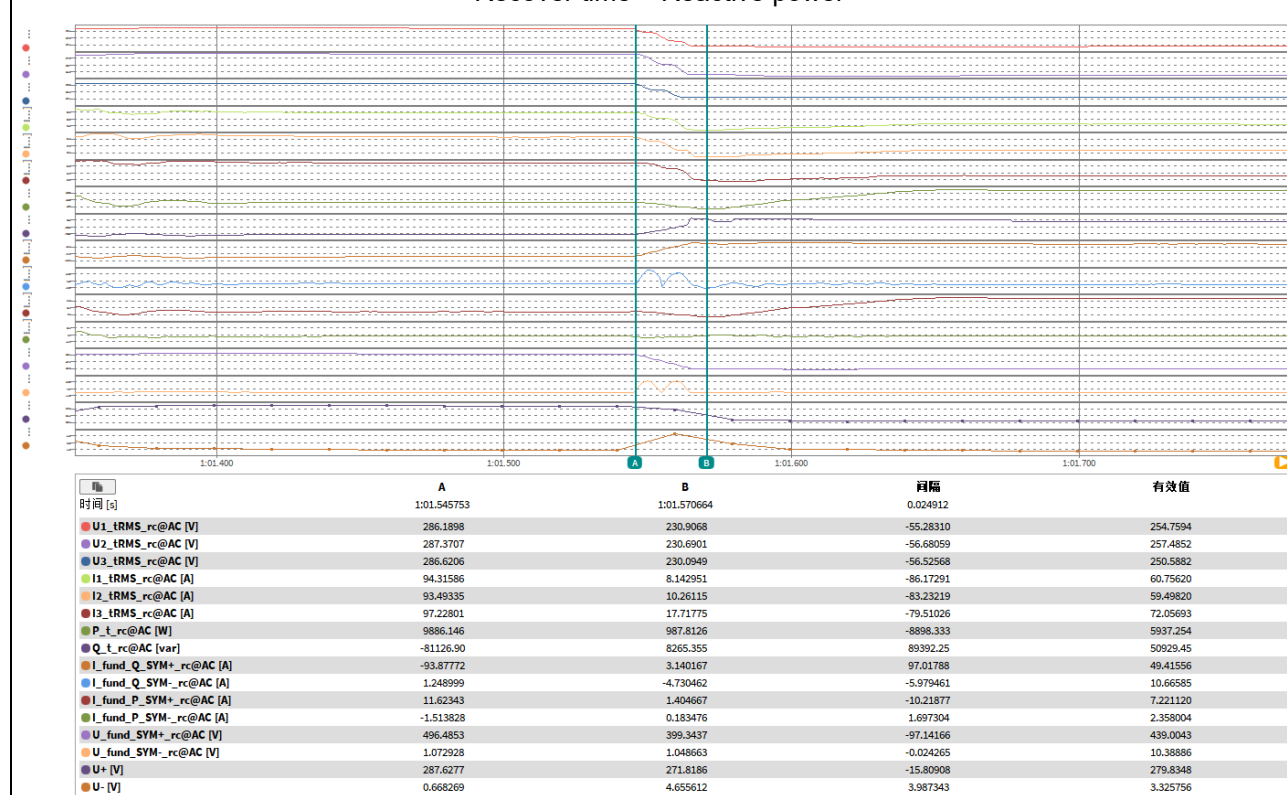
Injected reactive current at 60ms



Recover time – Active power

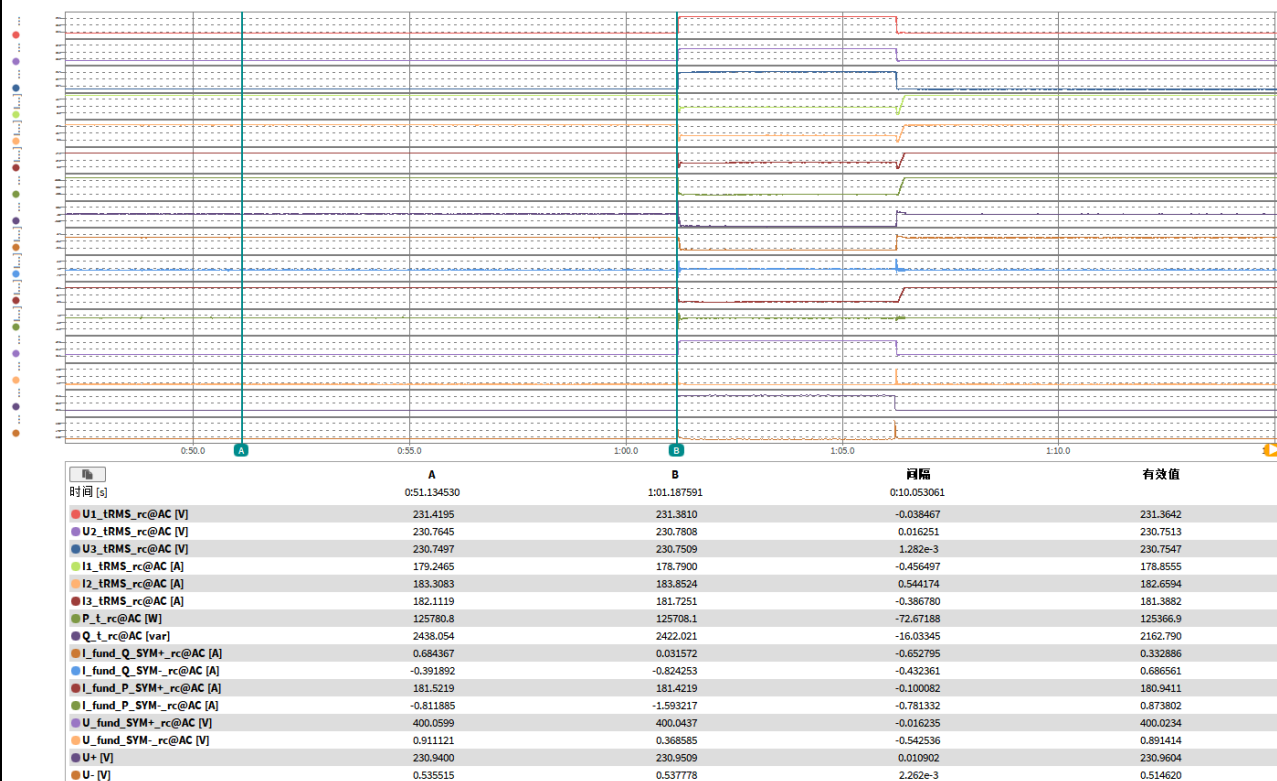


Recover time – Reactive power

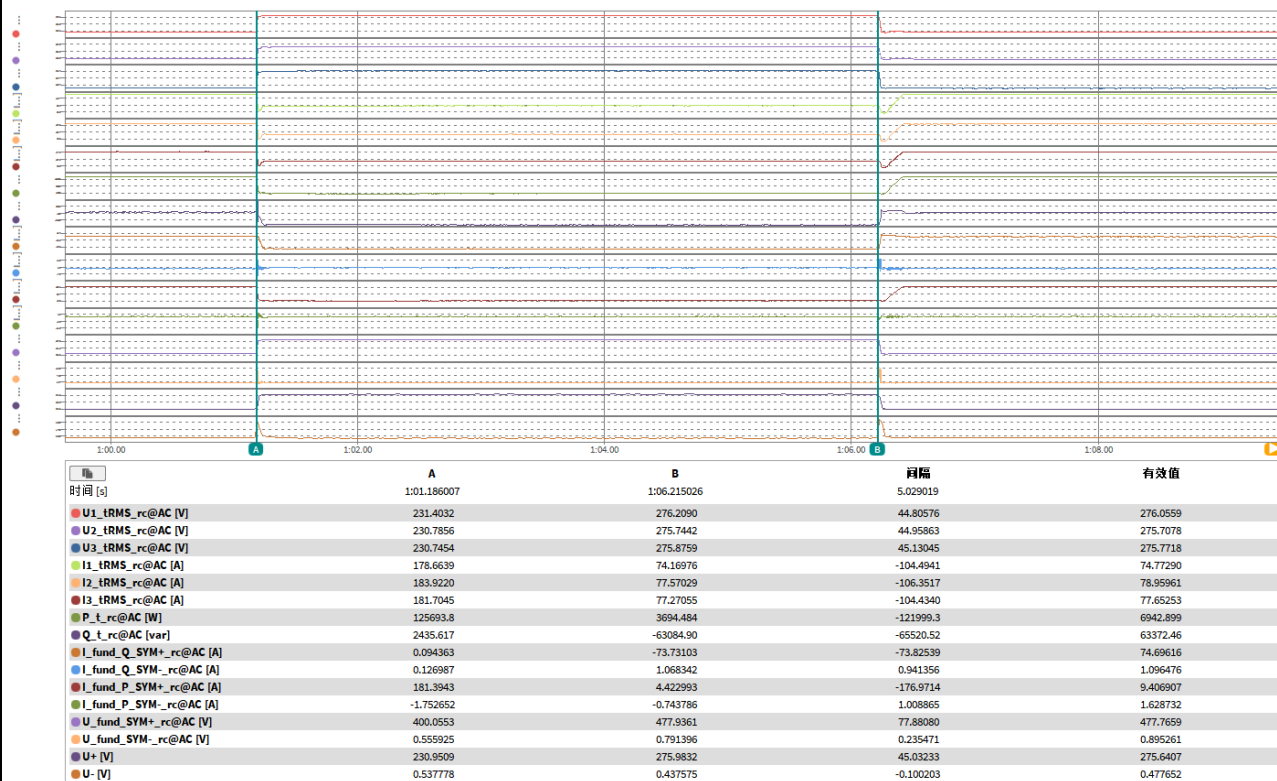


Test – VS2.1.1.a

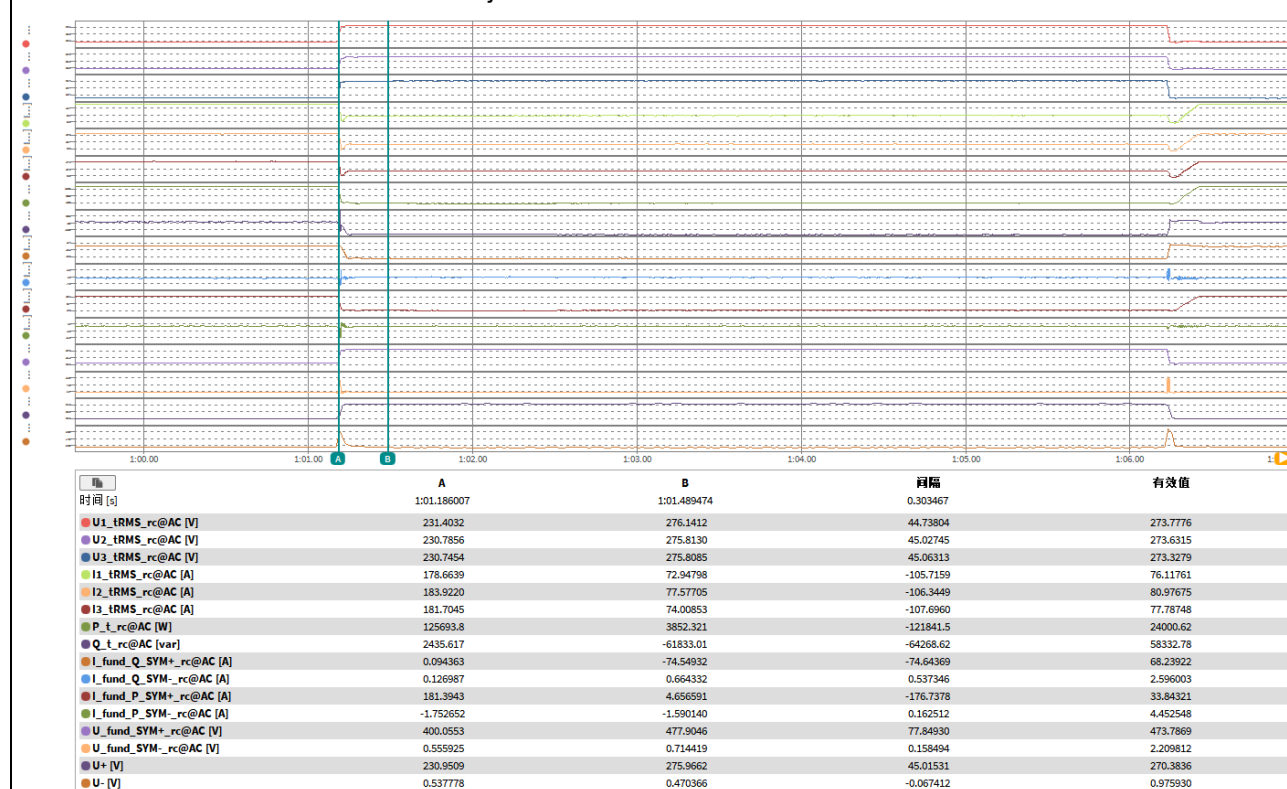
Before fault



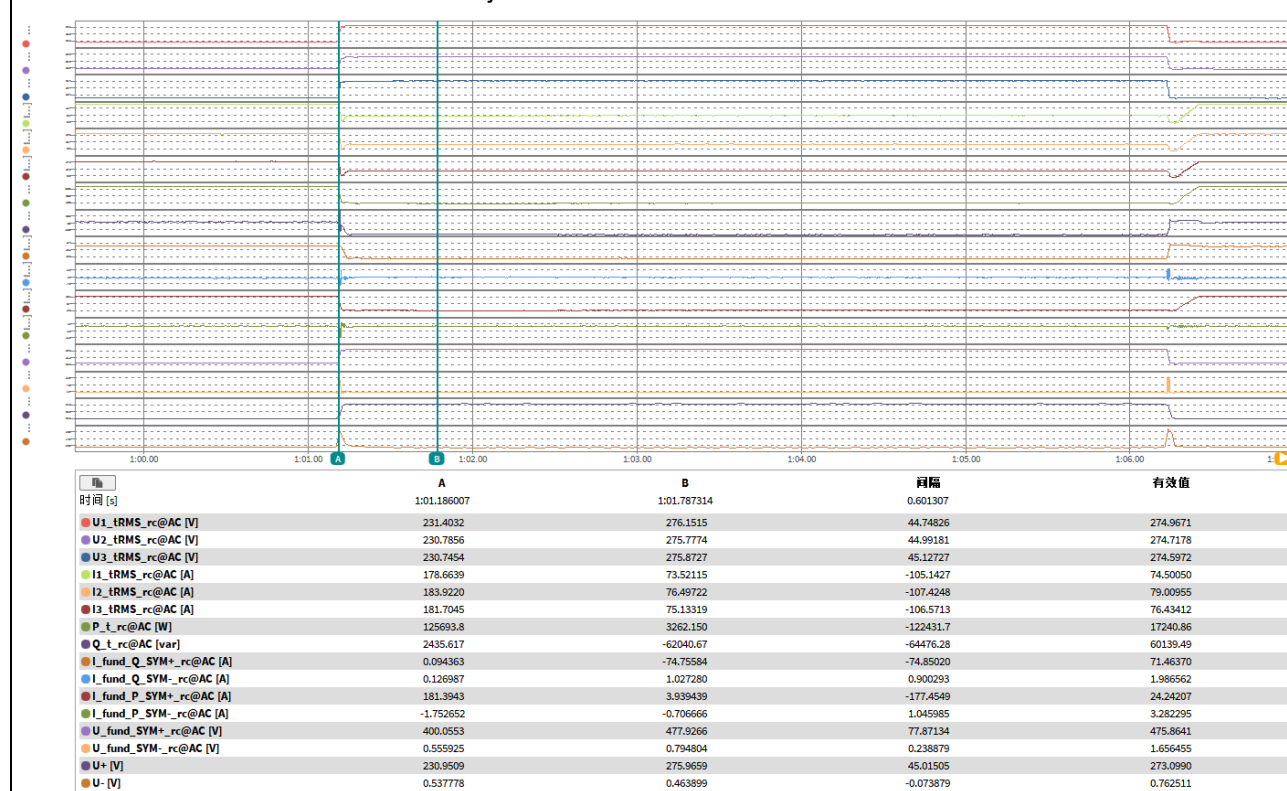
Duration fault time



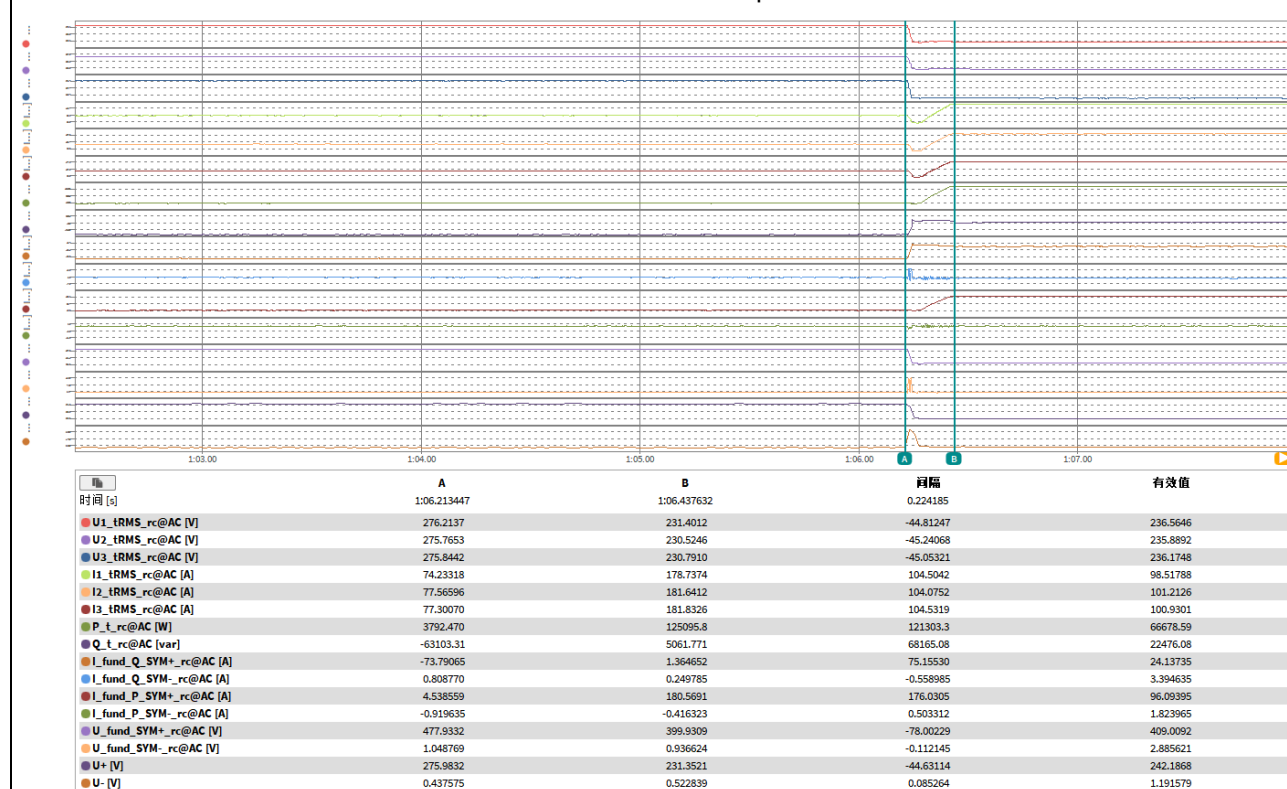
Injected reactive current at 30ms



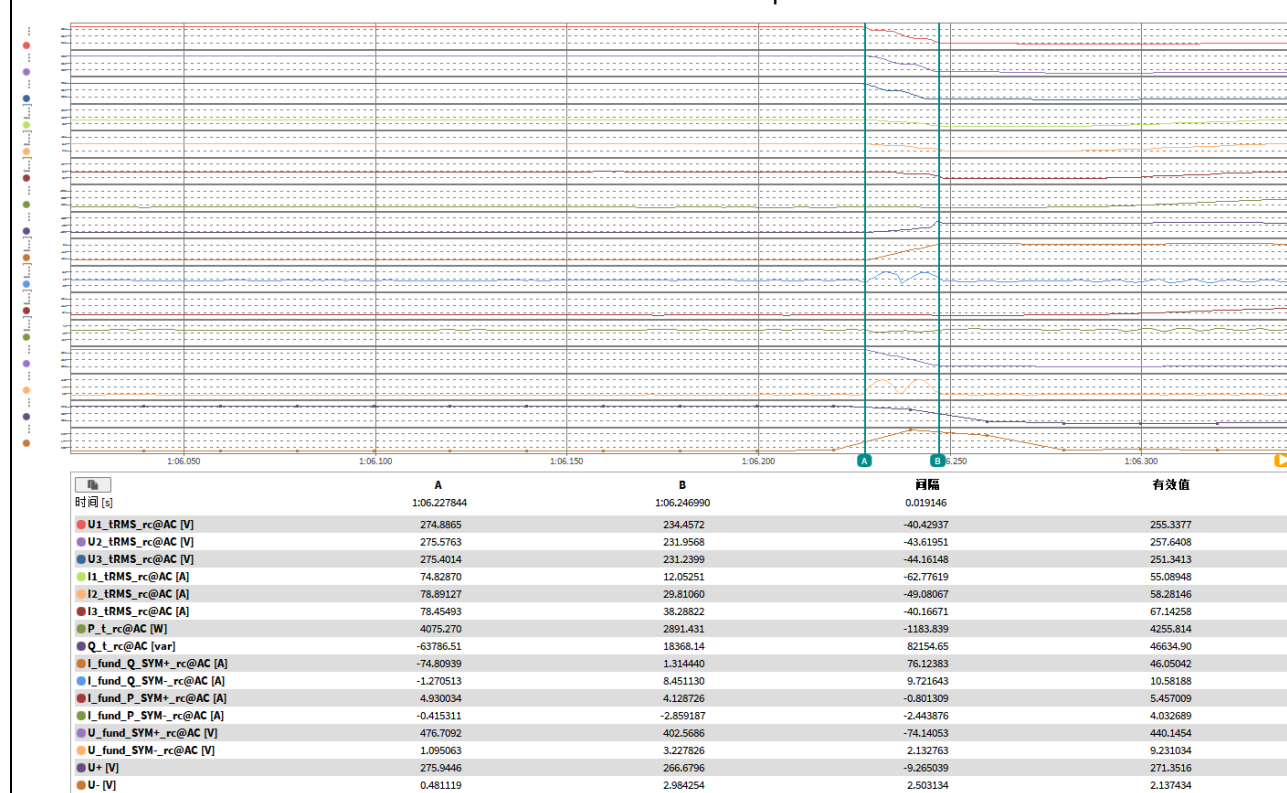
Injected reactive current at 60ms



Recover time – Active power

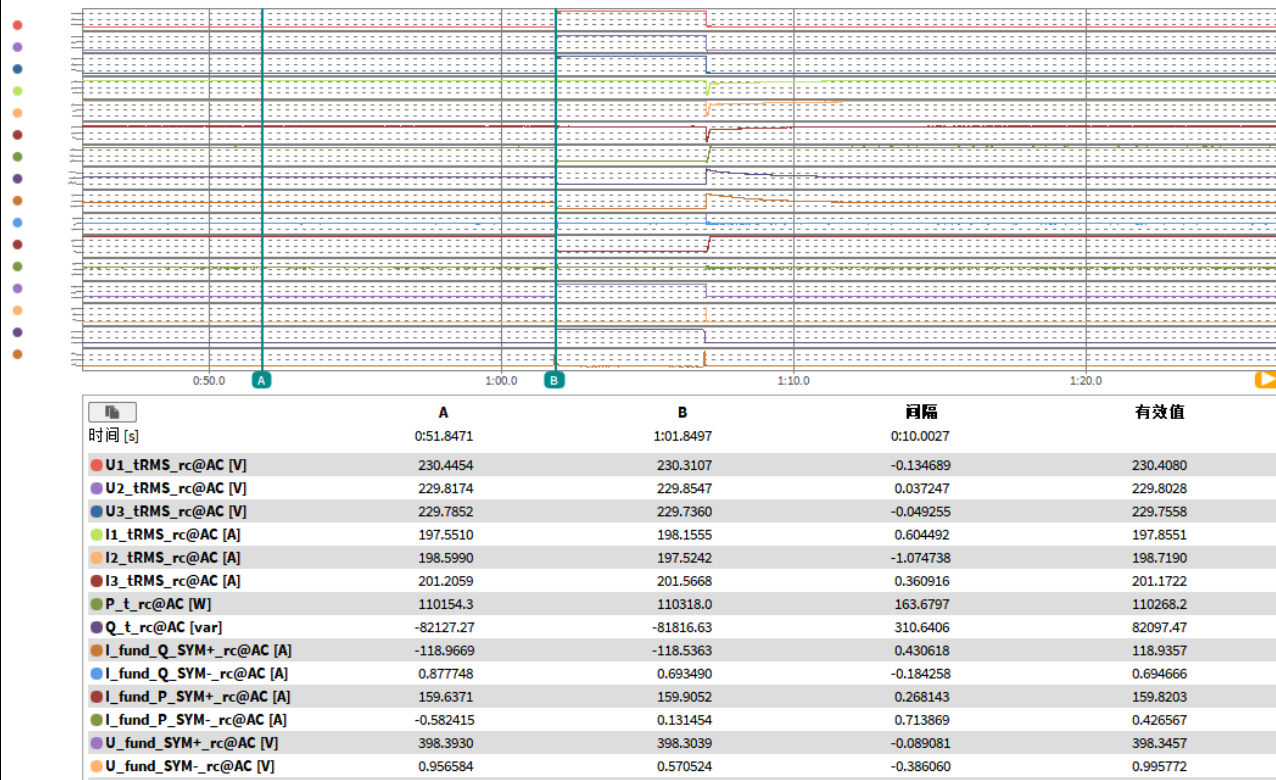


Recover time – Reactive power

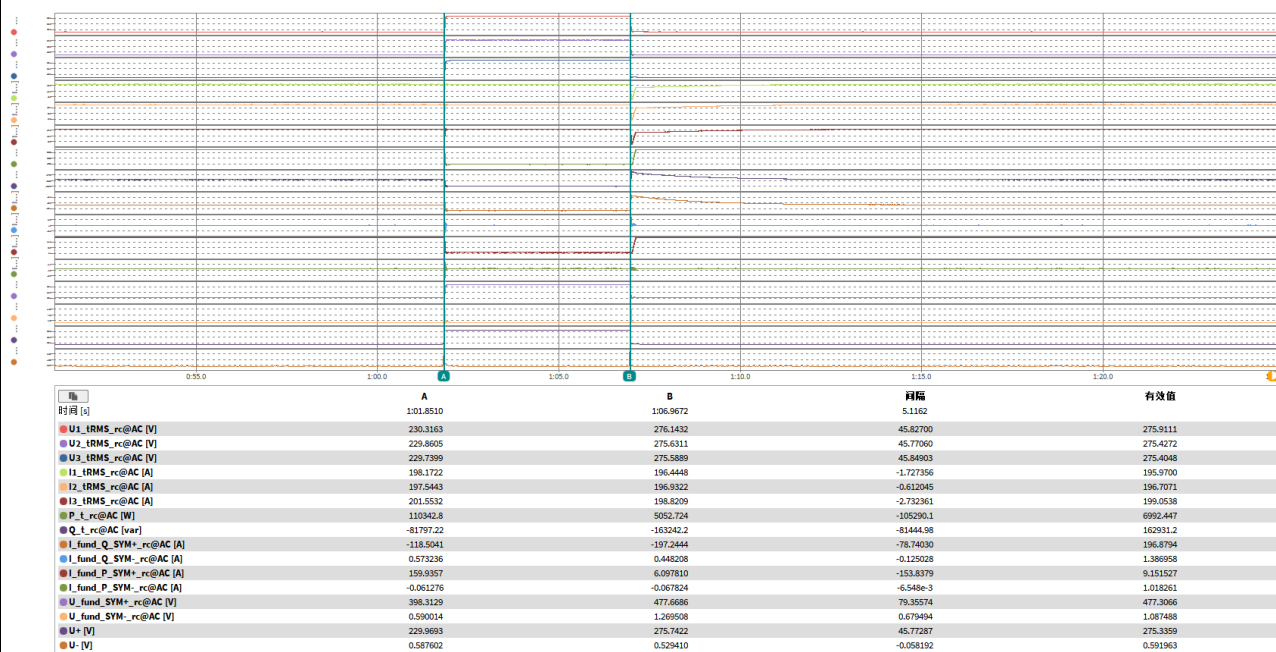


Test – VS2.1.1.b

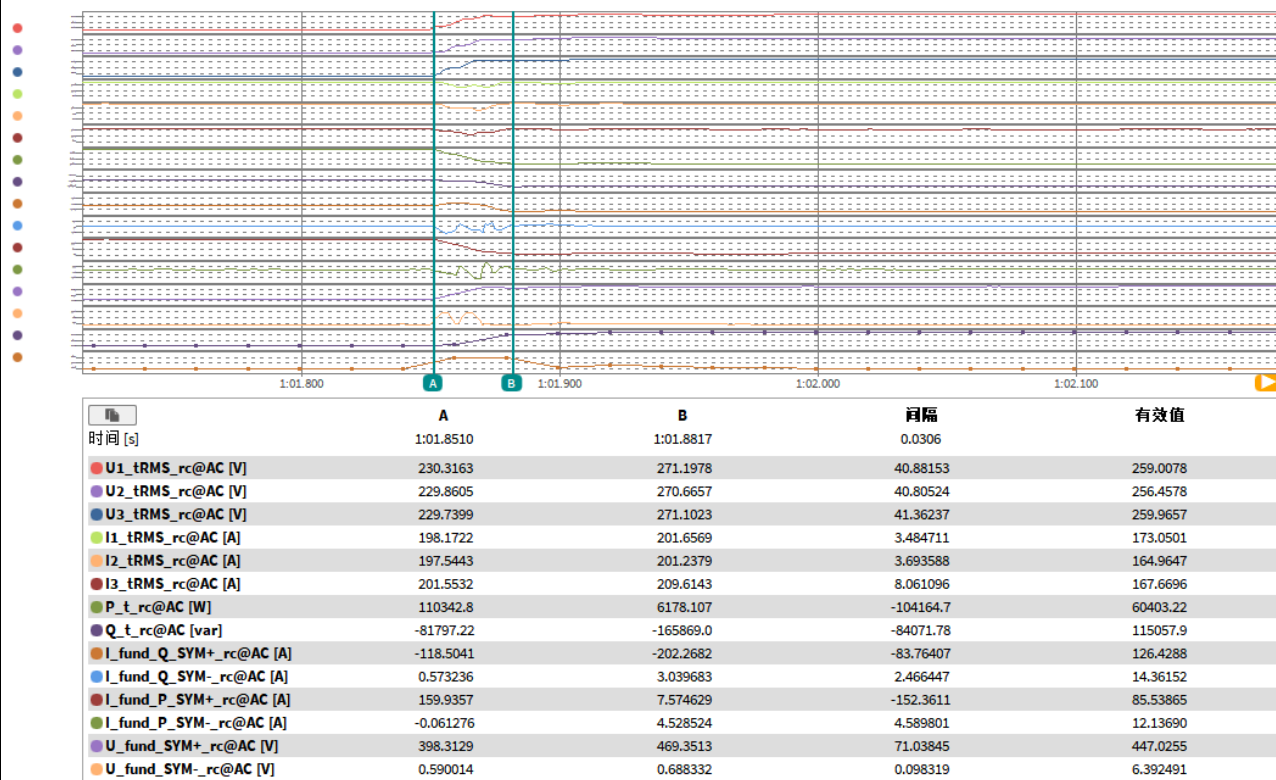
Before fault



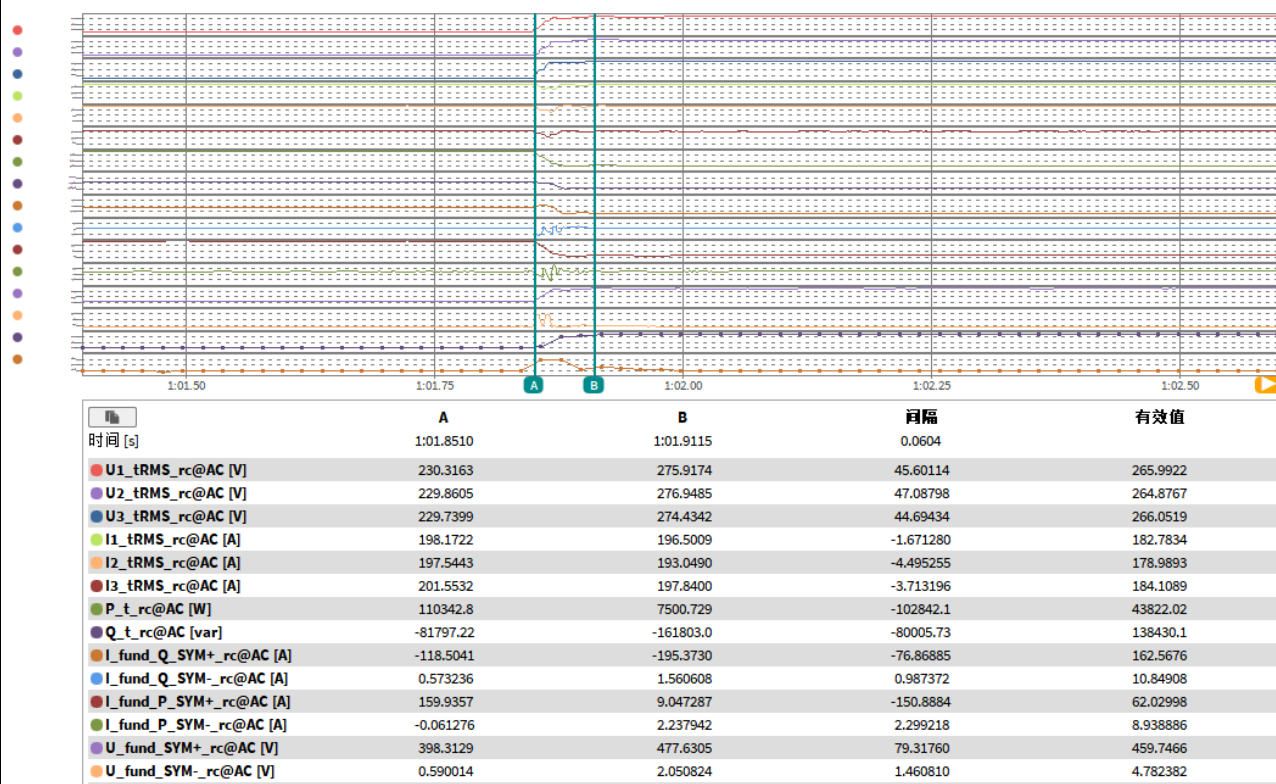
Duration fault time



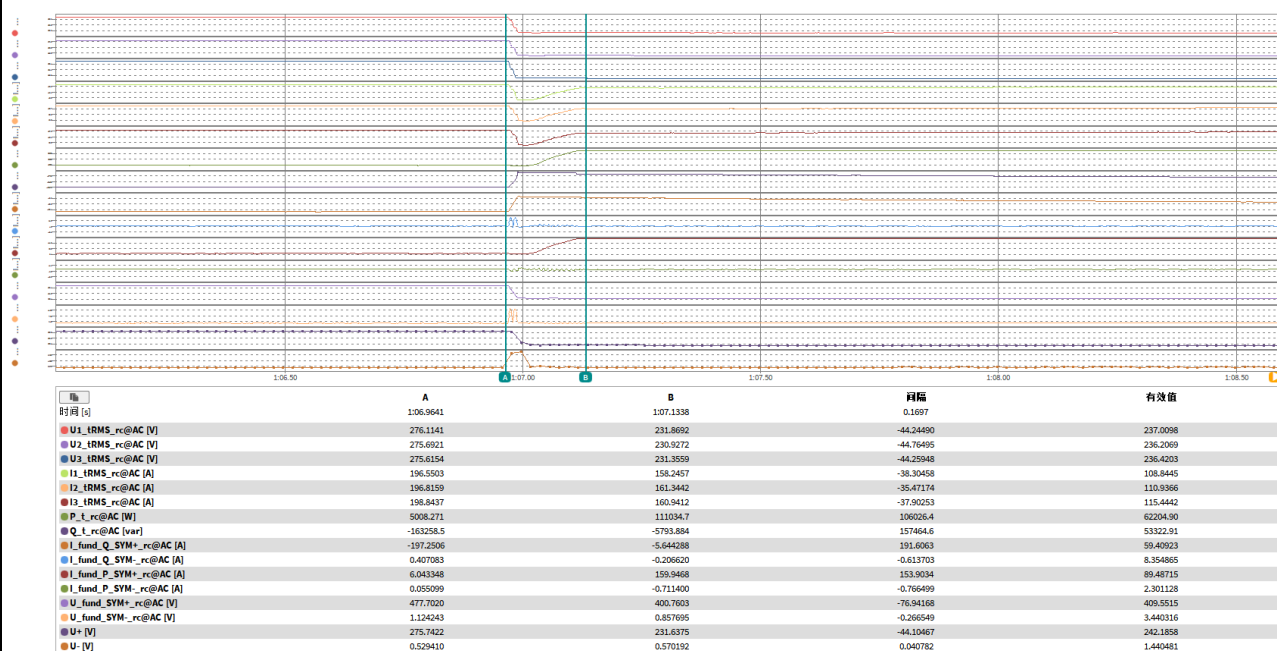
Injected reactive current at 30ms



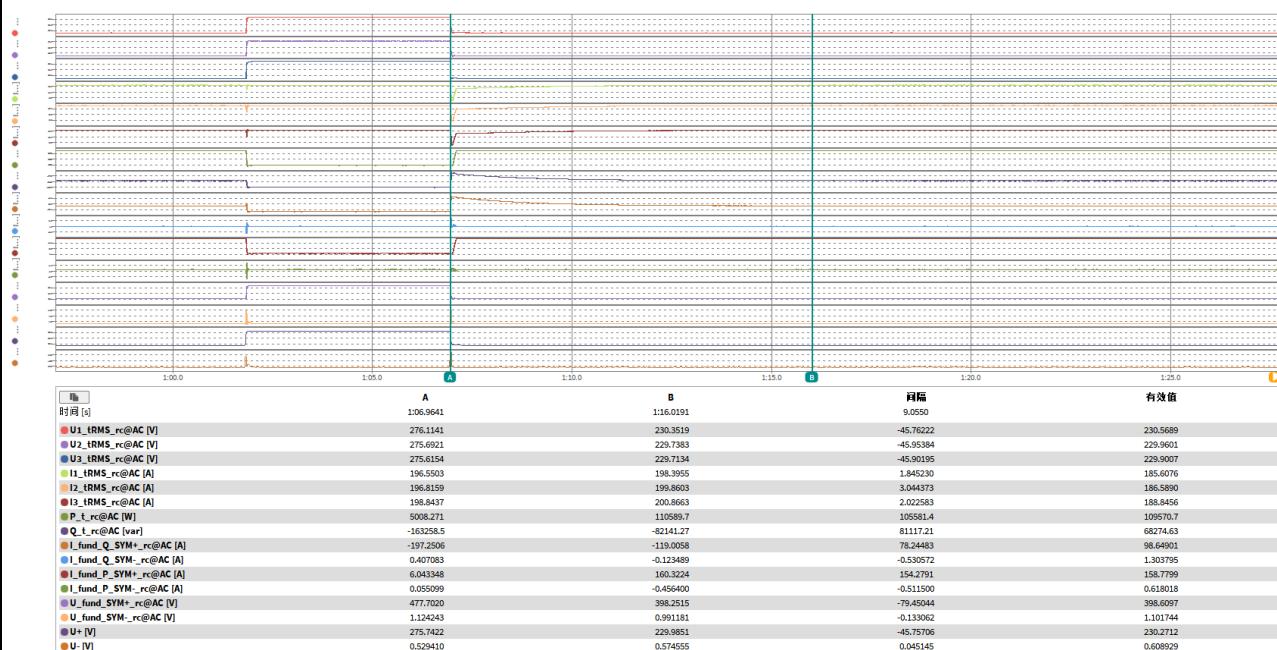
Injected reactive current at 60ms



Recover time – Active power

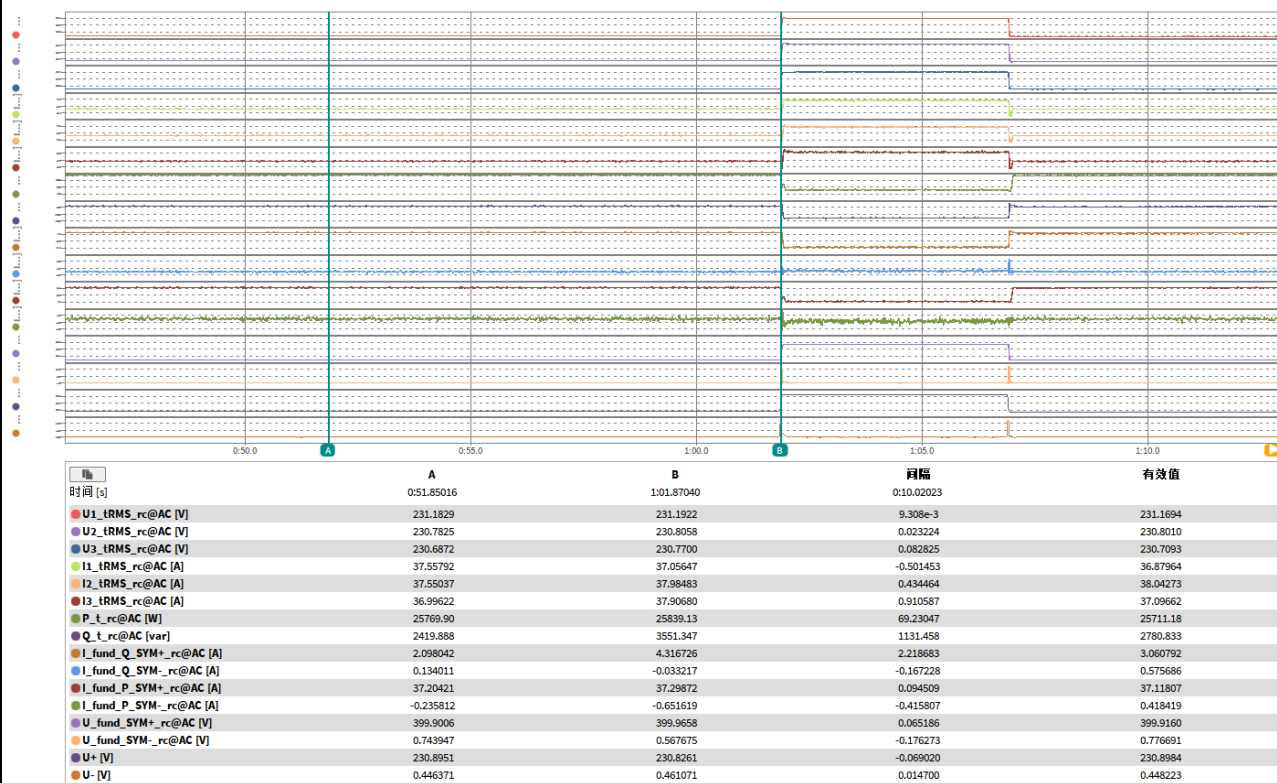


Recover time – Reactive power

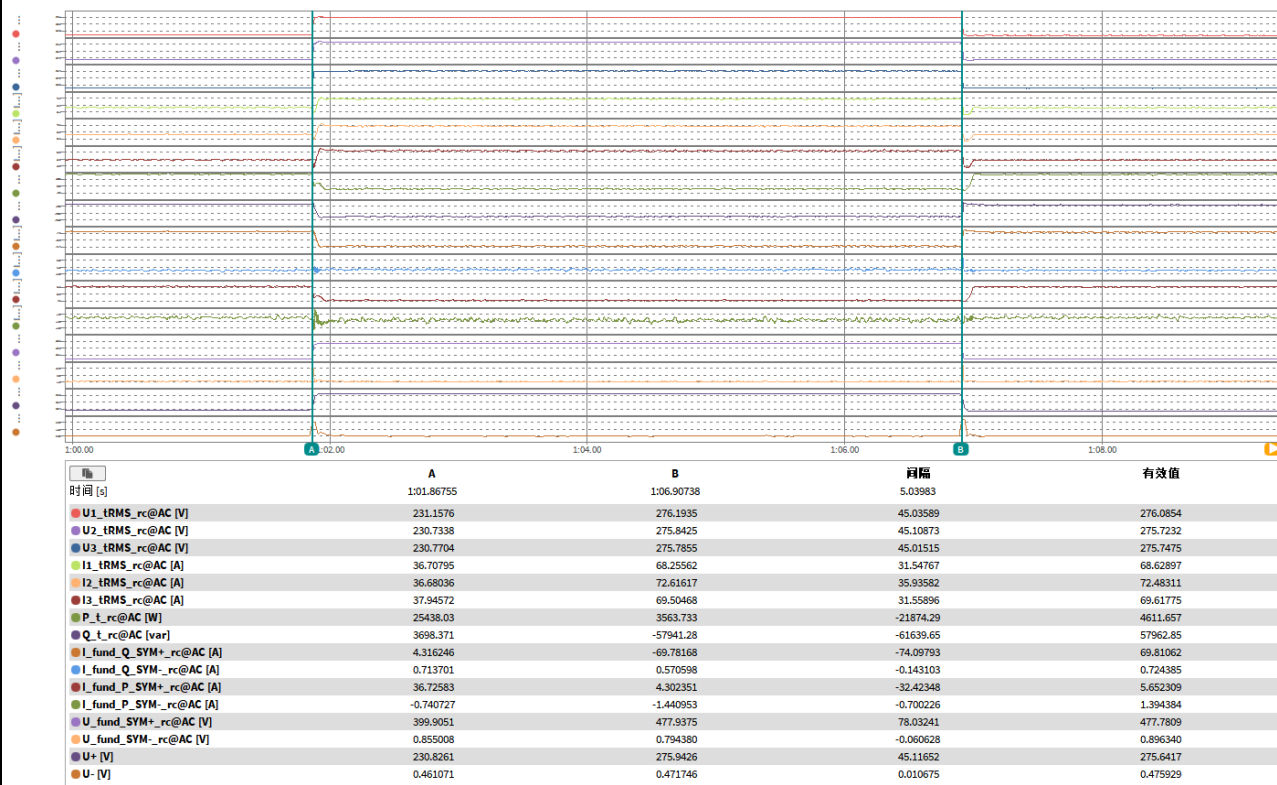


Test – VS2.1.2

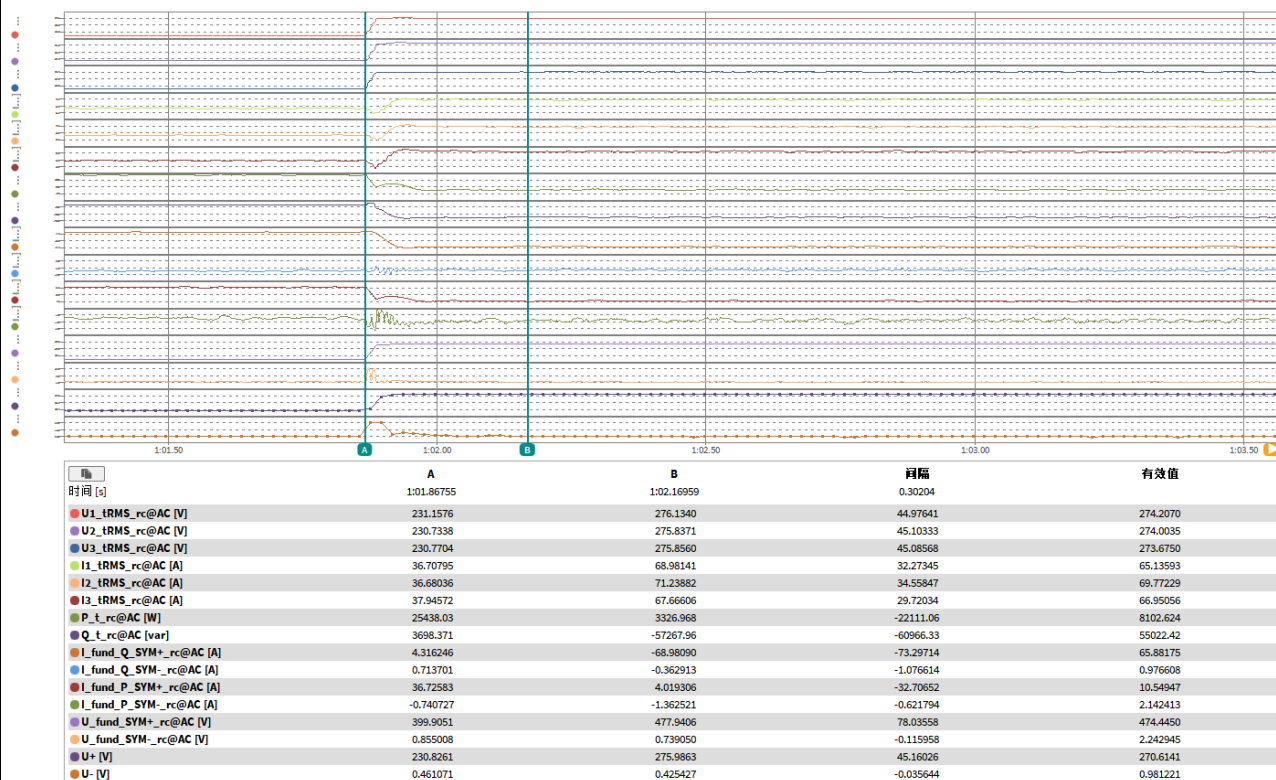
Before fault



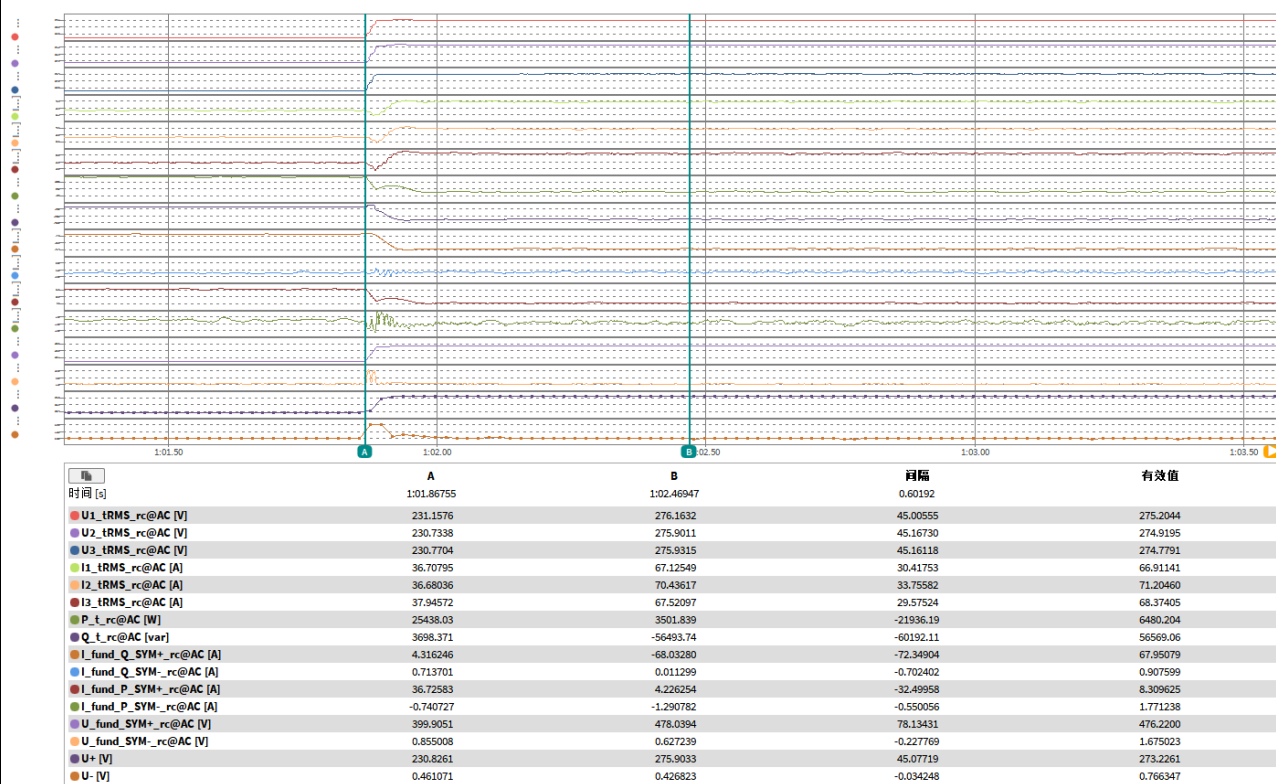
Duration fault time



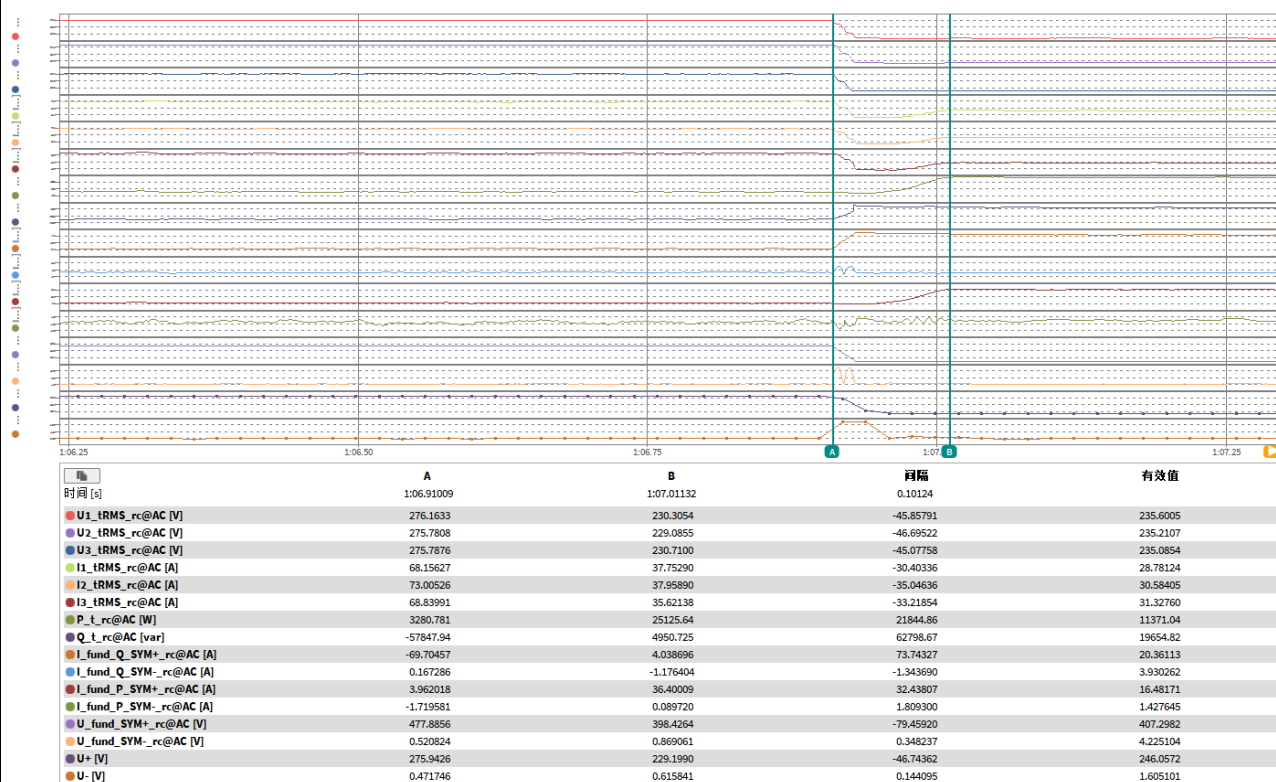
Injected reactive current at 30ms



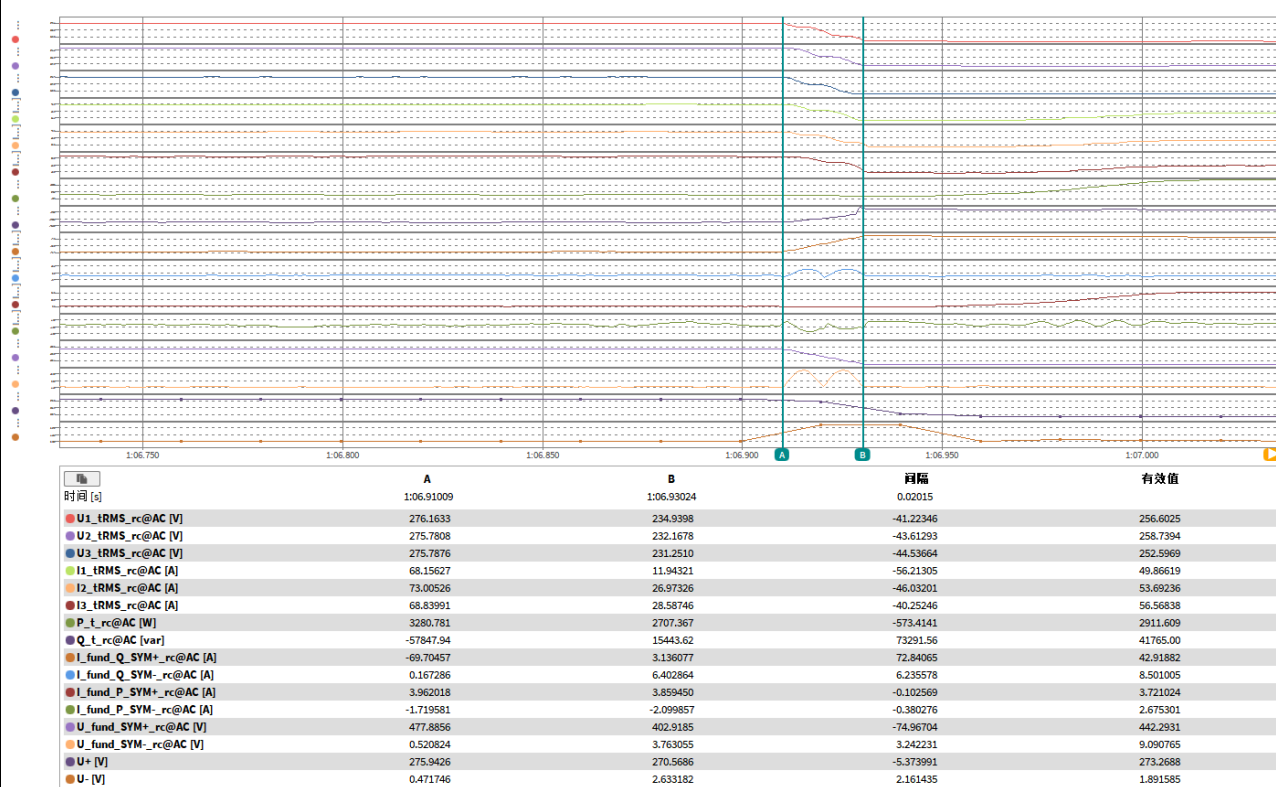
Injected reactive current at 60ms



Recover time – Active power

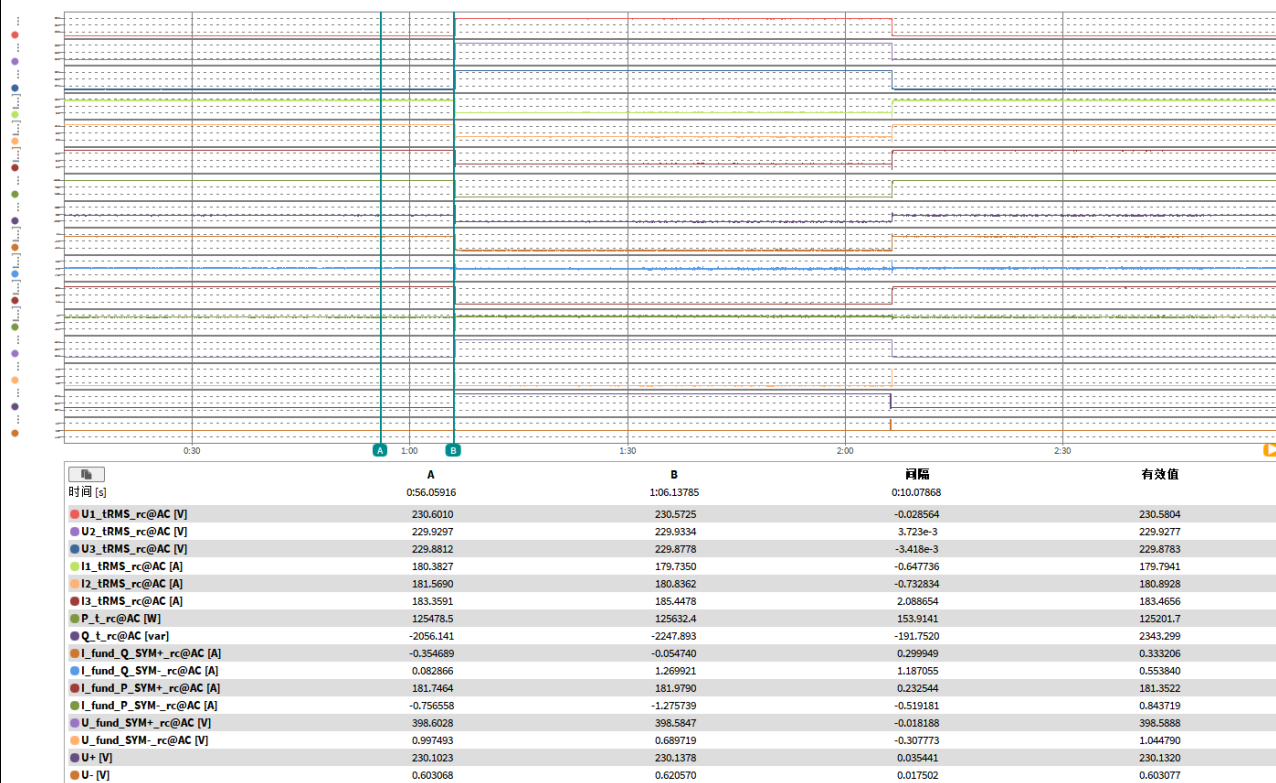


Recover time – Reactive power

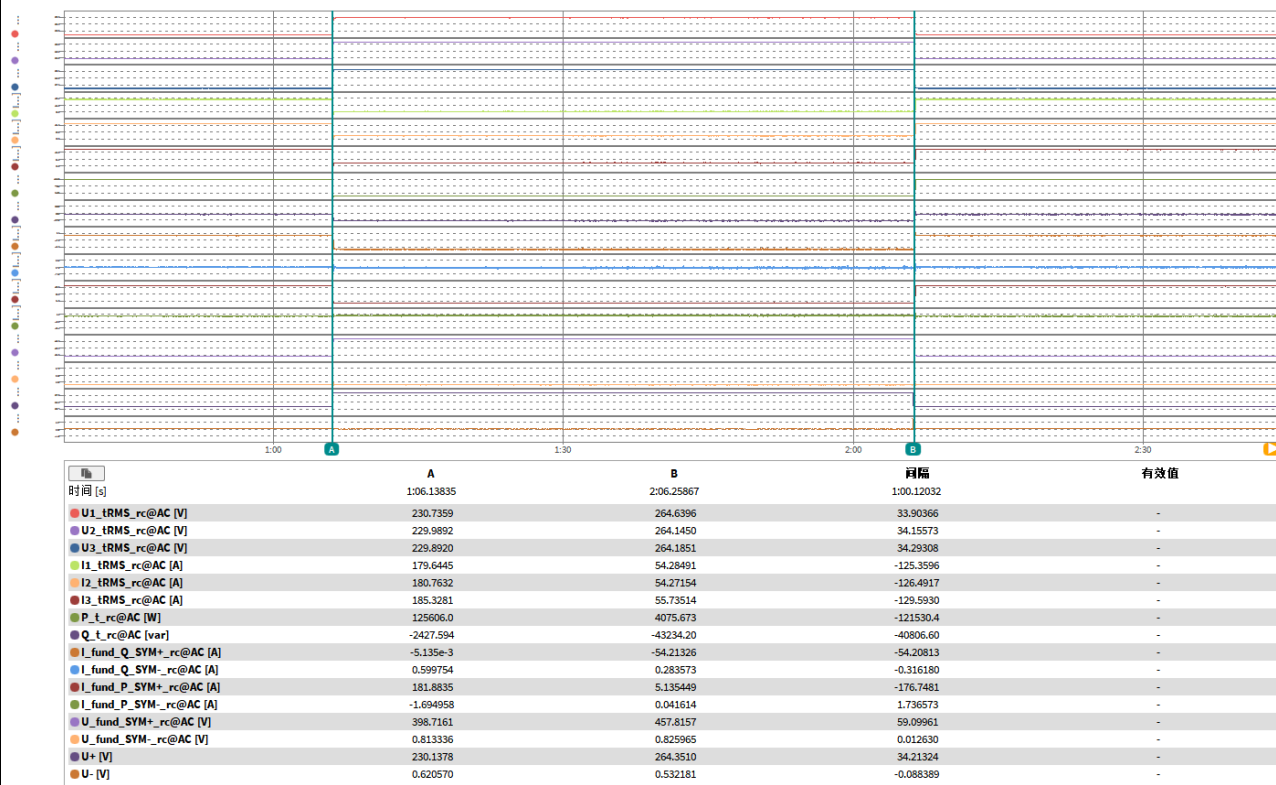


Test – VS2.2.1

Before fault



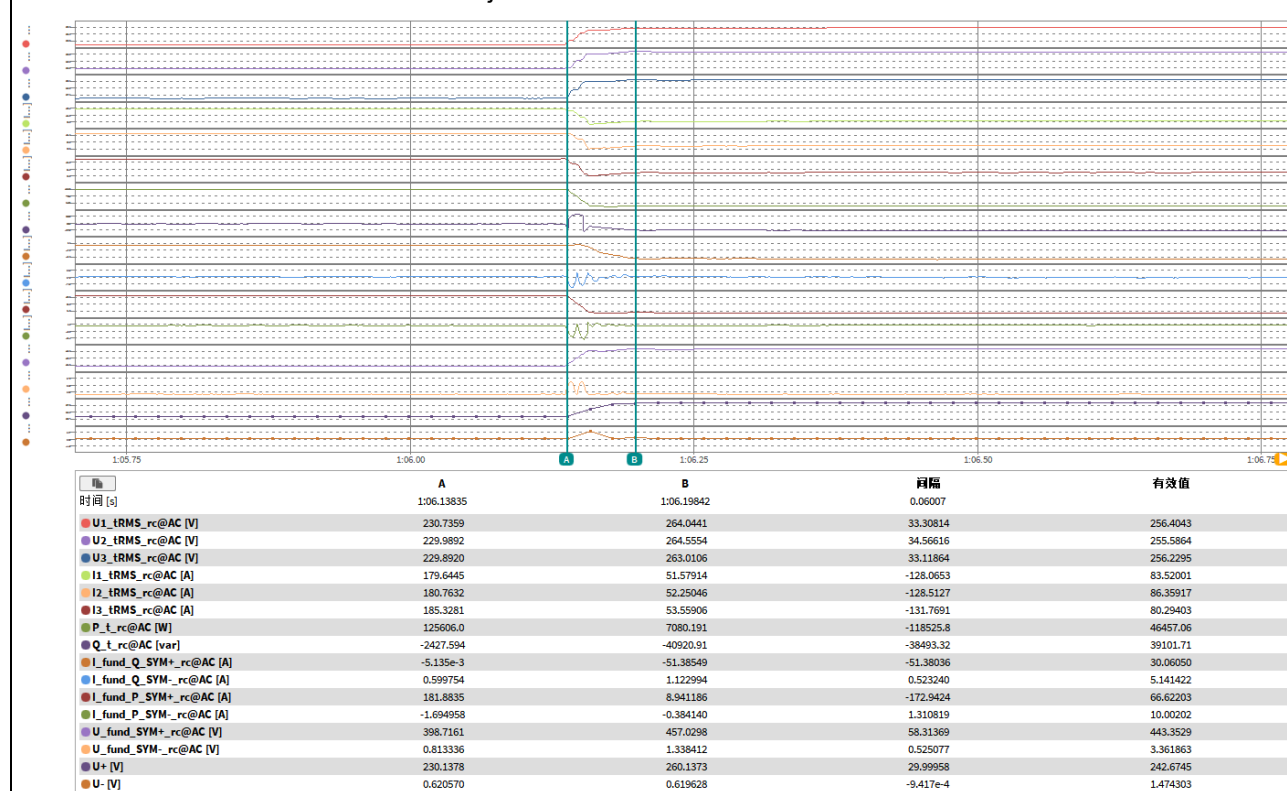
Duration fault time



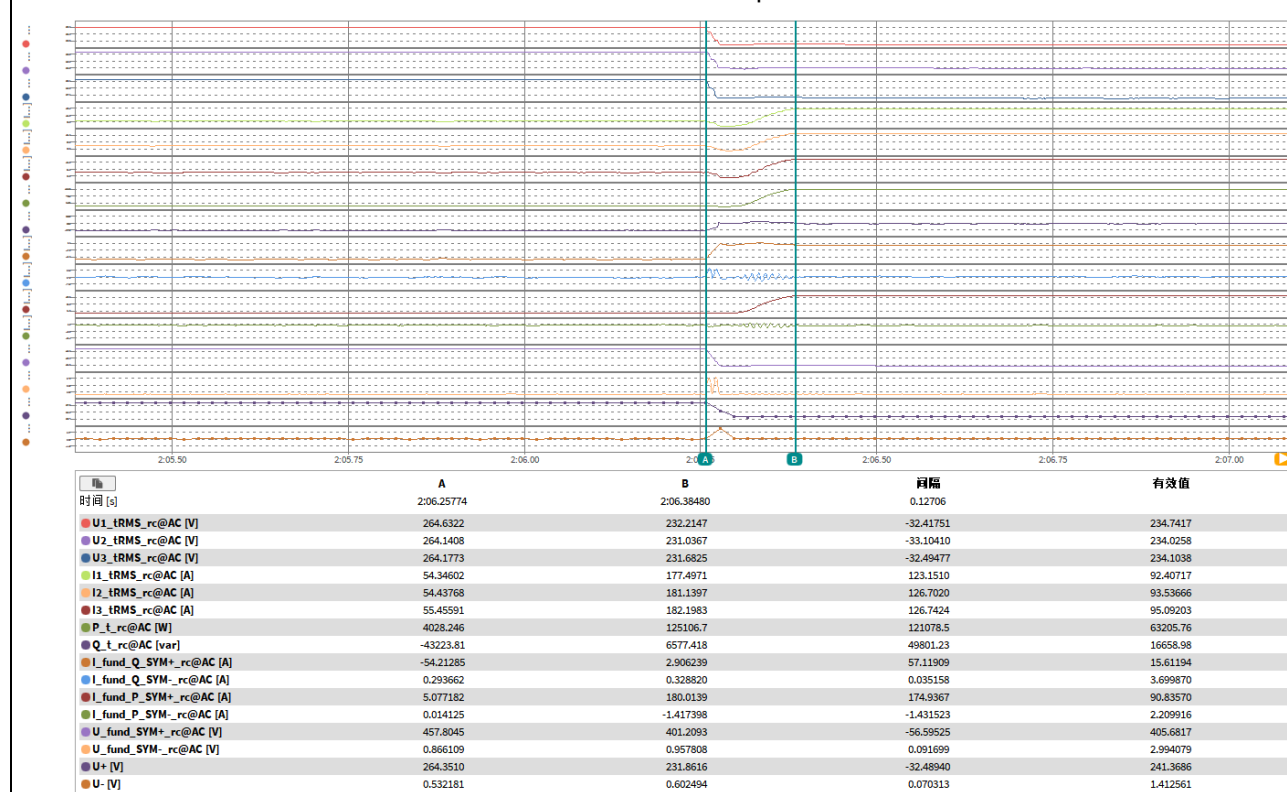
Injected reactive current at 30ms



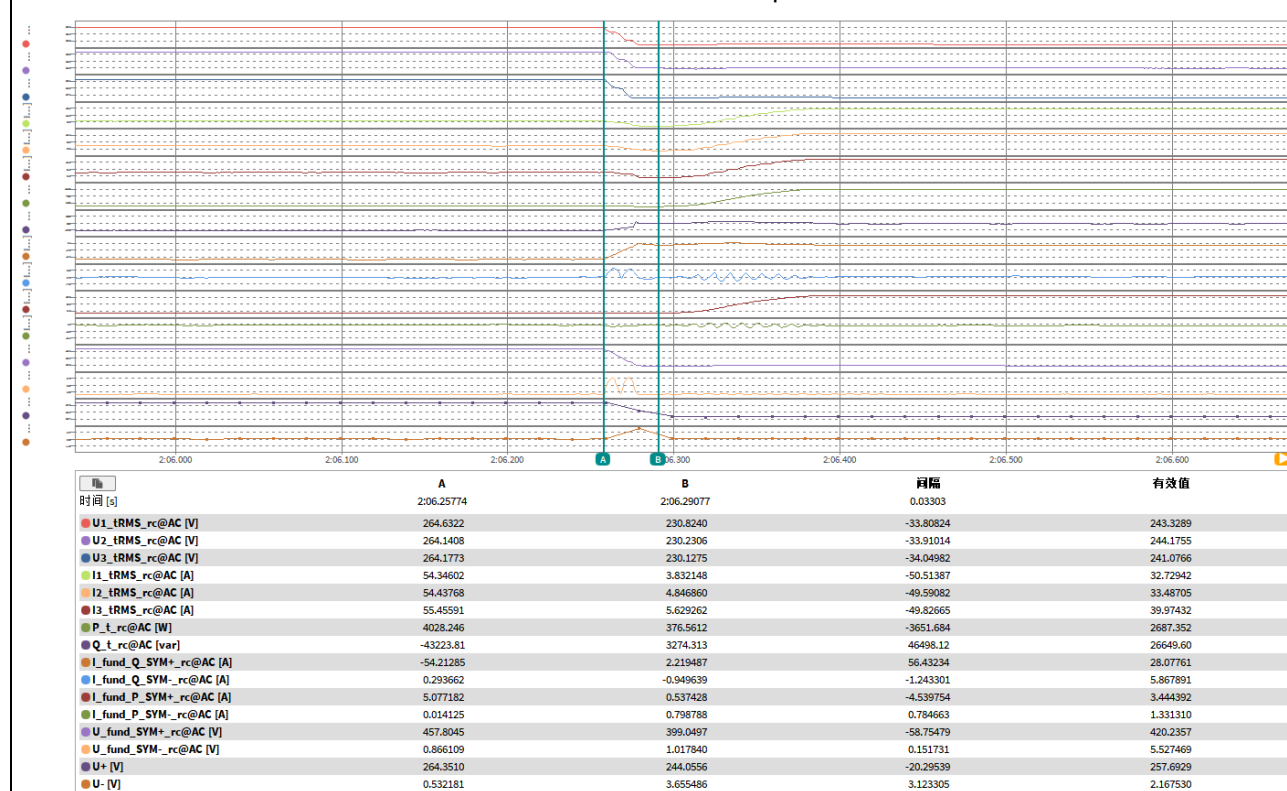
Injected reactive current at 60ms



Recover time – Active power

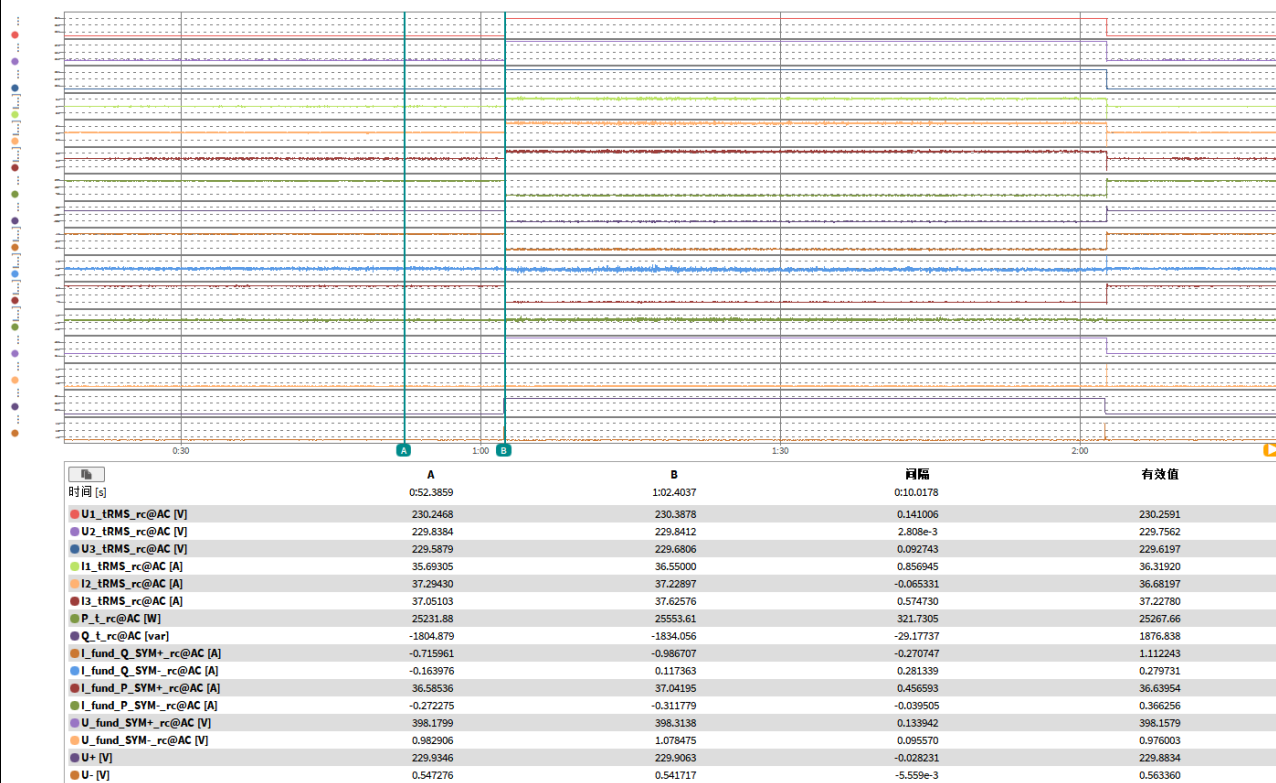


Recover time – Reactive power

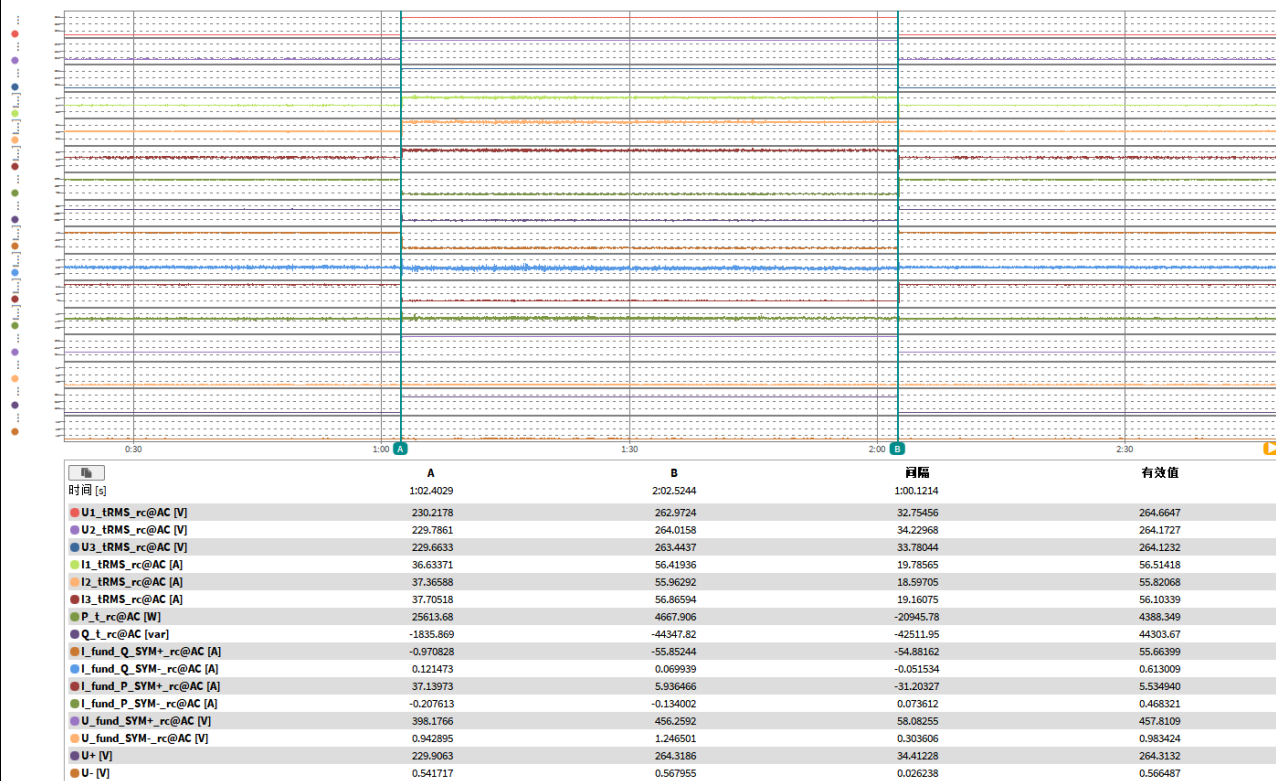


Test – VS2.2.2

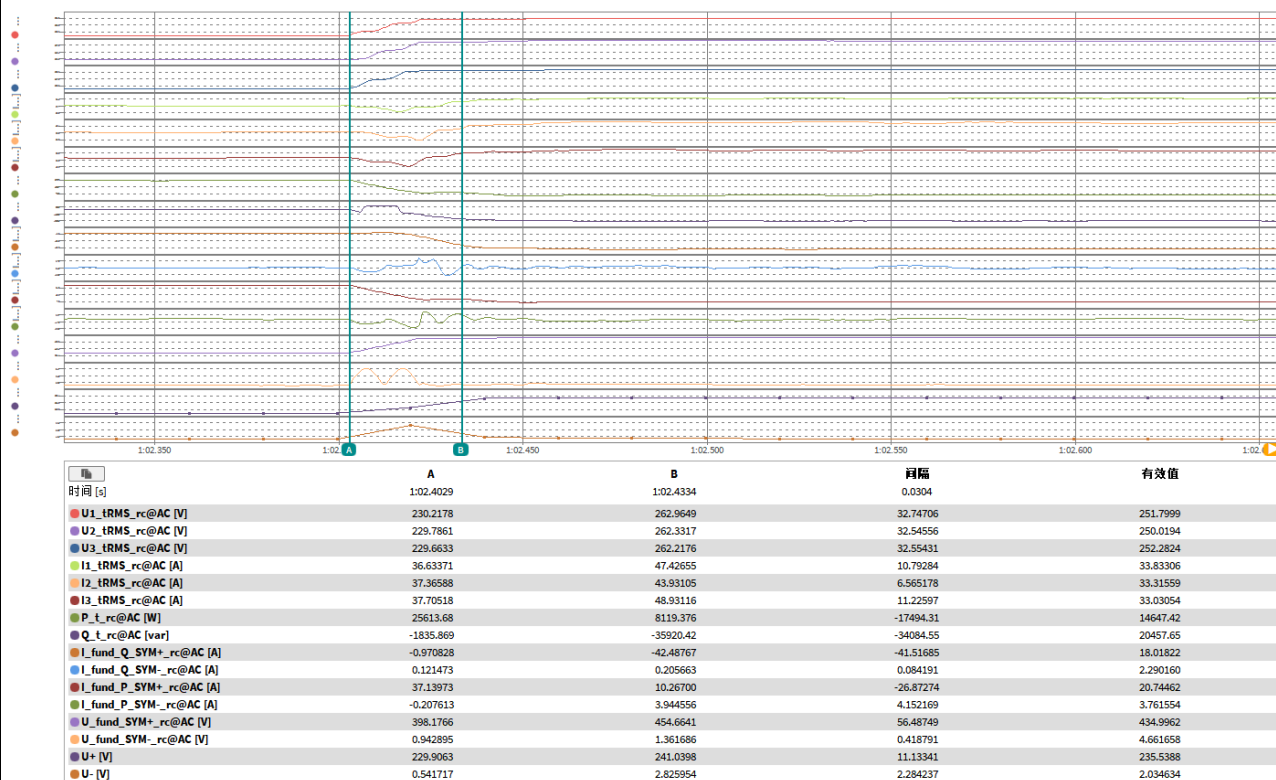
Before fault



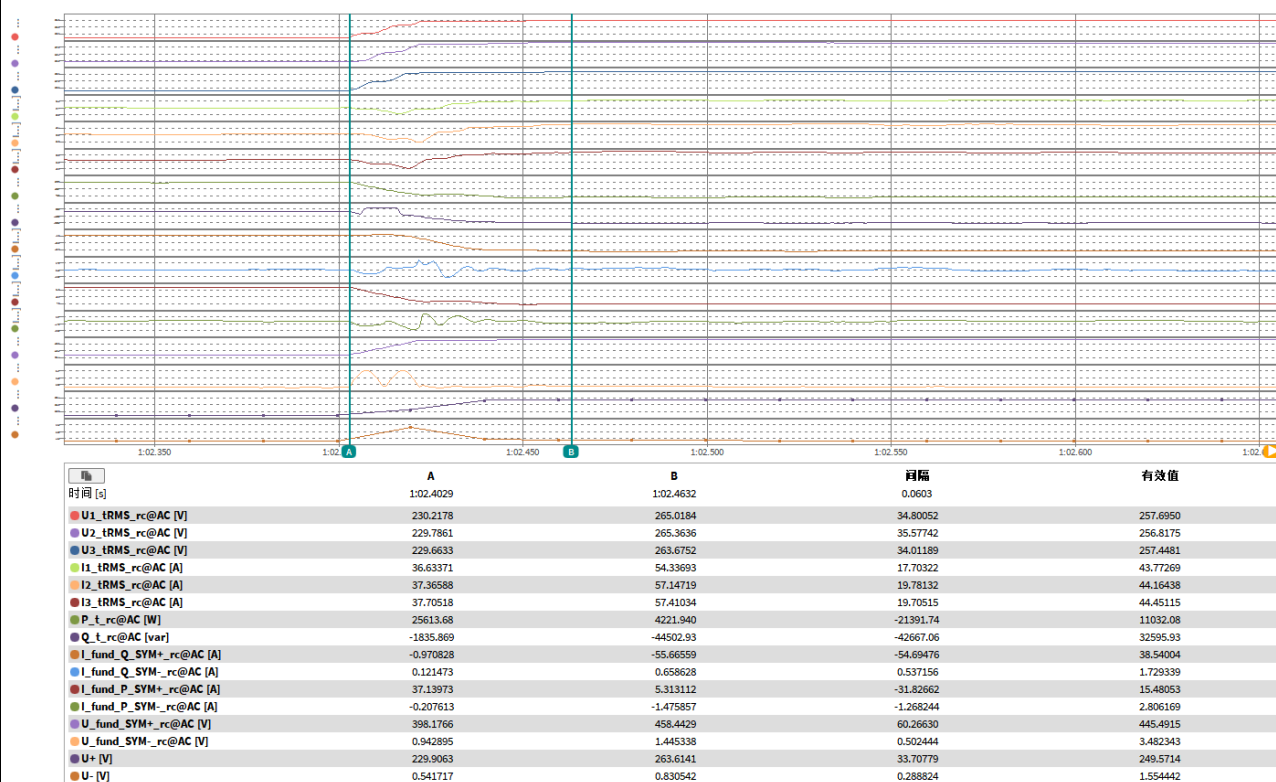
Duration fault time



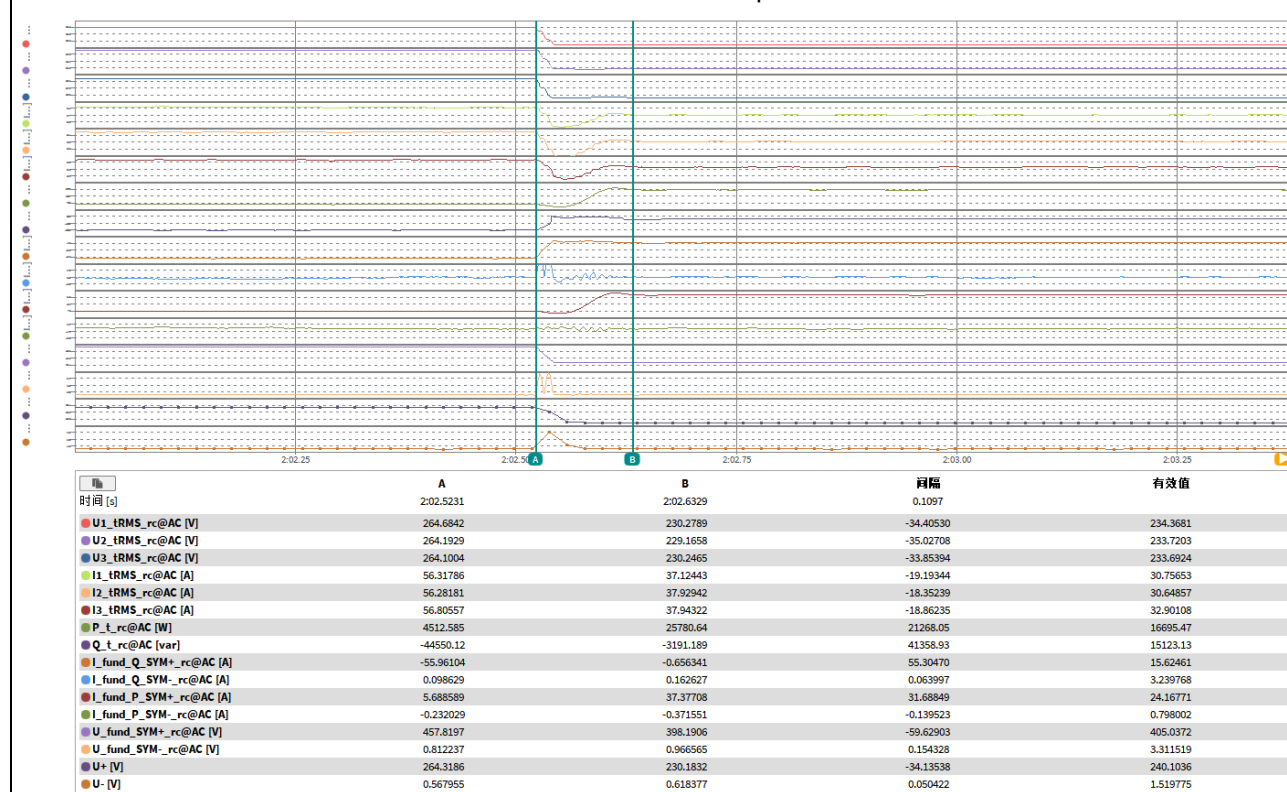
Injected reactive current at 30ms



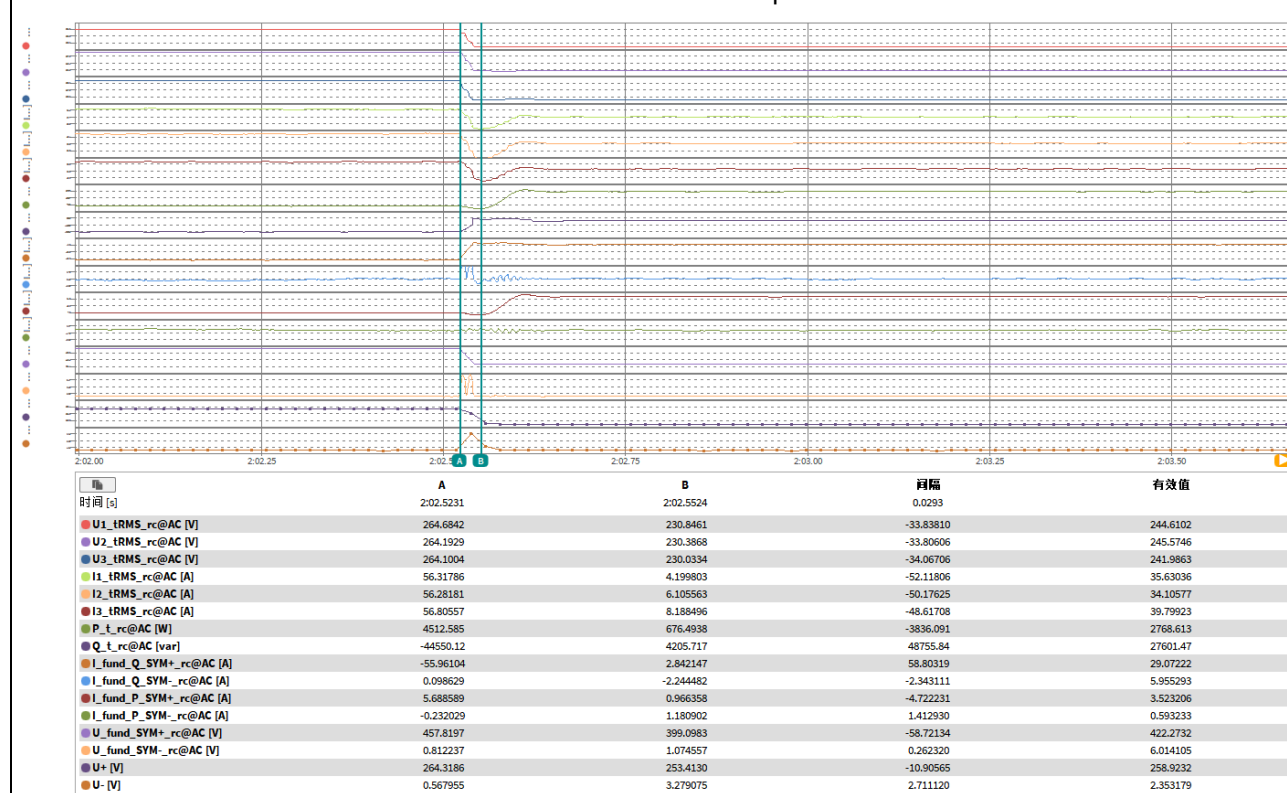
Injected reactive current at 60ms



Recover time – Active power

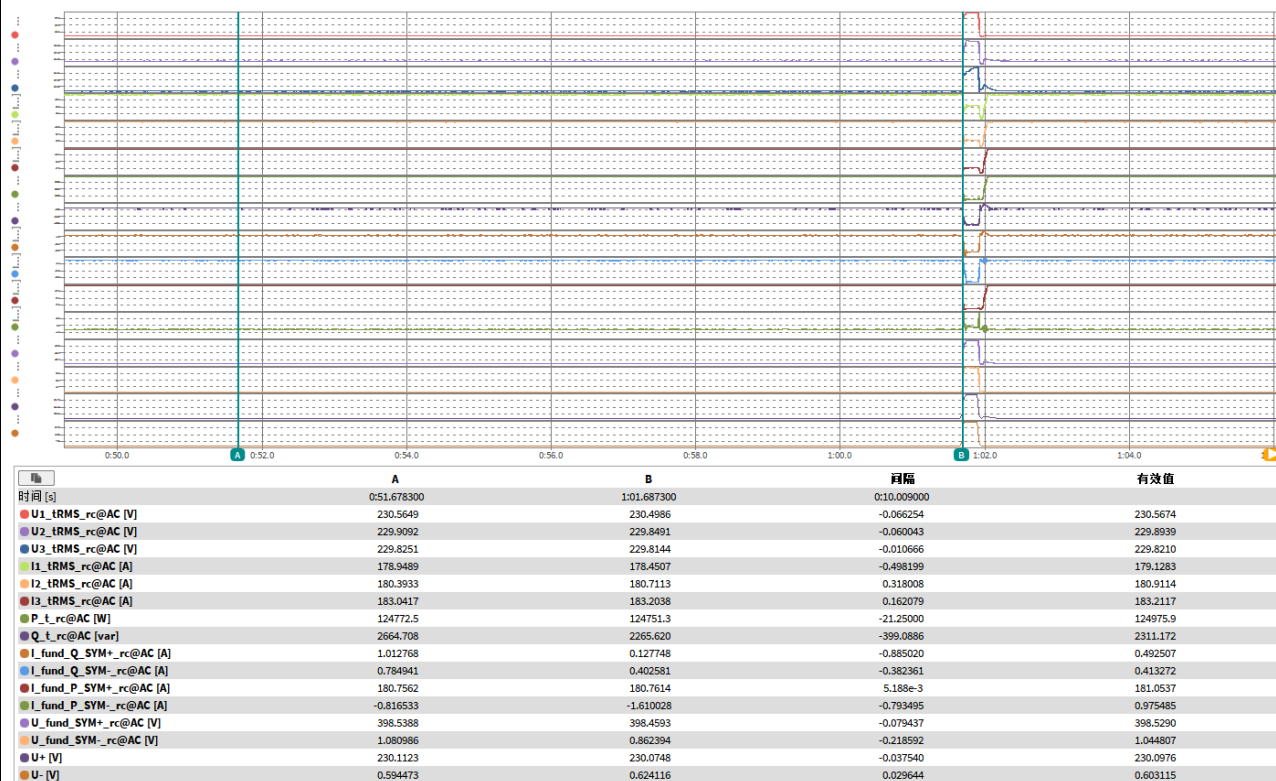


Recover time – Reactive power

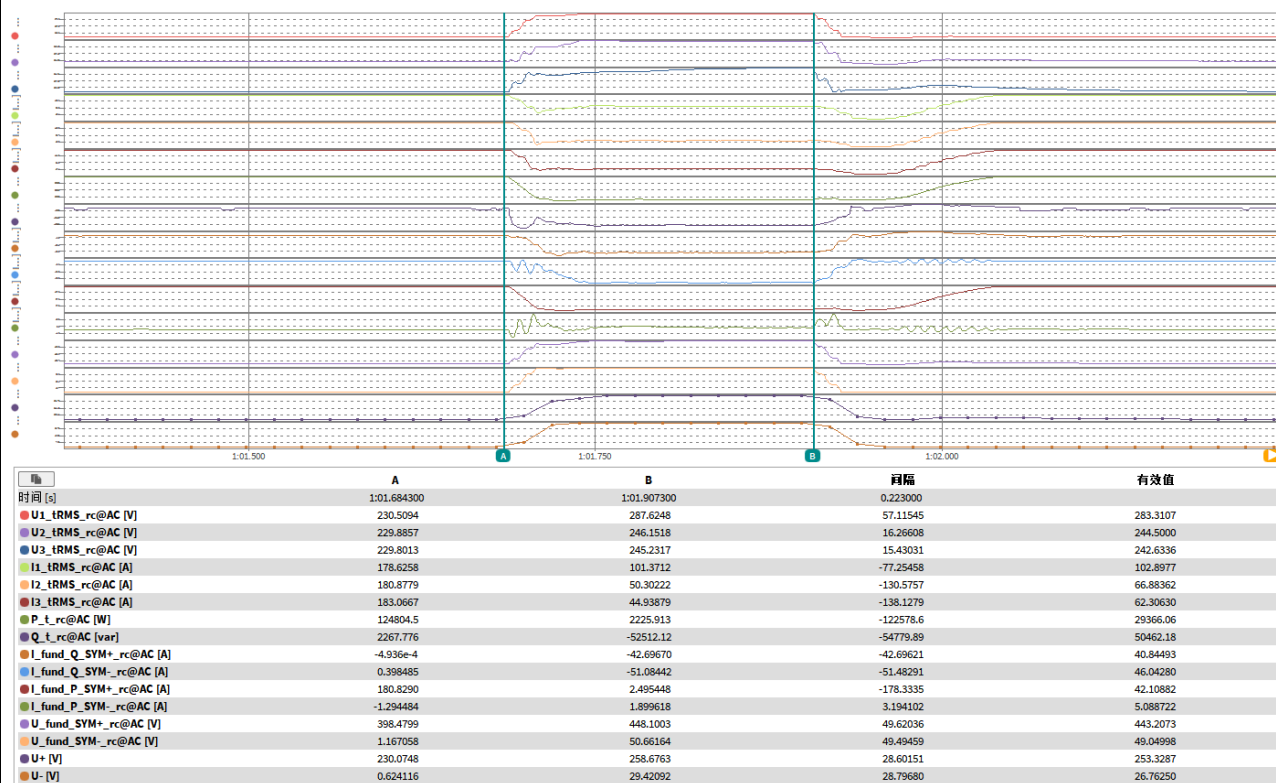


Test – VS3.1

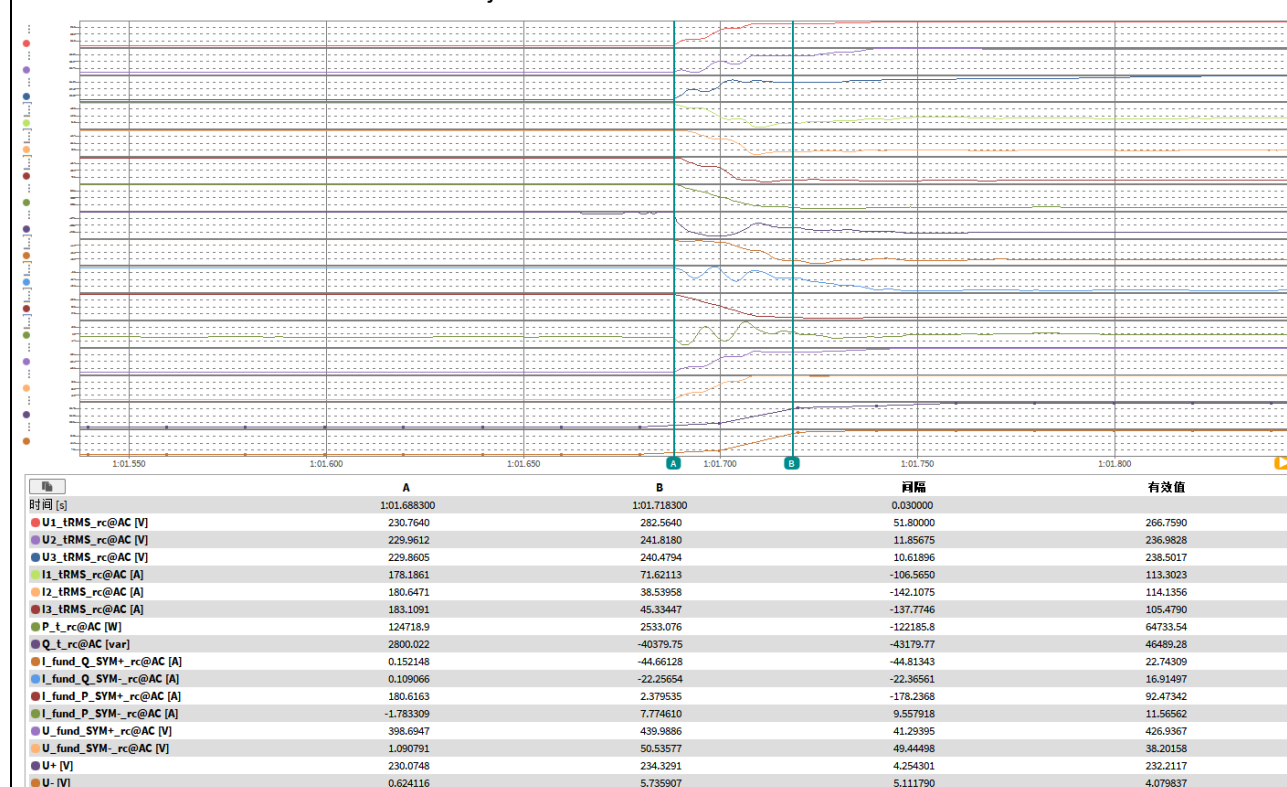
Before fault



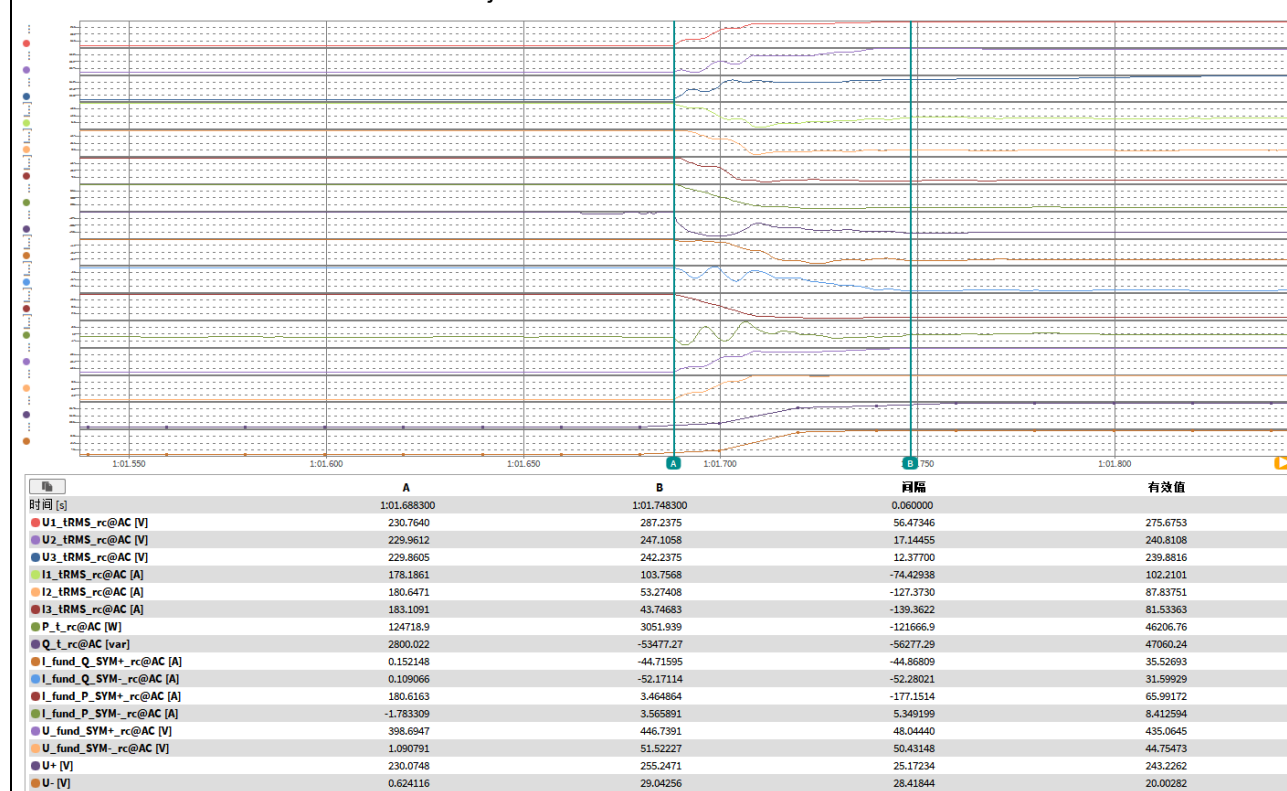
Duration fault time



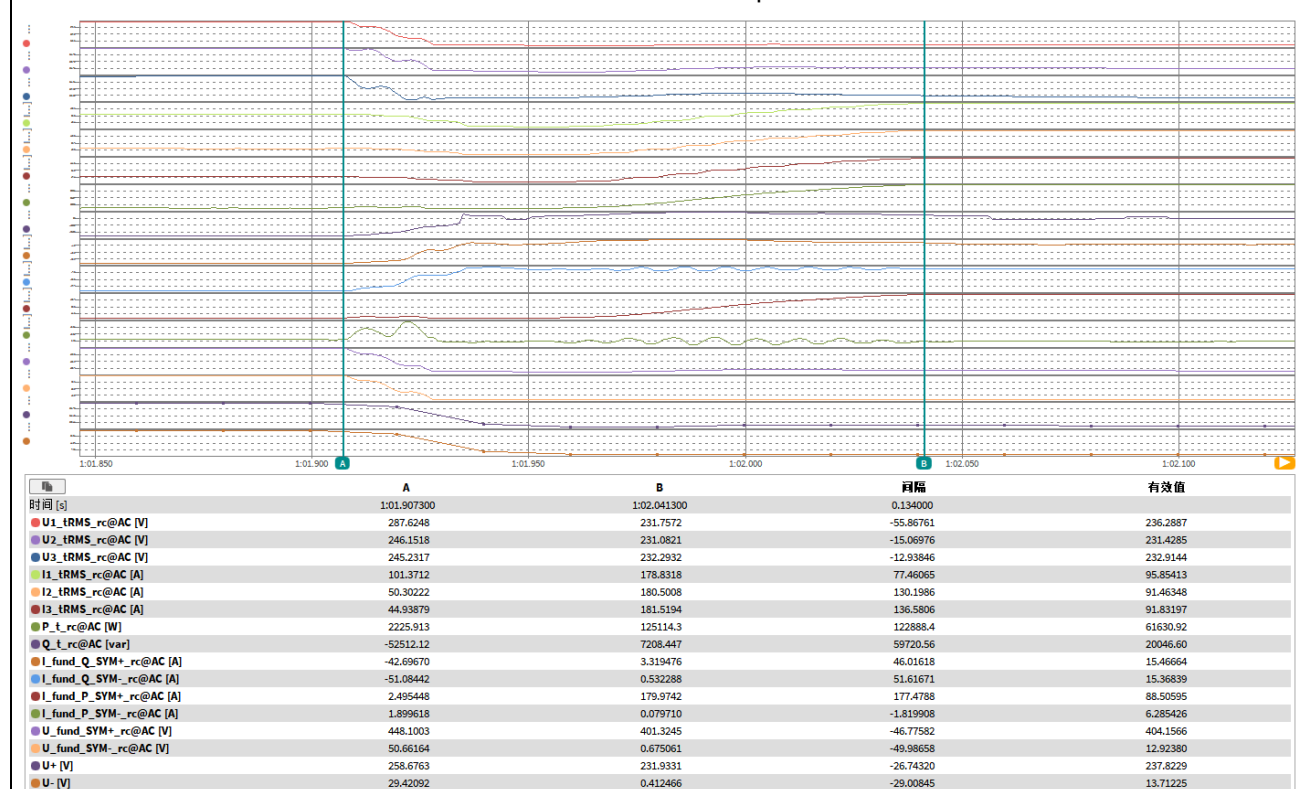
Injected reactive current at 30ms



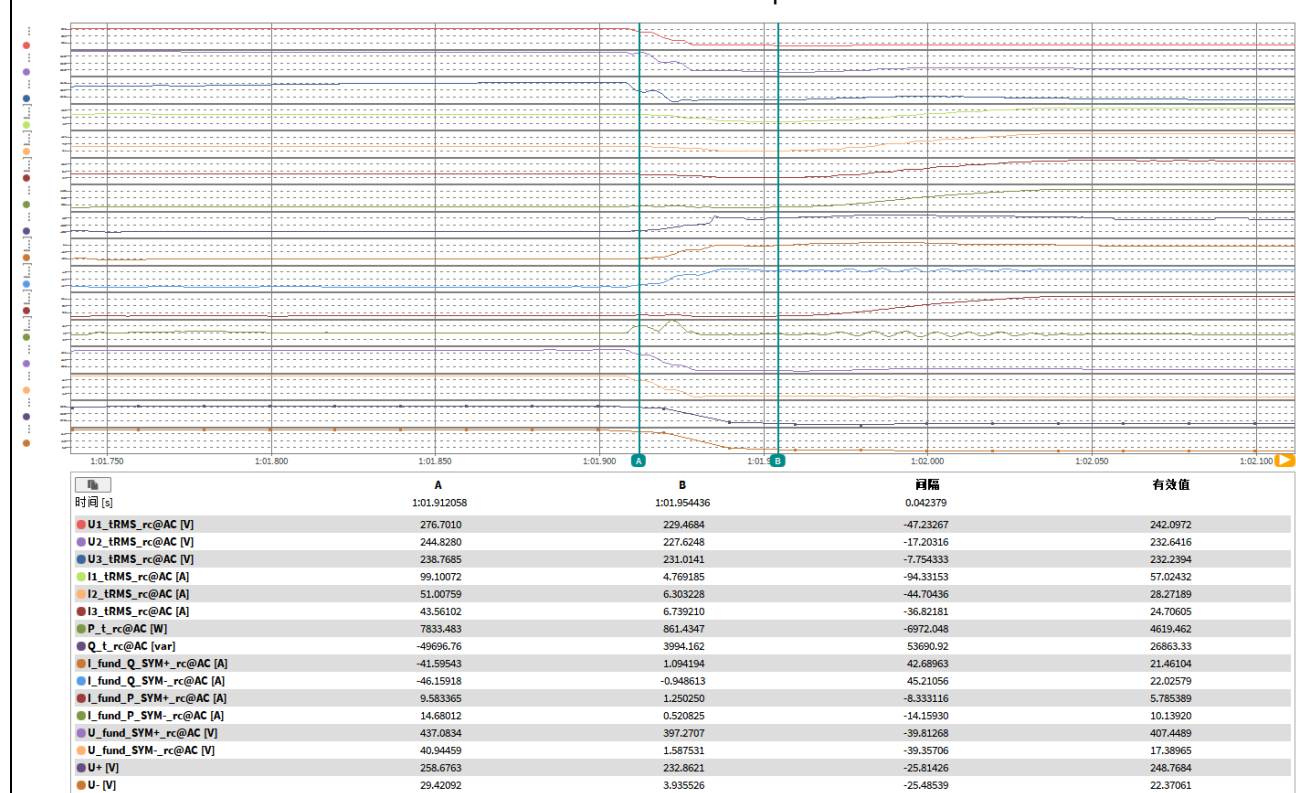
Injected reactive current at 60ms



Recover time – Active power

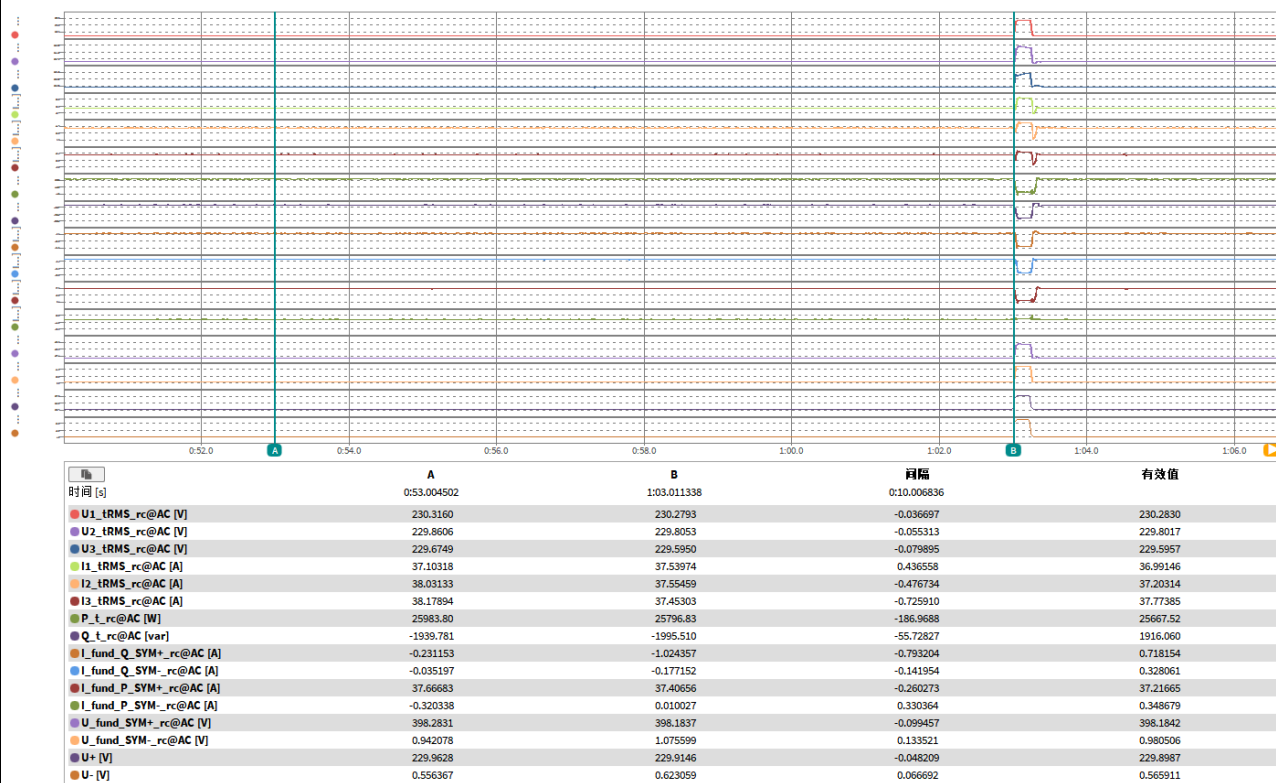


Recover time – Reactive power

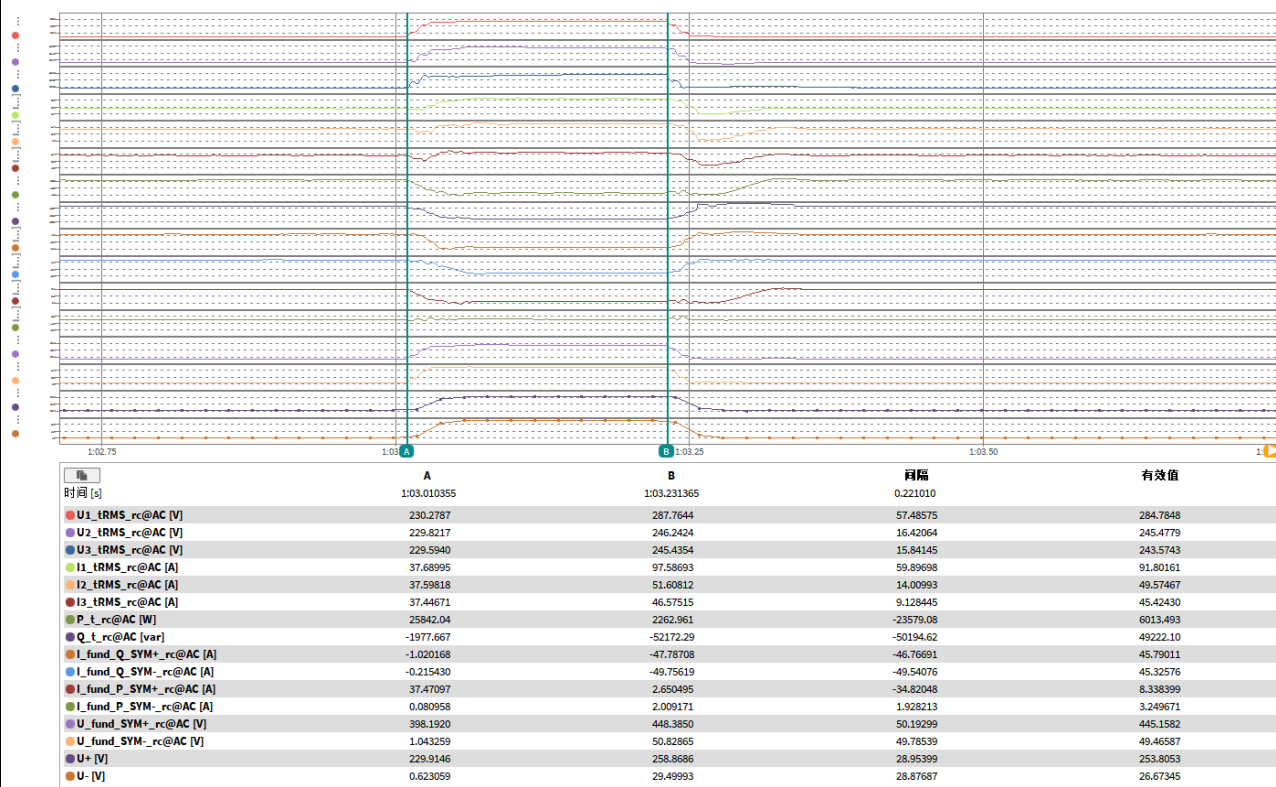


Test – VS3.2

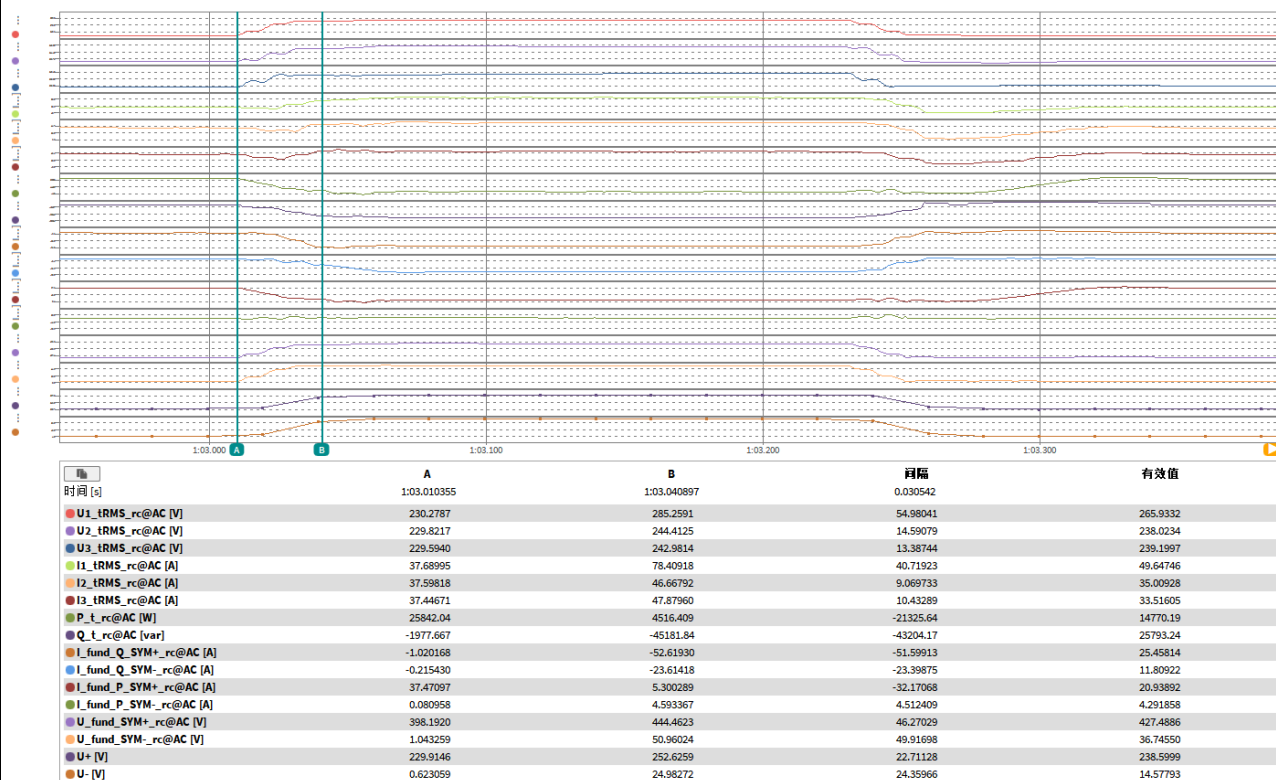
Before fault



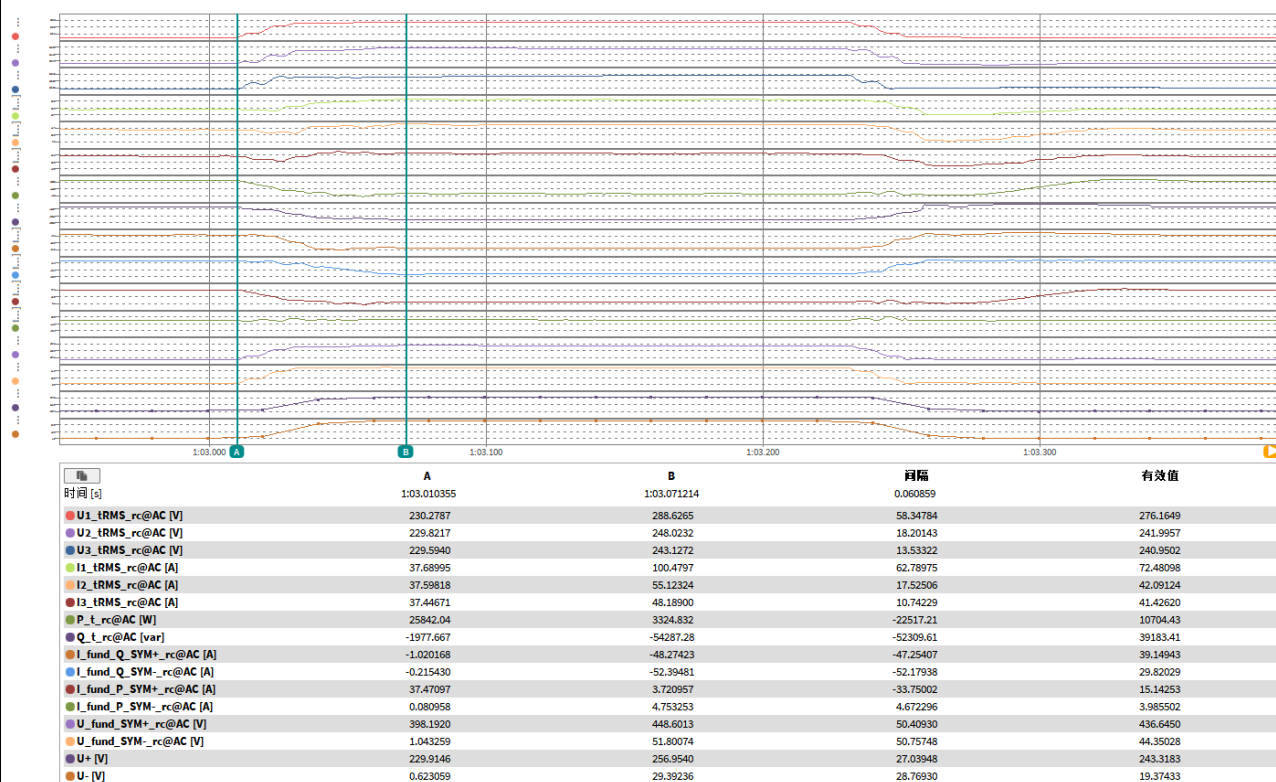
Duration fault time



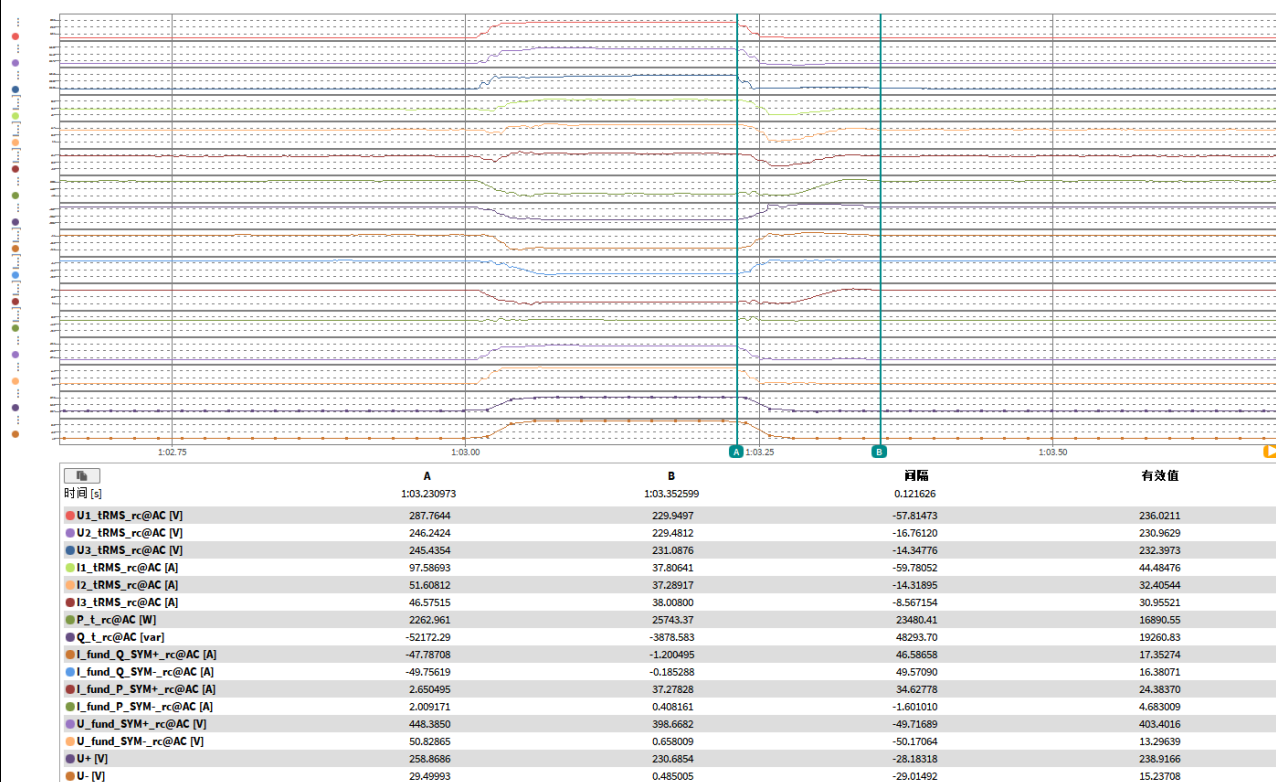
Injected reactive current at 30ms



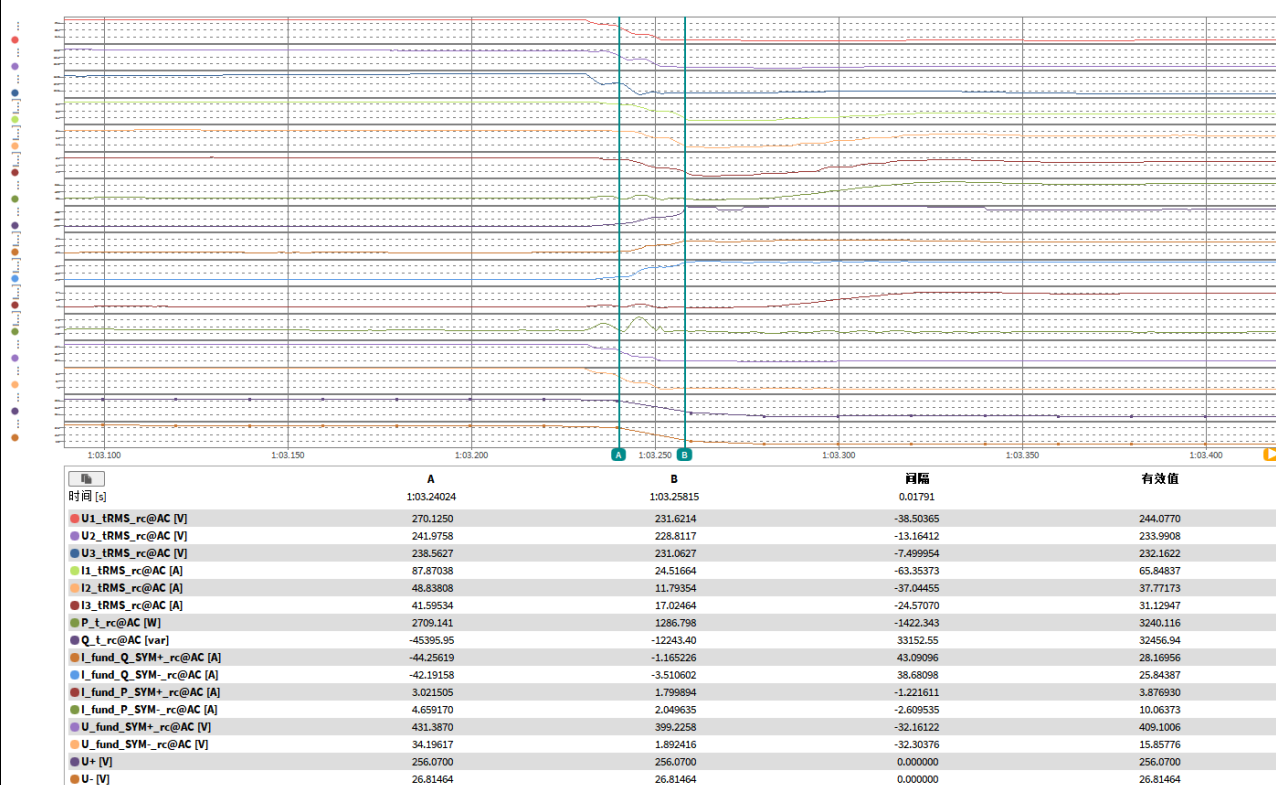
Injected reactive current at 60ms



Recover time – Active power

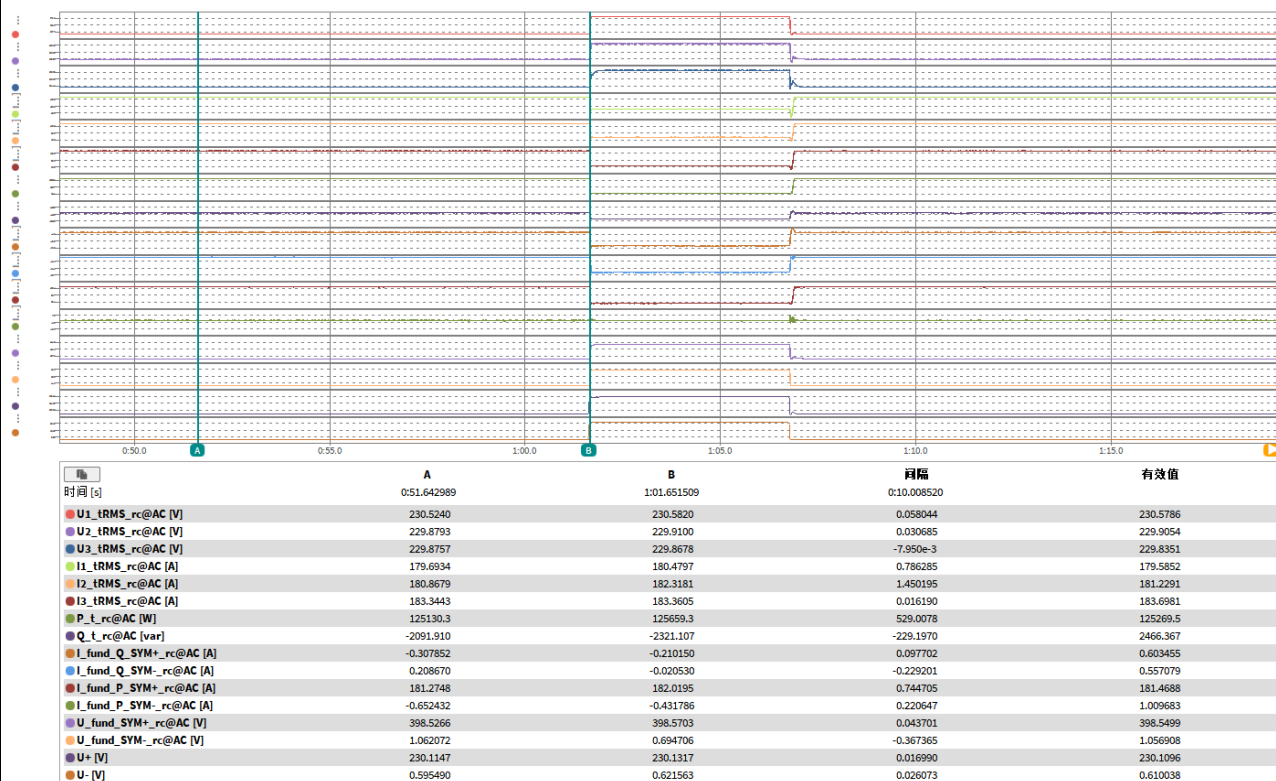


Recover time – Reactive power

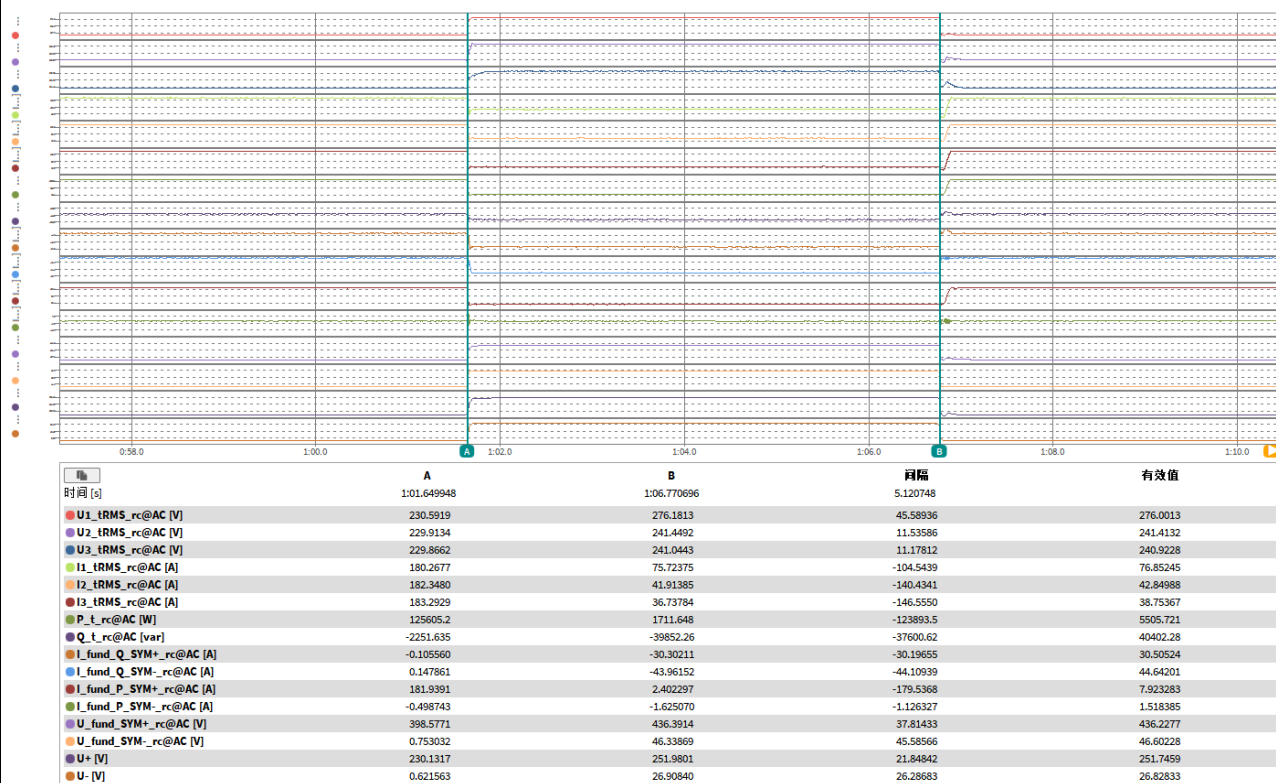


Test – VS4.1.1

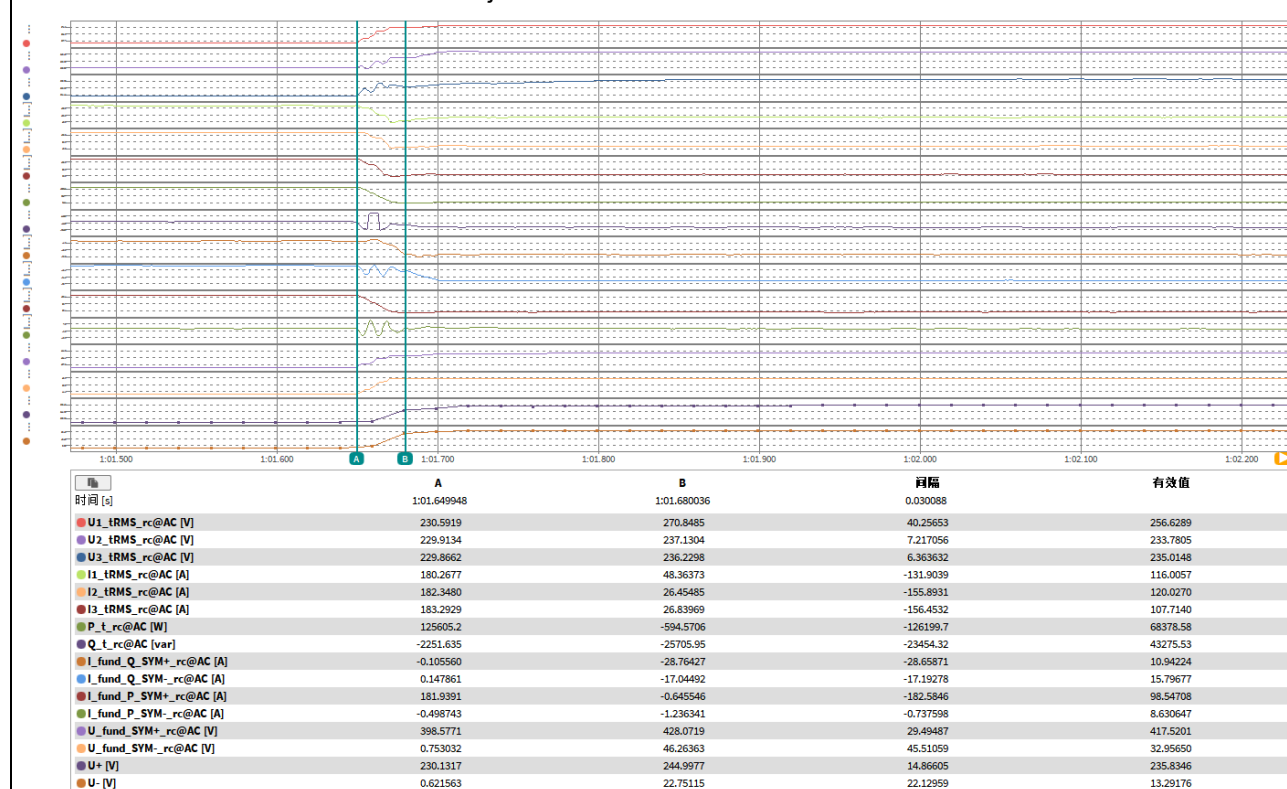
Before fault



Duration fault time



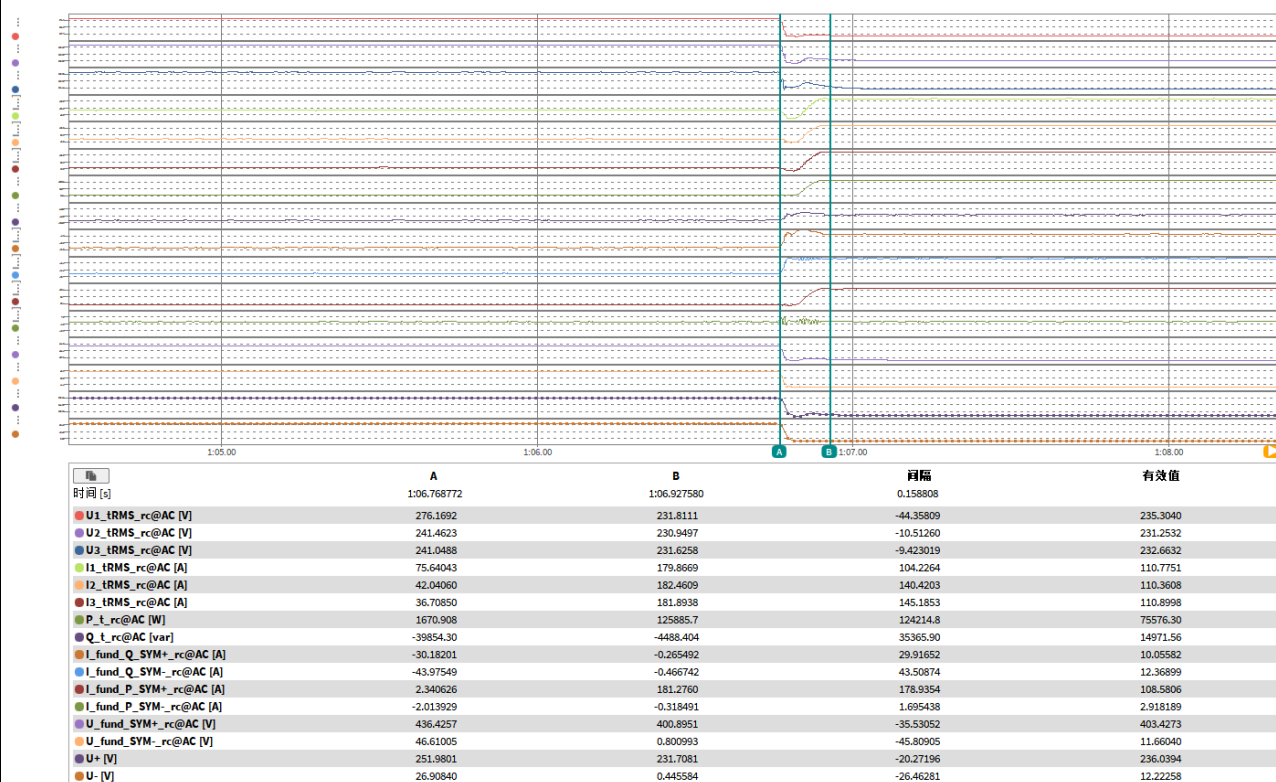
Injected reactive current at 30ms



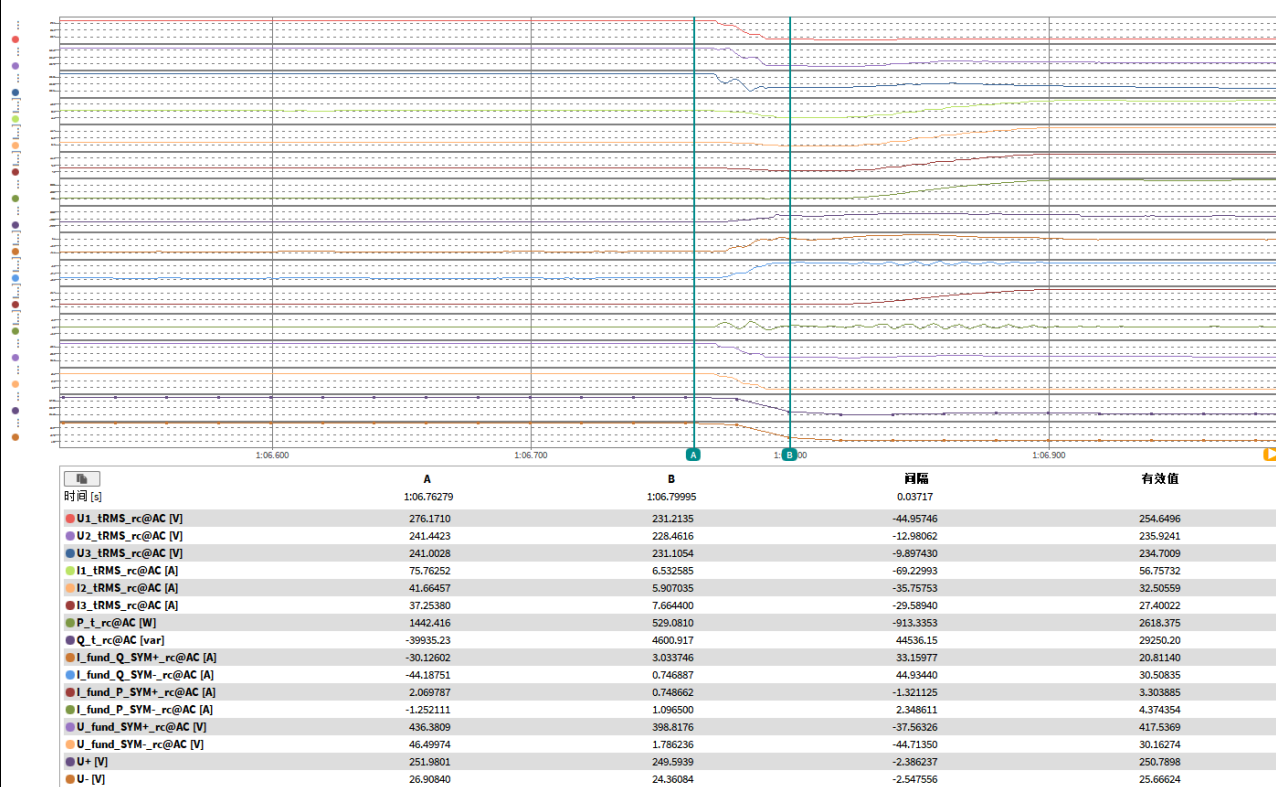
Injected reactive current at 60ms



Recover time – Active power

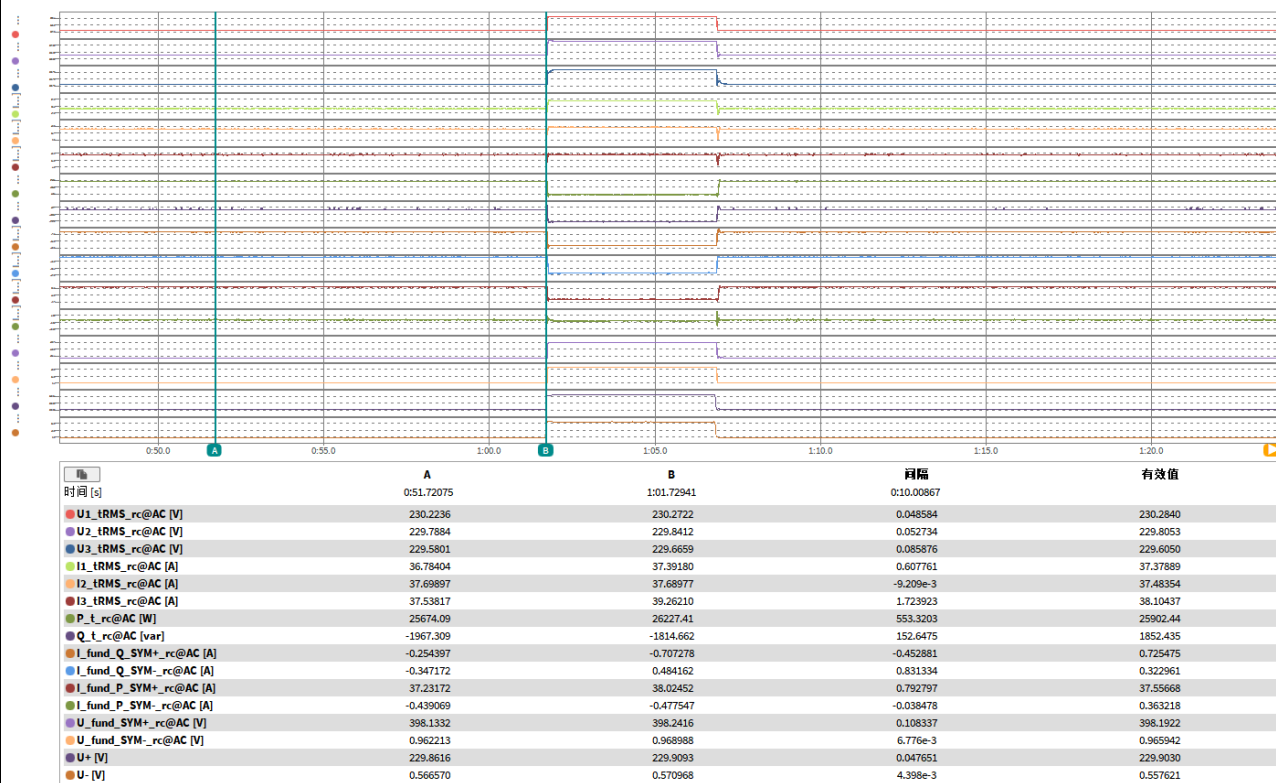


Recover time – Reactive power

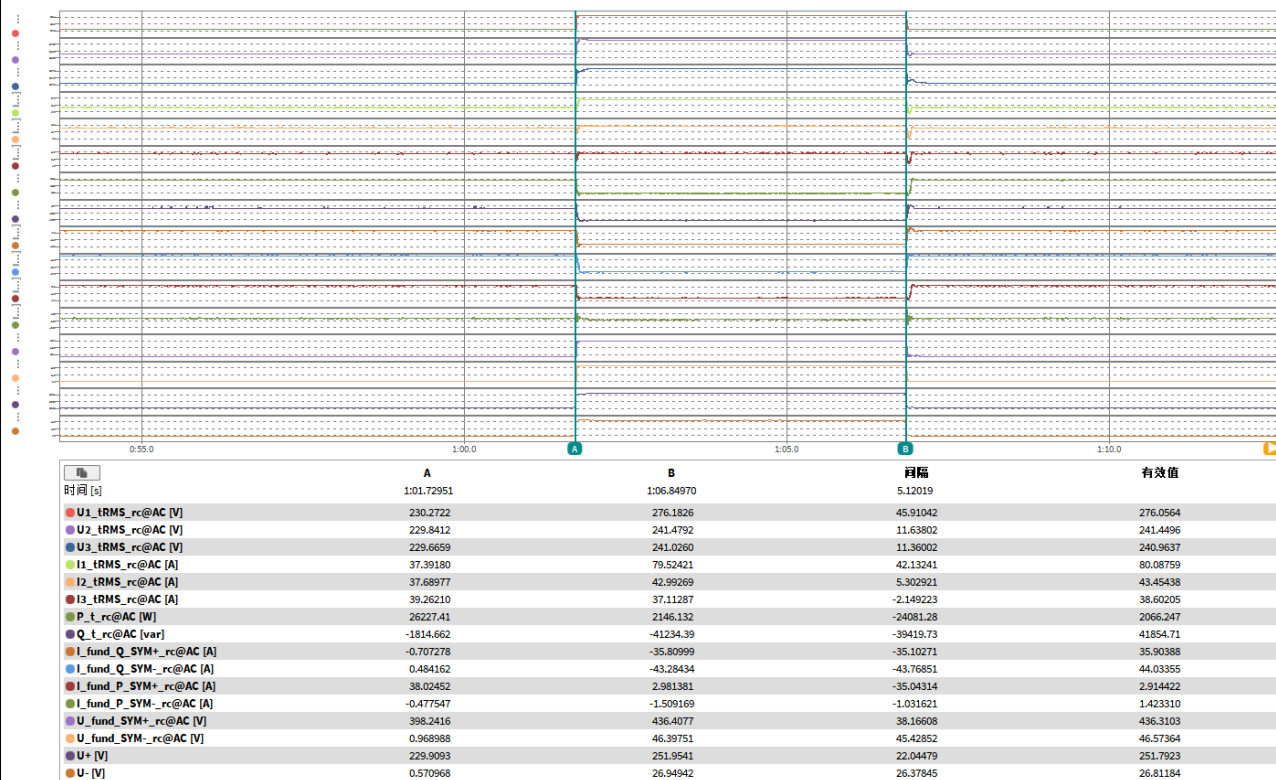


Test – VS4.1.2

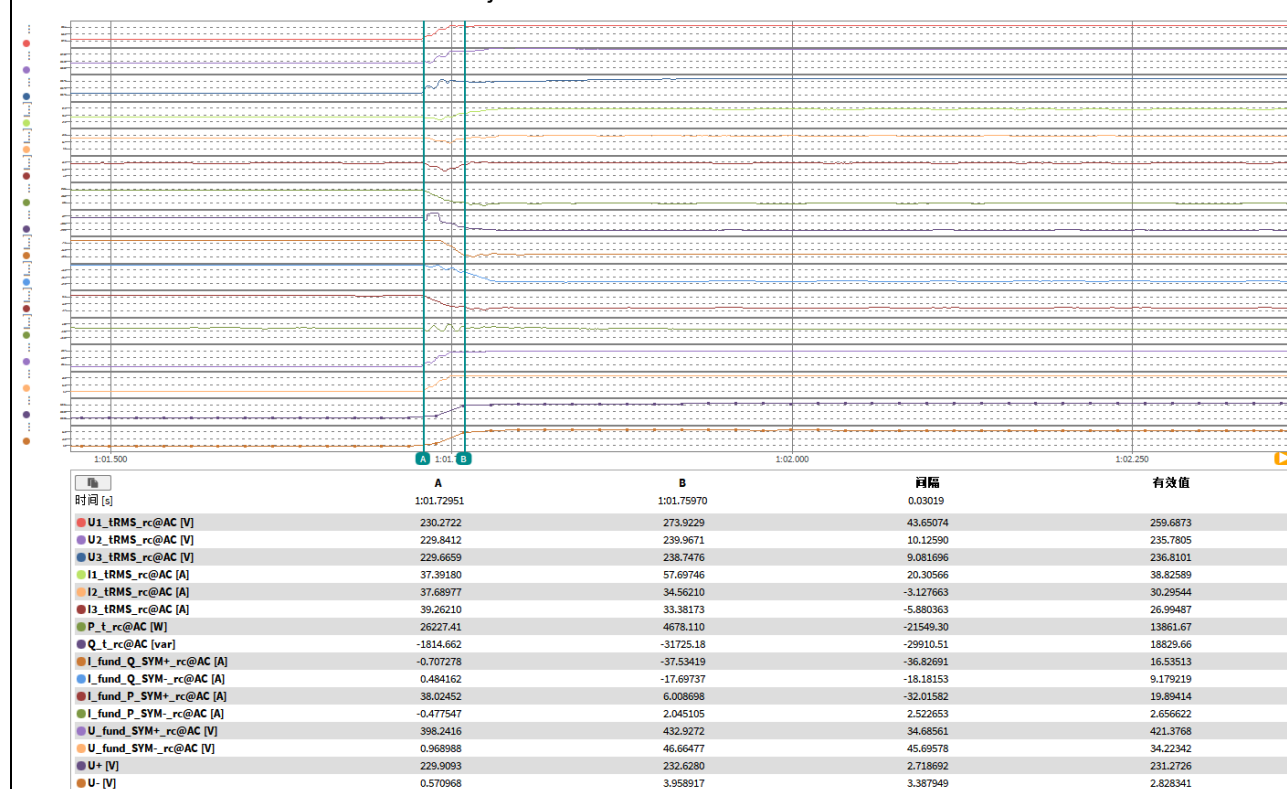
Before fault



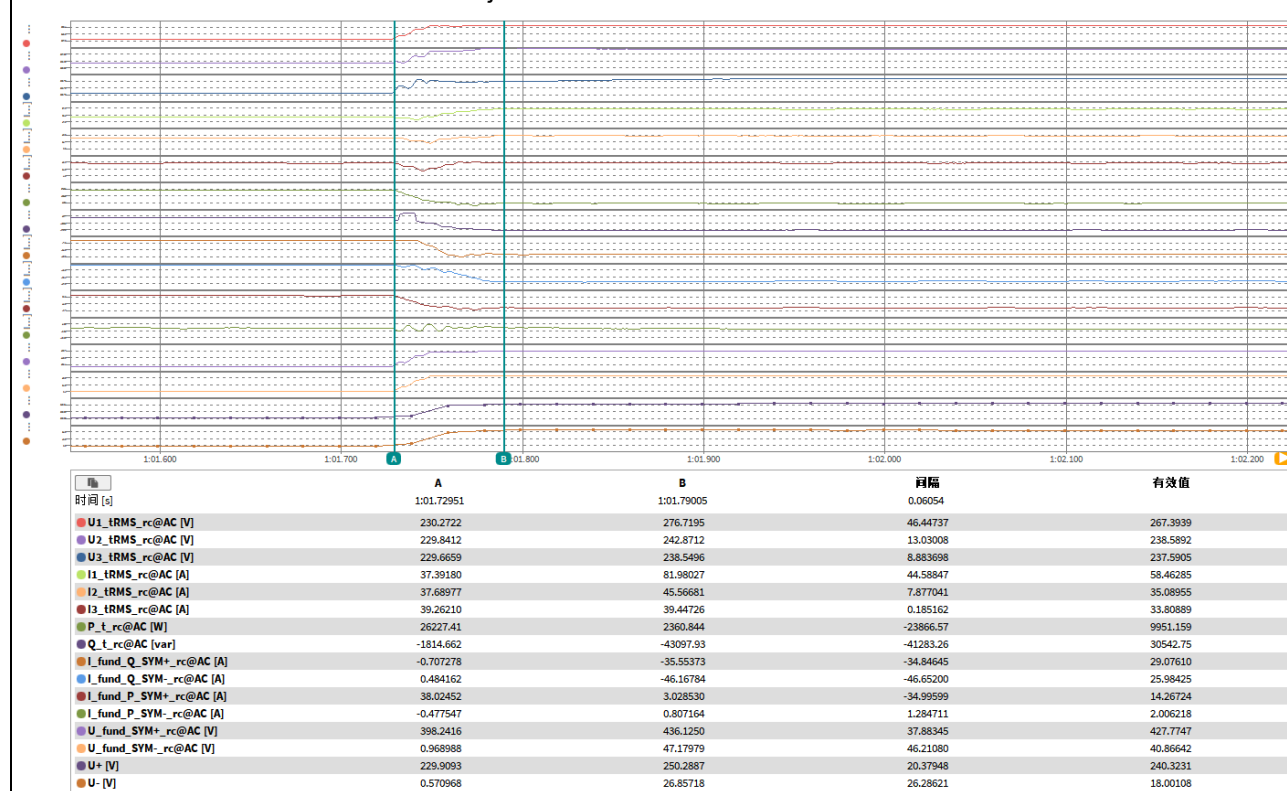
Duration fault time



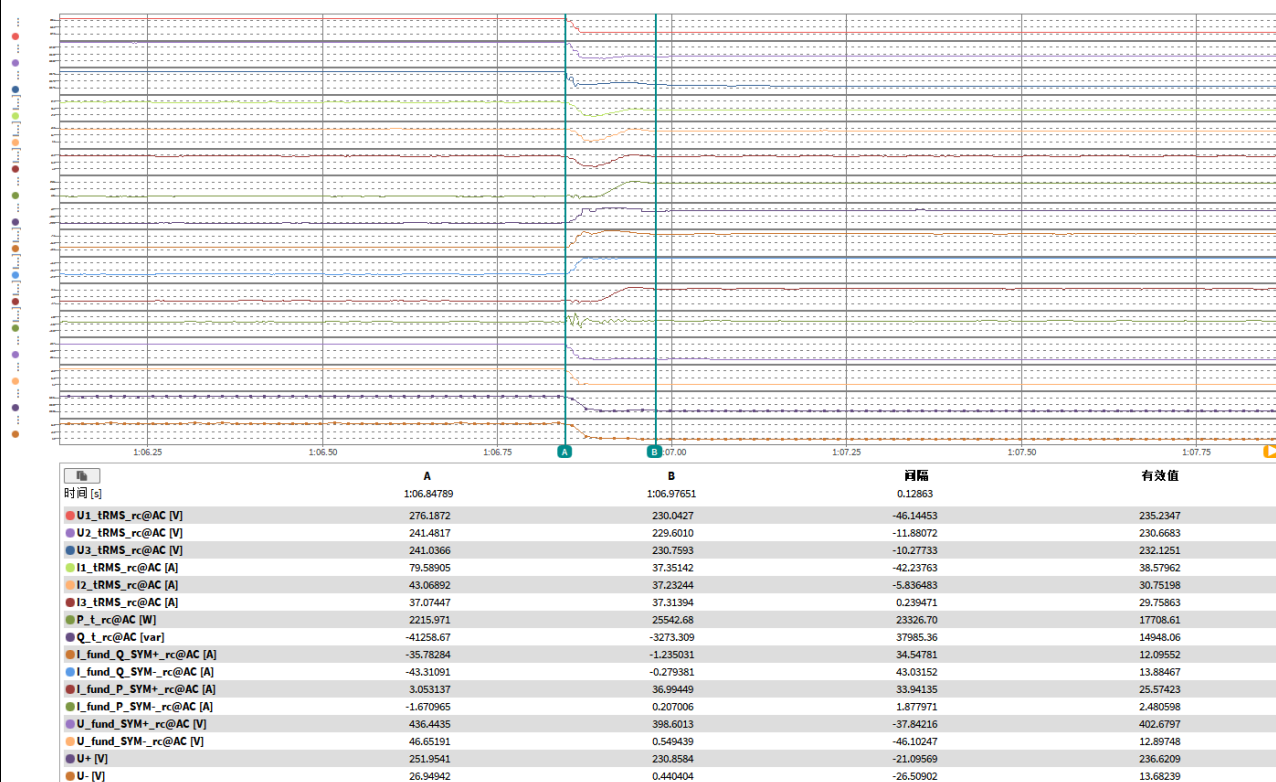
Injected reactive current at 30ms



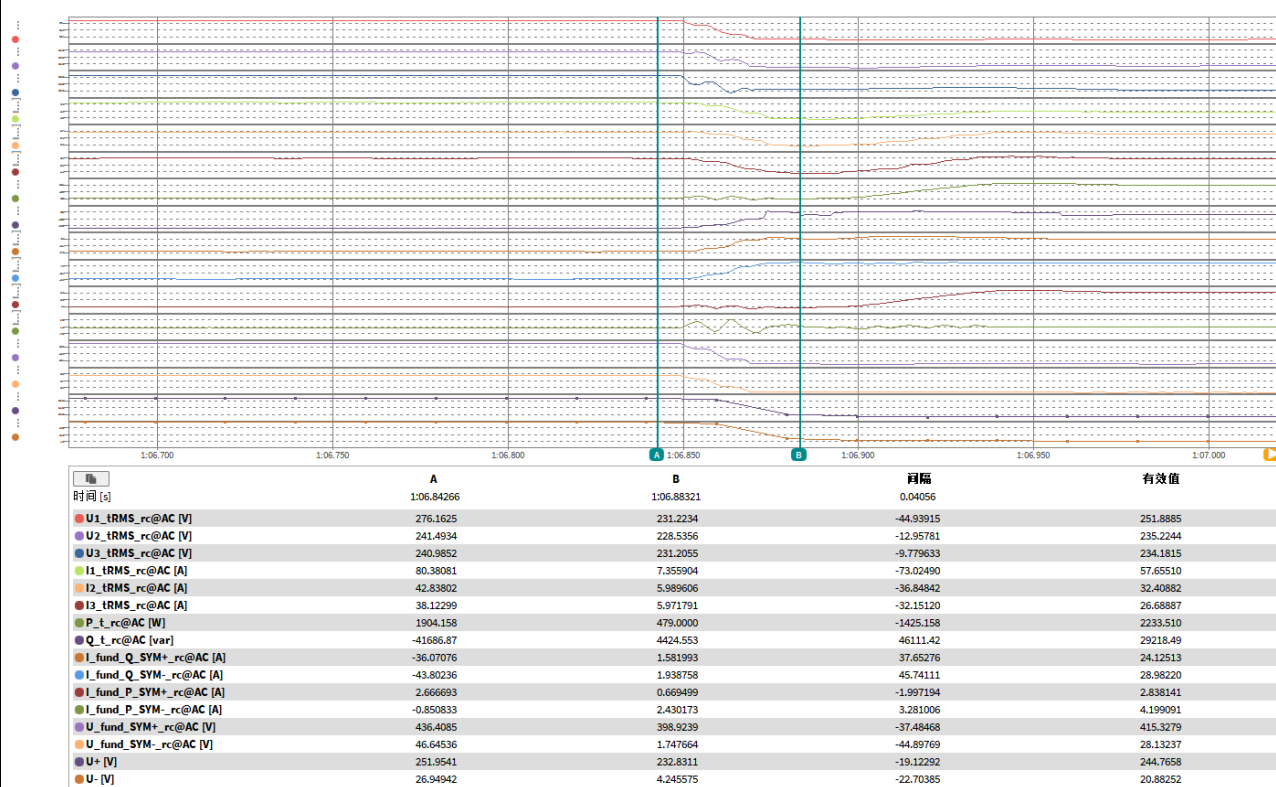
Injected reactive current at 60ms



Recover time – Active power

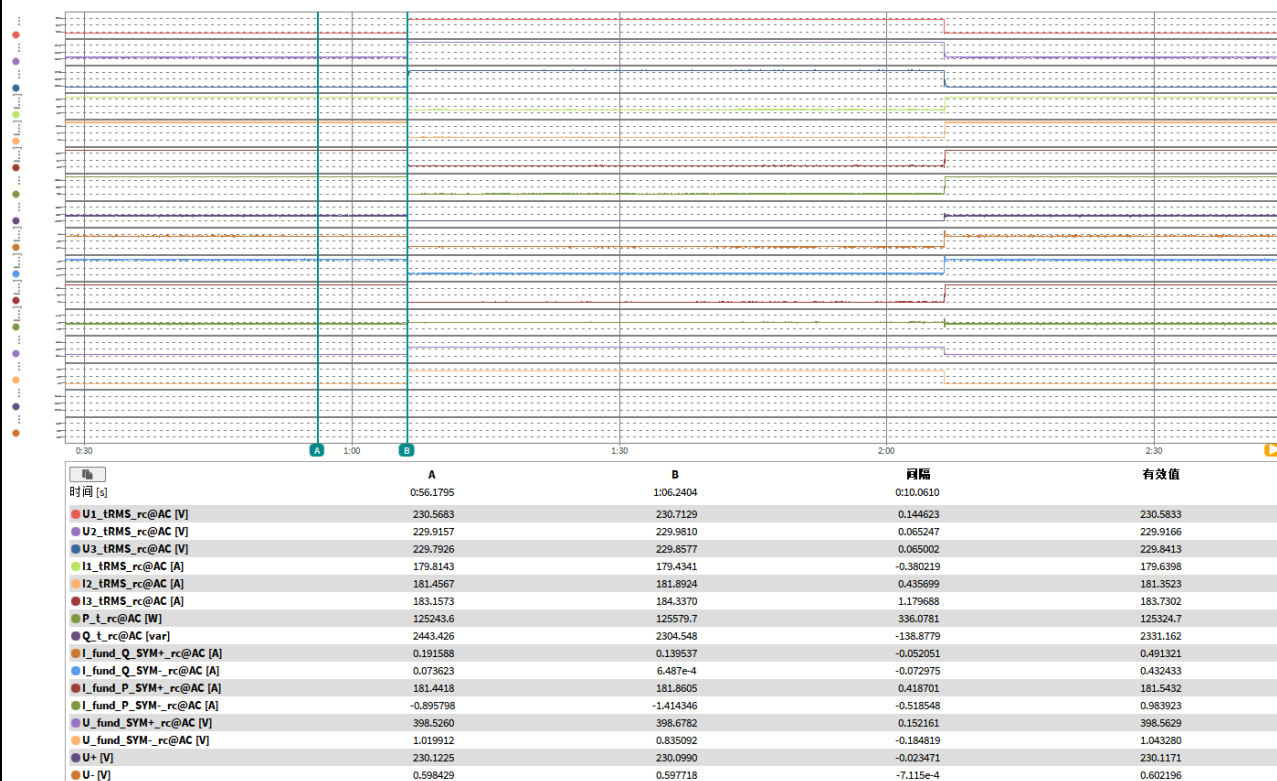


Recover time – Reactive power

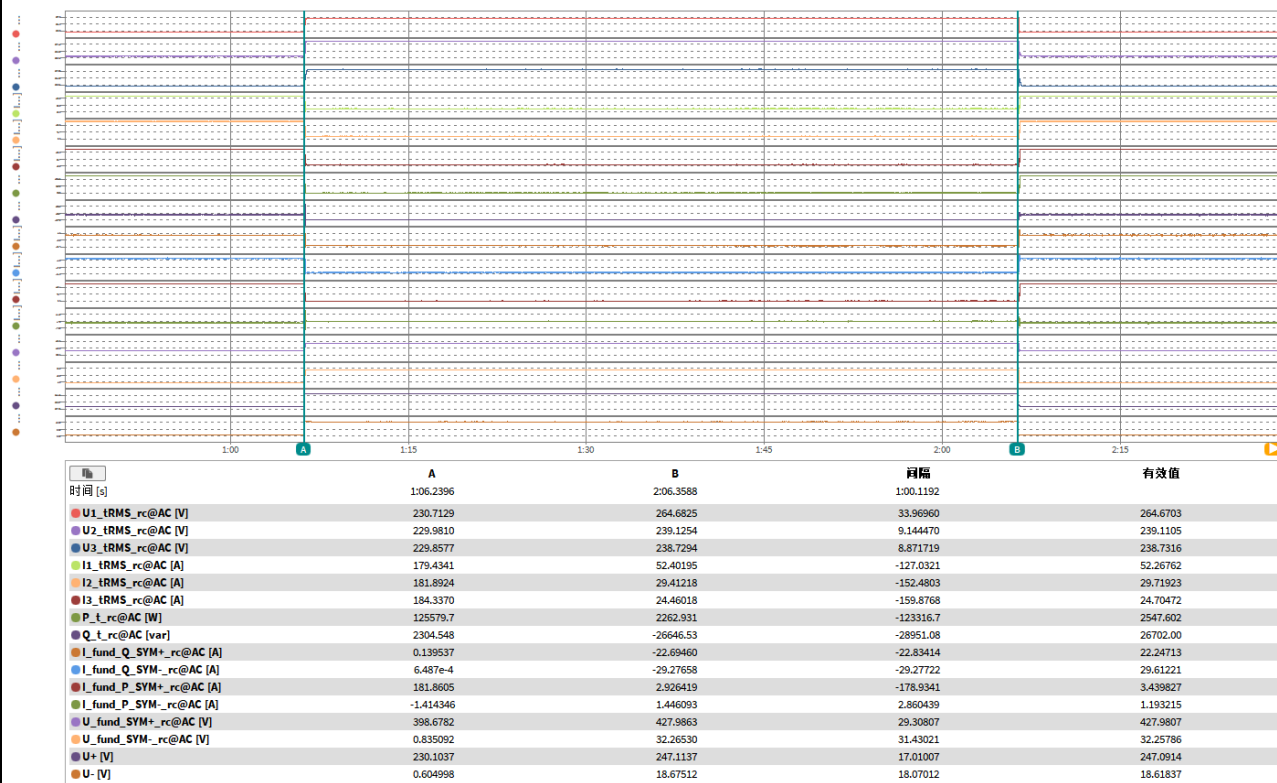


Test – VS4.2.1

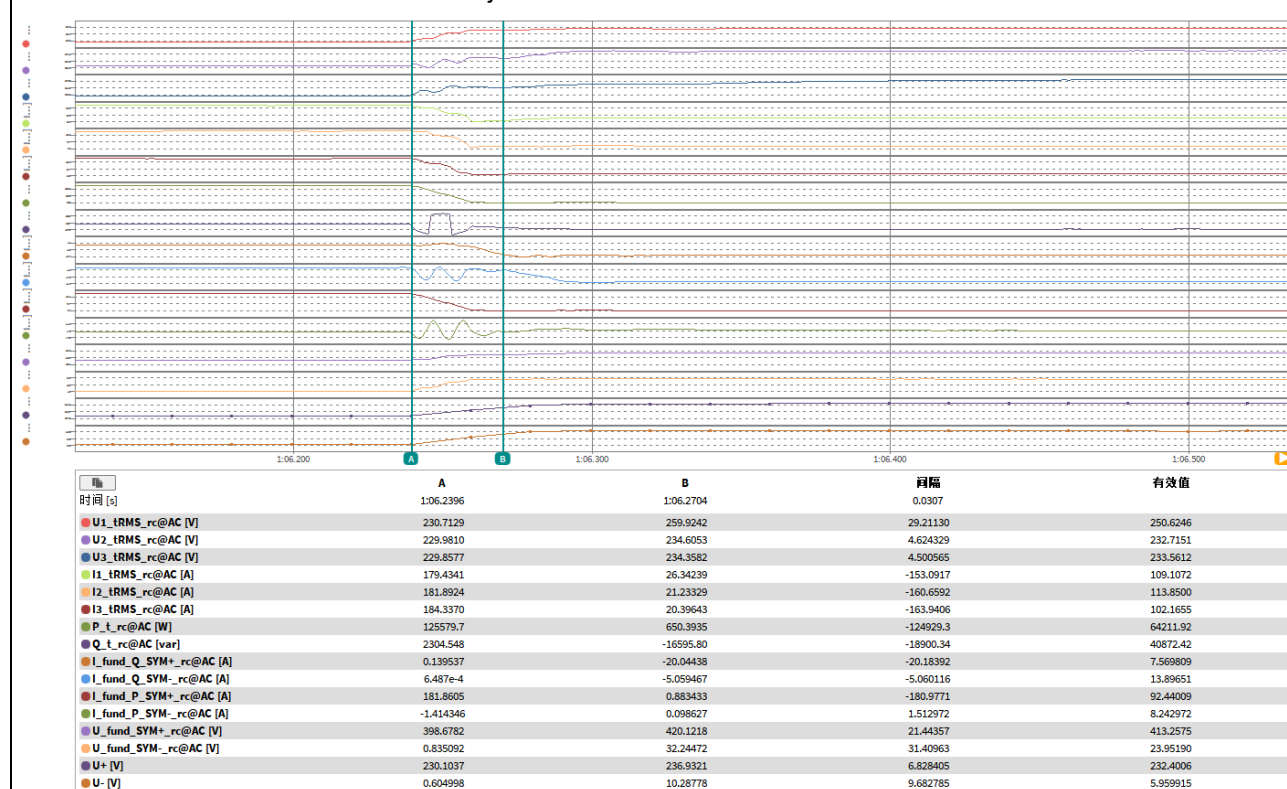
Before fault



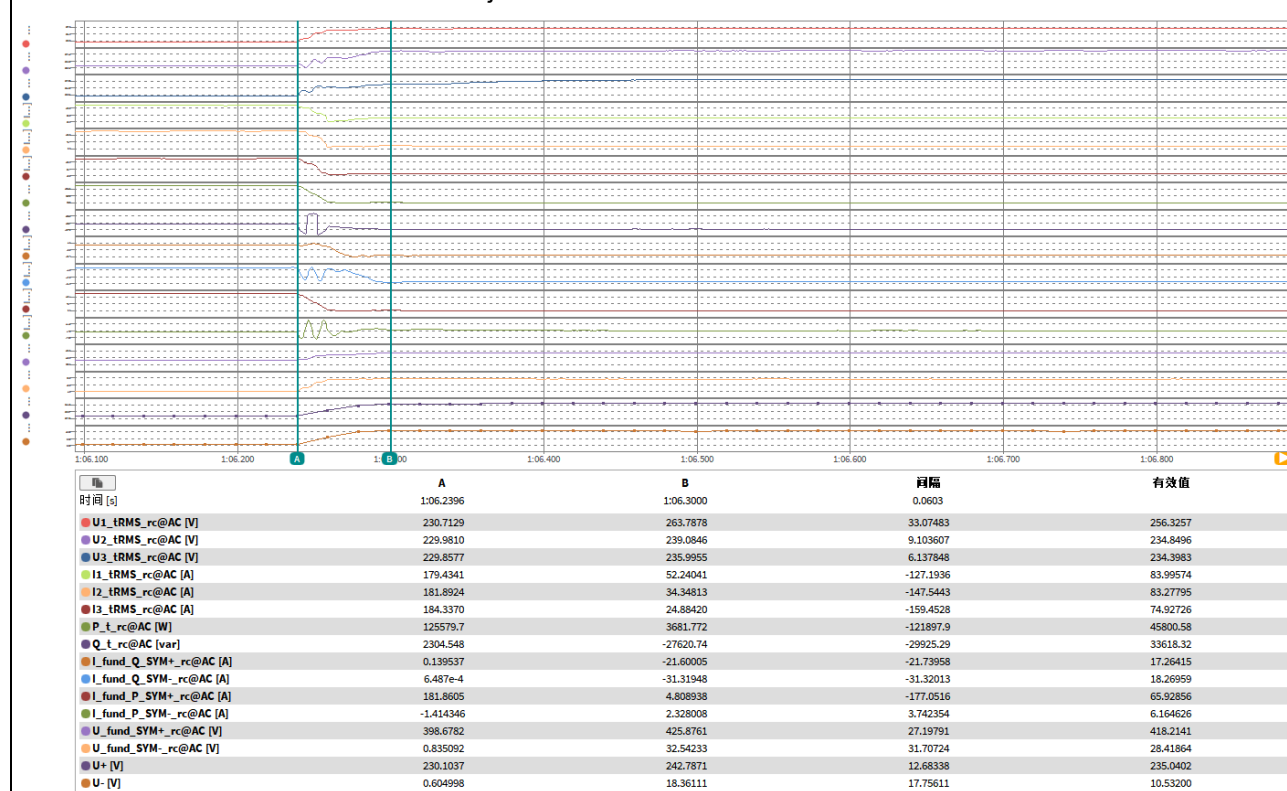
Duration fault time



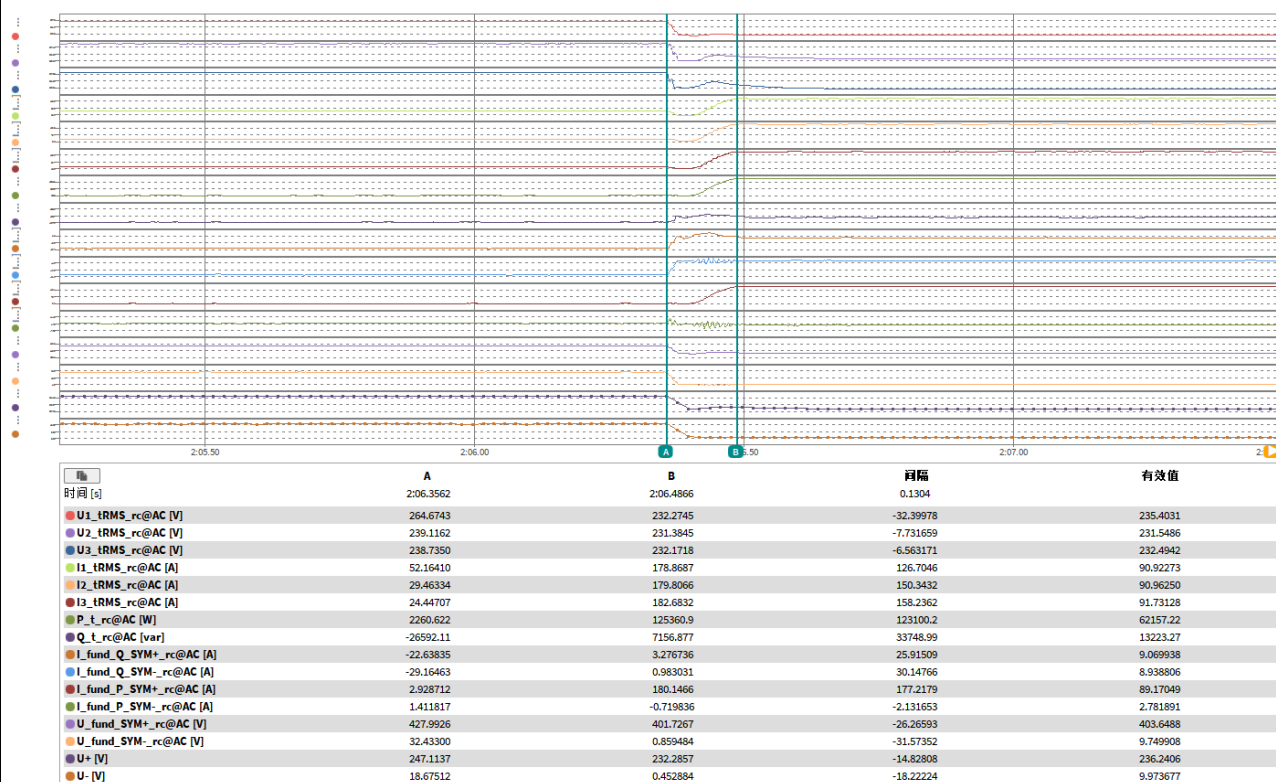
Injected reactive current at 30ms



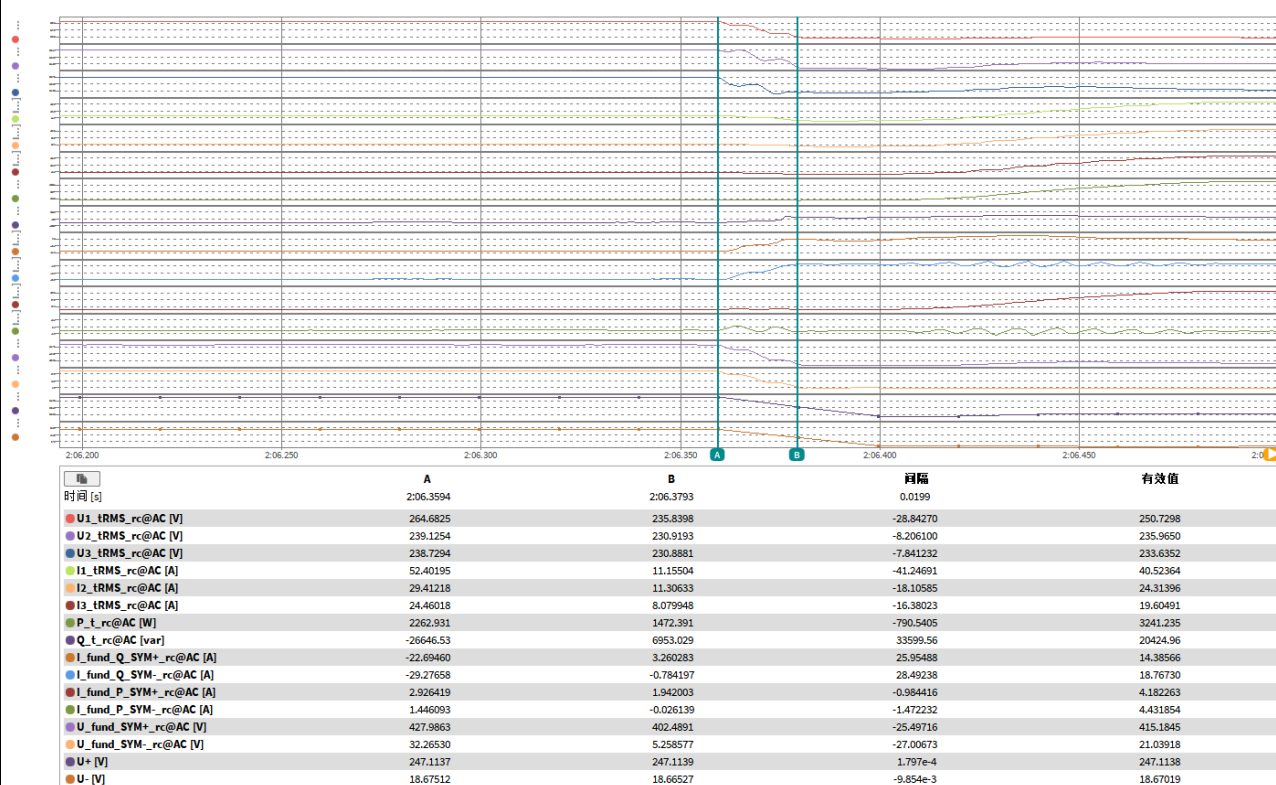
Injected reactive current at 60ms



Recover time – Active power

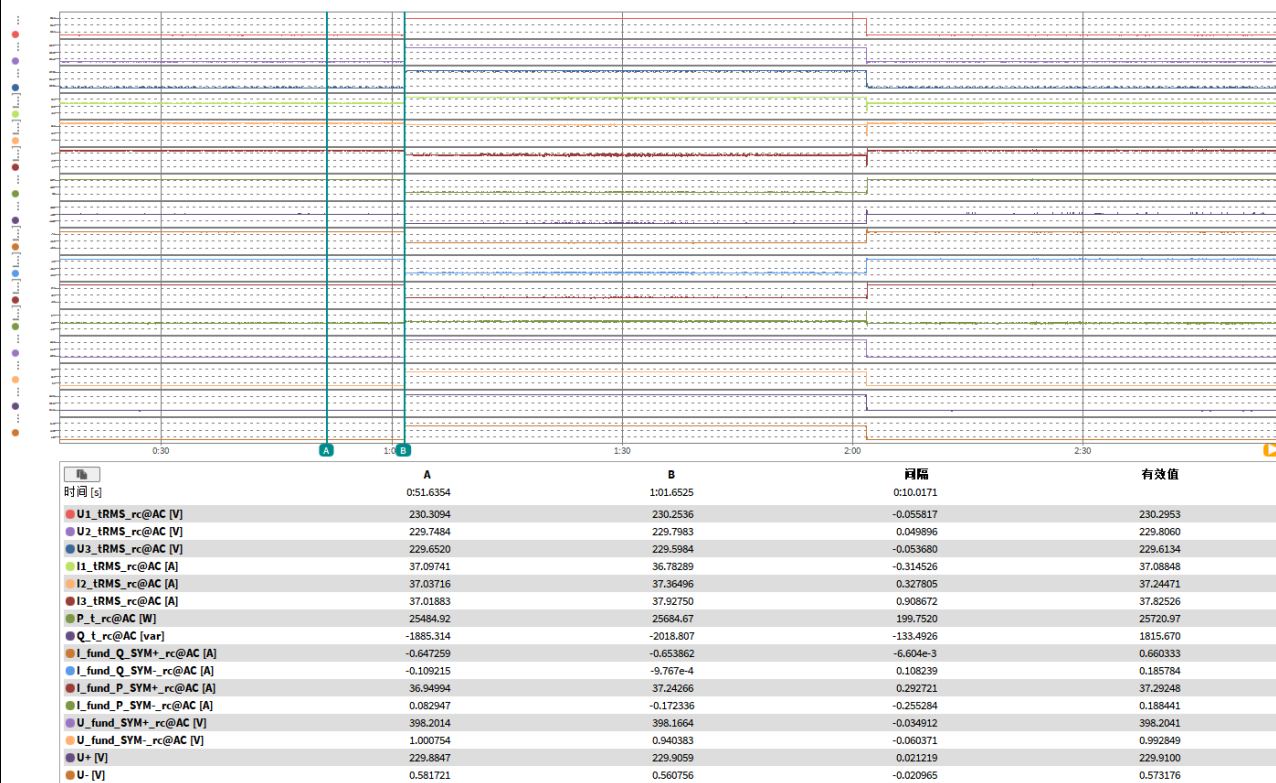


Recover time – Reactive power

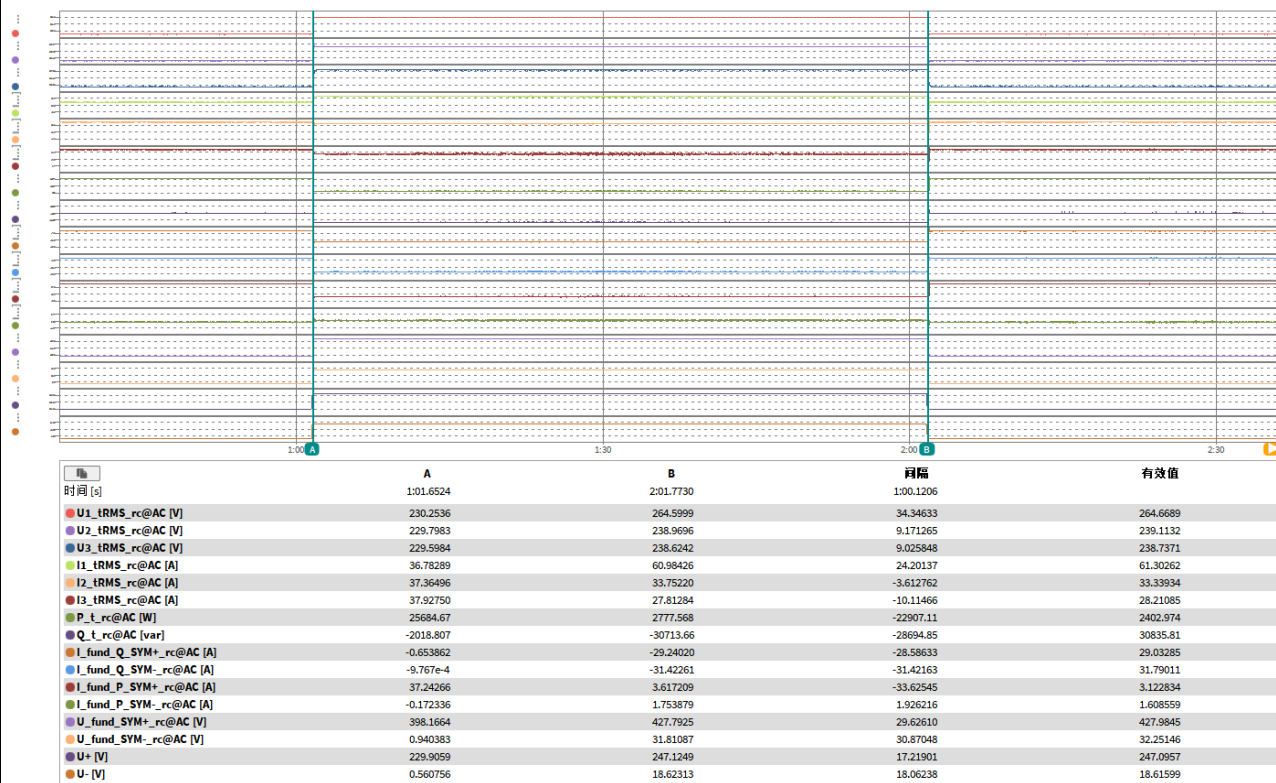


Test – VS4.2.2

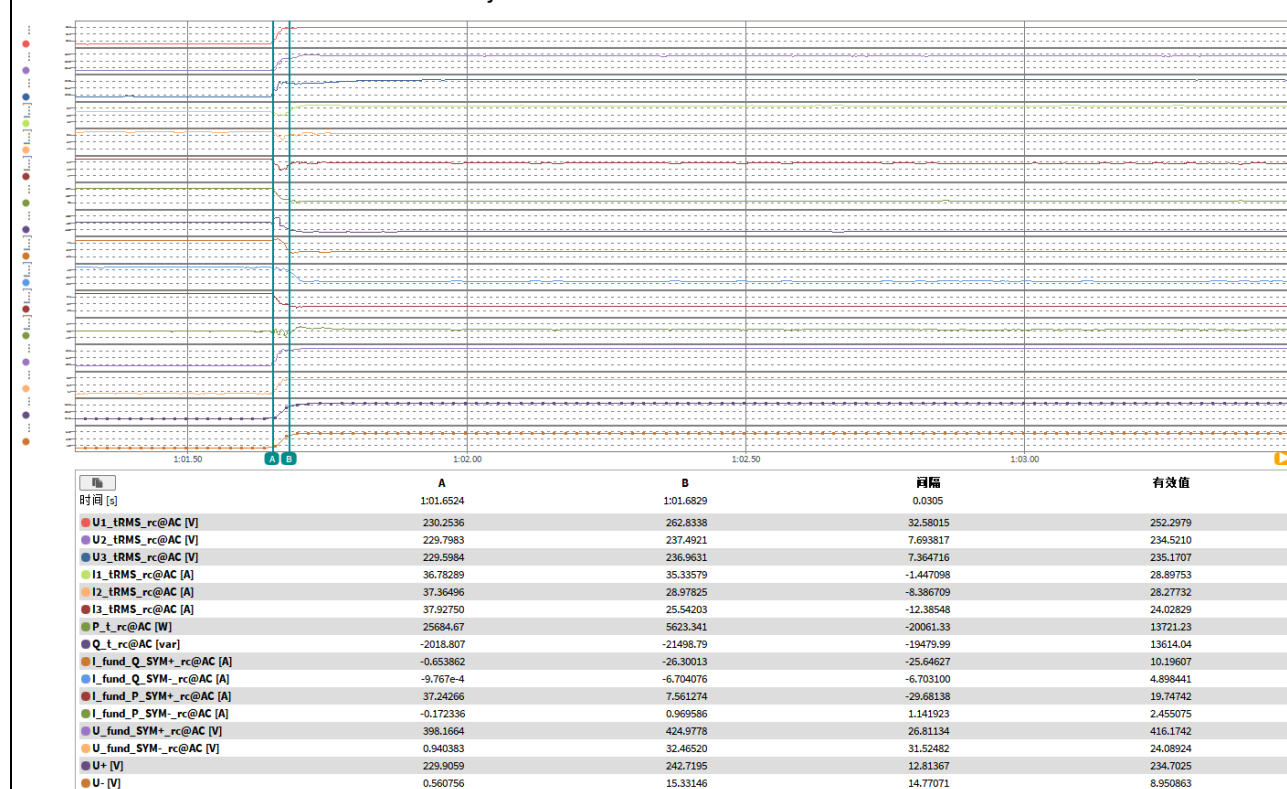
Before fault



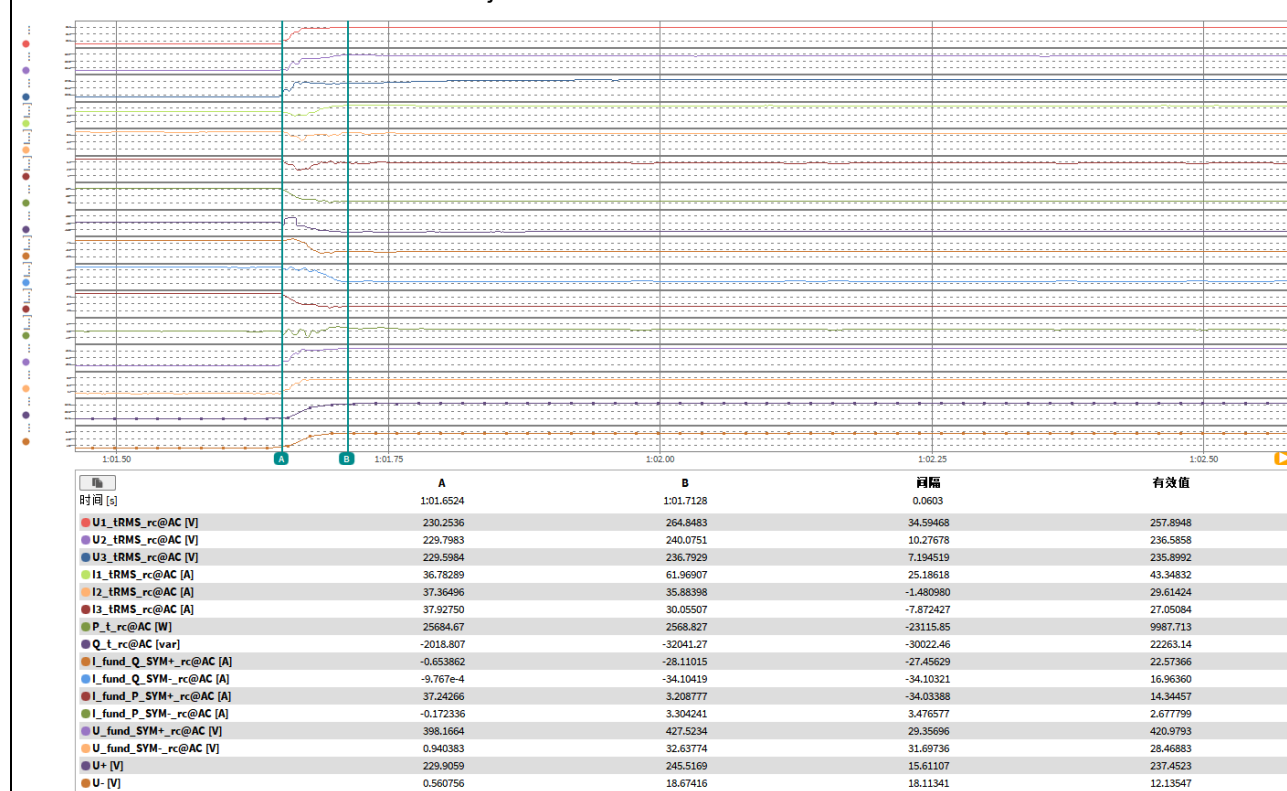
Duration fault time



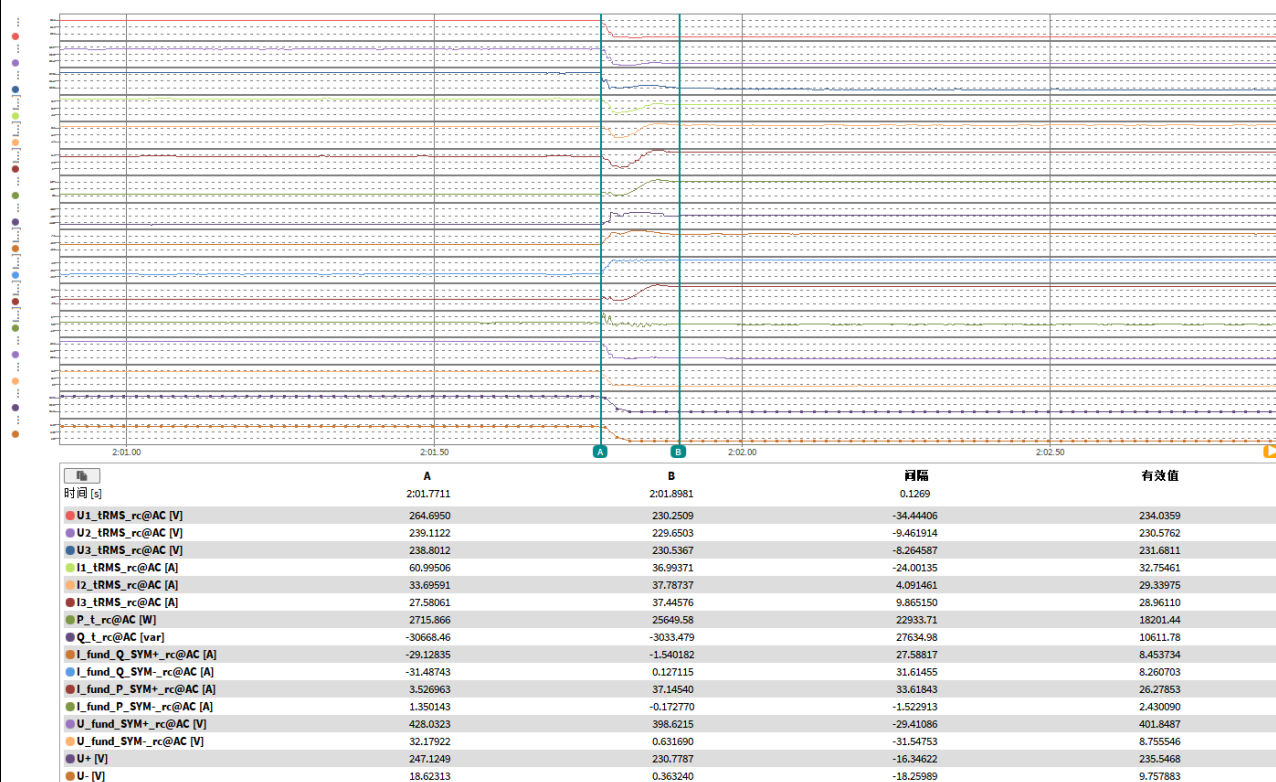
Injected reactive current at 30ms



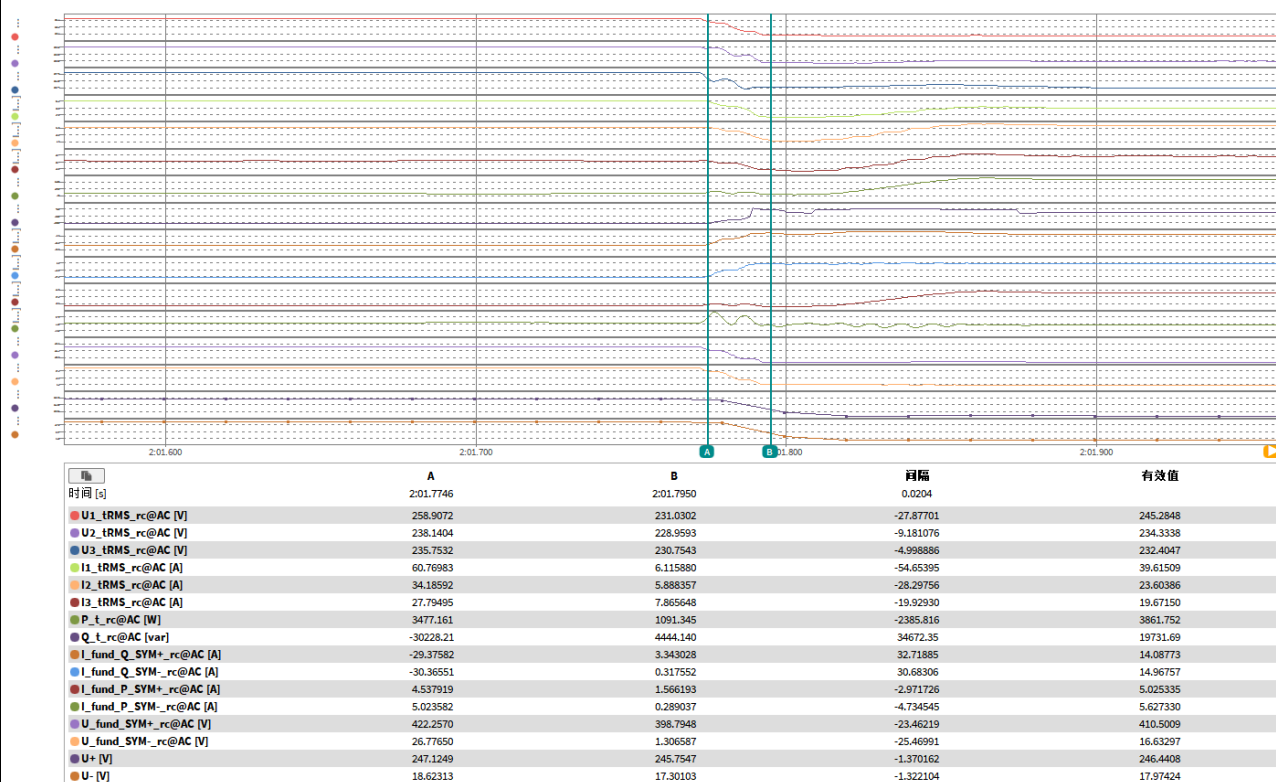
Injected reactive current at 60ms



Recover time – Active power



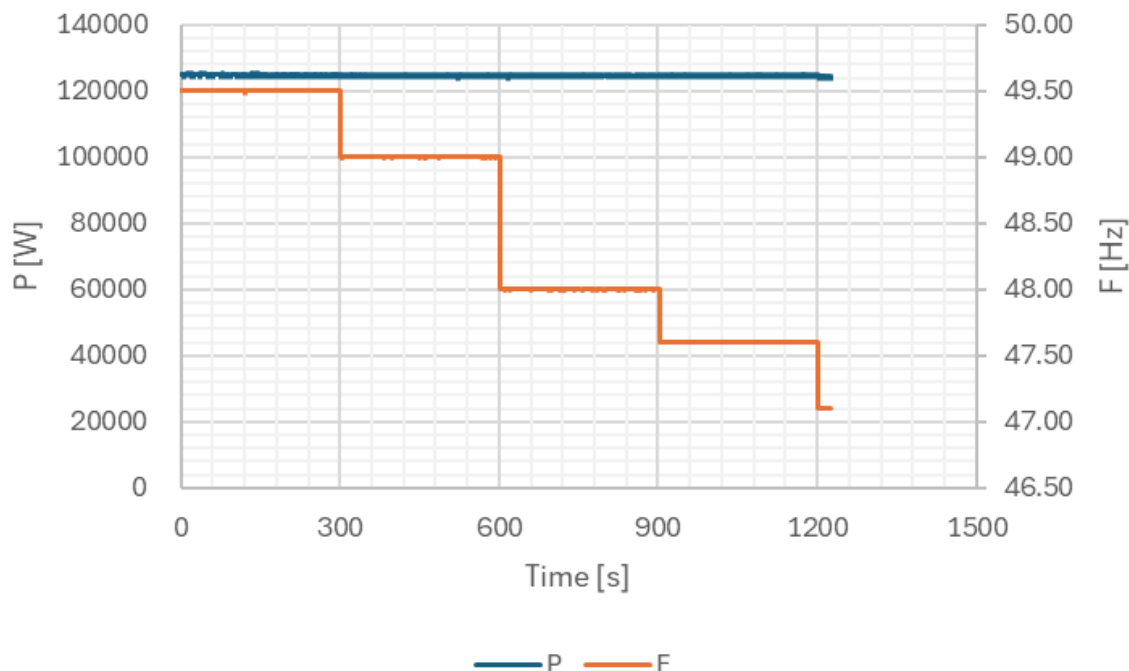
Recover time – Reactive power



4.4.3 Minimal requirement for active power delivery at under-frequency

P

Graph of frequency a) to b) to c) to d) to e):



	Switch to:				
5-min mean value (each)	a) 49.50 [Hz]	b) 49.00 [Hz]	c) 48.00 [Hz]	d) 47.60 [Hz]	e) 47.10 [Hz]
Frequency [Hz]:	49.50	49.00	48.00	47.60	47.10
Active power [W]:	124928	124740	124667	124718	124573
$\Delta P/P_M$ [%] per 1 Hz:	--	0.21	0.27	0.23	0.34

Test:

Operating points a) to d) must be kept for at least 5 minutes.

Operating points e) must be kept for at least 20s.

The test must be carried out at 100% P_n .

With a programmable AC source, the PGU is operated at 100% P_n and 49.50 ± 0.01 Hz. Since then the frequency has changed to 49.00Hz, 48.00Hz, 47.60Hz, 47.10Hz, A 5-min mean value is recorded both before and after the frequency change.

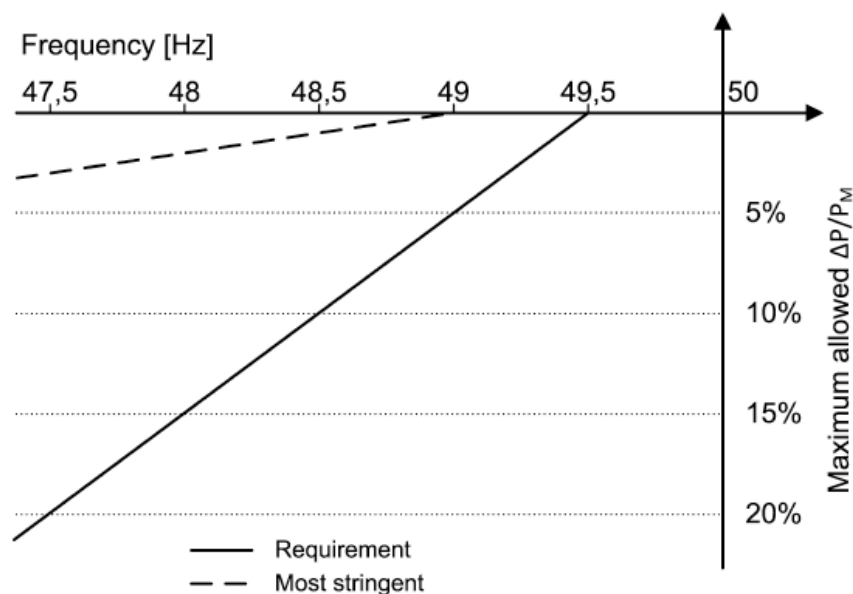
Assessment criterion:

The test is passed when the micro-generator

- The frequency should then be set to 49.5 Hz for 5 minutes. The output should remain at 100% of

registered Capacity.

- The frequency should then be set to 49.0 Hz and once the output has stabilised, held at this frequency for 5 minutes. The Active Power output must not be below 99% of registered Capacity.
- The frequency should then be set to 48.0 Hz and once the output has stabilised, held at this frequency for 5 minutes. The Active Power output must not be below 97% of registered Capacity.
- The frequency should then be set to 47.6 Hz and once the output has stabilised, held at this frequency for 5 minutes. The Active Power output must not be below 96.2% of registered Capacity.
- The frequency should then be set to 47.1 Hz and held at this frequency for 20s. The Active Power output must not be below 95.0% of registered Capacity and the Synchronous Power Generating Module must not trip in less than the 20s



Maximum allowable power reduction in case of under-frequency

EN 50549-10:2022		
Clause	Test items	Remark
5.4	Active response to frequency deviation	
5.4.3.2	Power response to over-frequencies	P
5.4.3.3	Power response to under-frequencies	P

5.4.3.2 Power response to over-frequency (For system with energy storage)									P
Test:									
1-min mean value [Hz]:	a) 50.00 ±0.01	b) 50.10 ±0.01	c) 50.80 ±0.01	e) 51.40 ±0.01	d) 52.00 ±0.01	f) 51.40 ±0.01	g) 50.80 ±0.01	h) 50.10 ±0.01	i) 50.00 ±0.01
1. Measurement a) to i): Active power output =100% P _{E_{max}} s=5% (40% P _{ref} / Hz), threshold frequency for start/return: 50.2Hz, P _{ref} =P _M									
Frequency [Hz]:	50.00	50.10	50.80	51.40	52.00	51.40	50.80	50.10	50.00
P _M [W]:	125000	125000	95000	65000	35000	65000	95000	125000	125000
P _{E60} [W]:	125182	125224	95638	65631	35974	65674	95698	125218	125262
ΔP _{E60} /P _M [%]:	0.15	0.18	0.51	0.51	0.78	0.54	0.56	0.17	0.21
Dead time(≤2s)	-	-	0.2	0.2	0.2	0.2	0.2	-	-
Step response time (≤30s)	-	-	0.4	0.4	0.6	0.2	0.2	-	-
Setting time(s)	-	-	0.4	0.4	0.6	0.2	0.2	-	-
Limit ΔP/P _{1min} :	± 10 % of P _{E_{max}}								
Test:									
1-min mean value [Hz]:	a) 50.00 ±0.01	b) 50.10 ±0.01	c) 50.80 ±0.01	e) 51.40 ±0.01	d) 52.00 ±0.01	f) 51.40 ±0.01	g) 50.80 ±0.01	h) 50.10 ±0.01	i) 50.00 ±0.01
2. Measurement a) to i): Active power output 50% after freezing = 100% P _{E_{max}} s=5% (40% P _{ref} / Hz), threshold frequency for start/return: 50.2Hz, P _{ref} =P _M									
Frequency [Hz]:	50.00	50.10	50.80	51.40	52.00	51.40	50.80	50.10	50.00
P _M [W]:	62500	62500	32500	2500	-27500	2500	32500	125000	125000
P _{E60} [W]:	62575	62595	33088	2937	-26676	2897	33145	125708	125630
ΔP _{E60} /P _M [%]:	0.06	0.08	0.47	0.35	0.66	0.32	0.52	0.57	0.50
Dead time(≤2s)	-	-	0.2	0.2	0.4	0.2	0.2	-	-
Step response time (≤30s)	-	-	0.4	0.6	0.8	0.4	0.2	-	-
Setting time(s)	-	-	0.4	0.6	0.8	0.4	0.2	-	-
Limit ΔP/P _{1min} :	± 10 % of P _{E_{max}} , ≤ 10% P _{E_{max}} /min								

Test:									
1-min mean value [Hz]:	a) 50.00 ±0.01	b) 50.25 ±0.01	c) 51.00 ±0.01	e) 51.50 ±0.01	d) 52.00 ±0.01	f) 51.50 ±0.01	g) 51.00 ±0.01	h) 50.50 ±0.01	i) 50.00 ±0.01
3. Measurement a) to i): Active power output -50% s=5%(40% Pref / Hz), threshold frequency for start/return: 50.2Hz, Pref=P _{Max}									
Frequency [Hz]:	50.00	50.10	50.80	51.40	52.00	51.40	50.80	50.10	50.00
P _M [W]:	-62500	-62500	-92500	- 122500	- 125000	- 122500	-92500	-62500	-62500
P _{E60} [W]:	-62784	-62830	-92372	- 122856	- 125289	- 122847	-92391	-62773	-62790
ΔP _{E60} /P _M [%]:	0.23	0.26	0.10	0.28	0.23	0.28	0.09	0.22	0.23
Dead time(≤2s)	-	-	0.2	0.2	0.2	0.2	0.2	0.2	-
Step response time (≤30s)	-	-	0.4	0.2	0.6	0.2	0.6	0.4	-
Setting time(s)	-	-	0.4	0.2	0.6	0.2	0.6	0.4	-
Limit ΔP/P _{1min} :	± 10 % of P _{Emax}								
Test:									
1-min mean value [Hz]:	a) 50.00 ±0.01	b) 50.25 ±0.01	c) 51.00 ±0.01	e) 51.50 ±0.01	d) 52.00 ±0.01	f) 51.50 ±0.01	g) 51.00 ±0.01	h) 50.40 ±0.01	i) 50.00 ±0.01
4. Measurement a) to i): Active power output 60% s=2%(100% Pref / Hz), threshold frequency for start/return: 50.5Hz, Pref=P _{Max}									
Frequency [Hz]:	50.00	50.25	51.00	51.50	52.00	51.50	51.00	50.40	50.00
P _M [W]:	75000	75000	12500	-50000	- 112500	- 112500	- 112500	75000	75000
P _{E60} [W]:	74706	74704	13511	-48891	- 113172	- 113264	- 113276	74792	74806
ΔP _{E60} /P _M [%]:	0.23	0.24	0.81	0.89	0.54	0.61	0.62	0.17	0.16
Dead time(≤2s)	-	-	0.2	0.2	0.2	-	-	-	-
Step response time (≤30s)	-	-	1.2	0.8	0.6	-	-	-	-
Setting time(s)	-	-	1.2	0.8	0.6	-	-	-	-
Limit ΔP/P _{1min} :	± 10 % of P _{Emax} , ≤ 10% P _{Emax} /min, hold 1s								

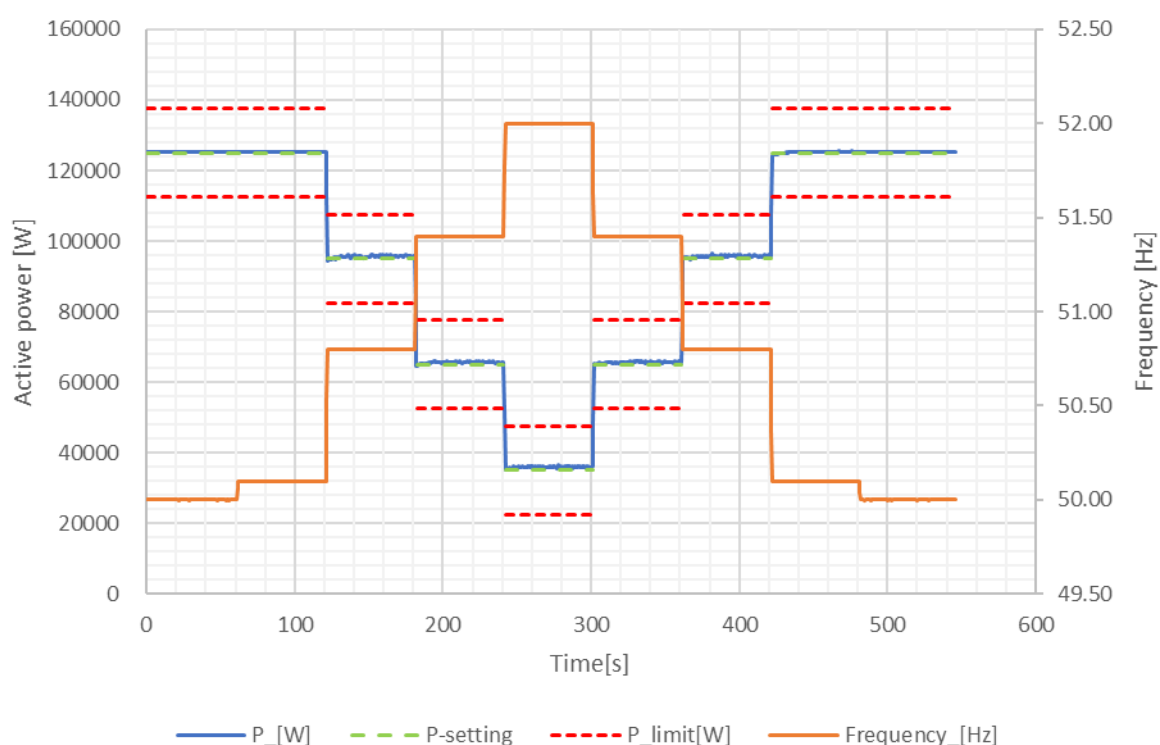
Test:

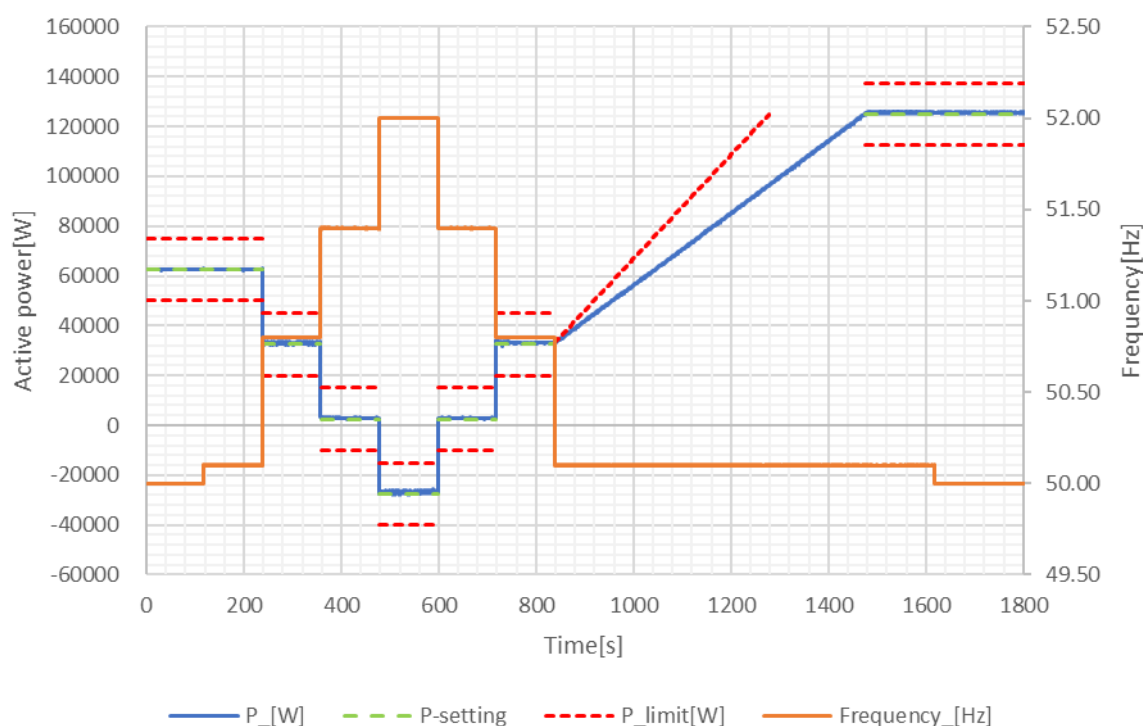
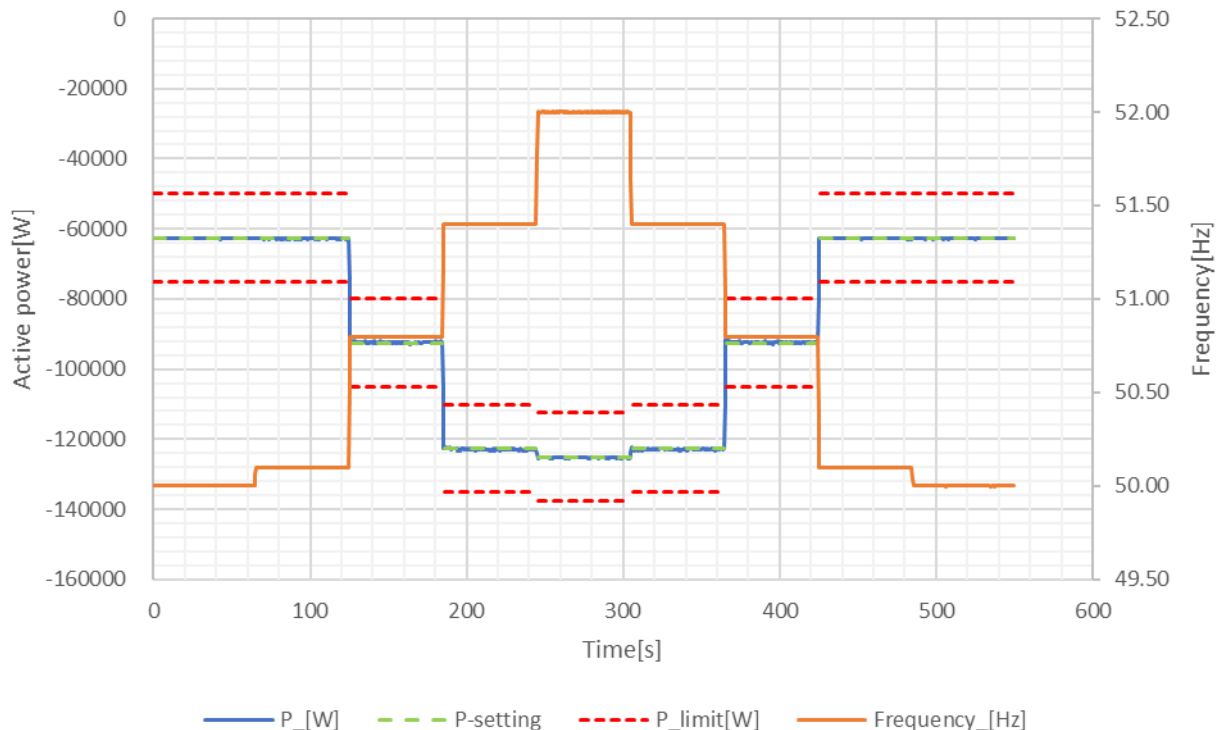
	a)	b)	c)	e)	d)	f)	g)	h)	i)
1-min mean value [Hz]:	50.00 ±0.01	50.10 ±0.01	50.80 ±0.01	51.40 ±0.01	52.00 ±0.01	51.40 ±0.01	50.80 ±0.01	50.10 ±0.01	50.00 ±0.01

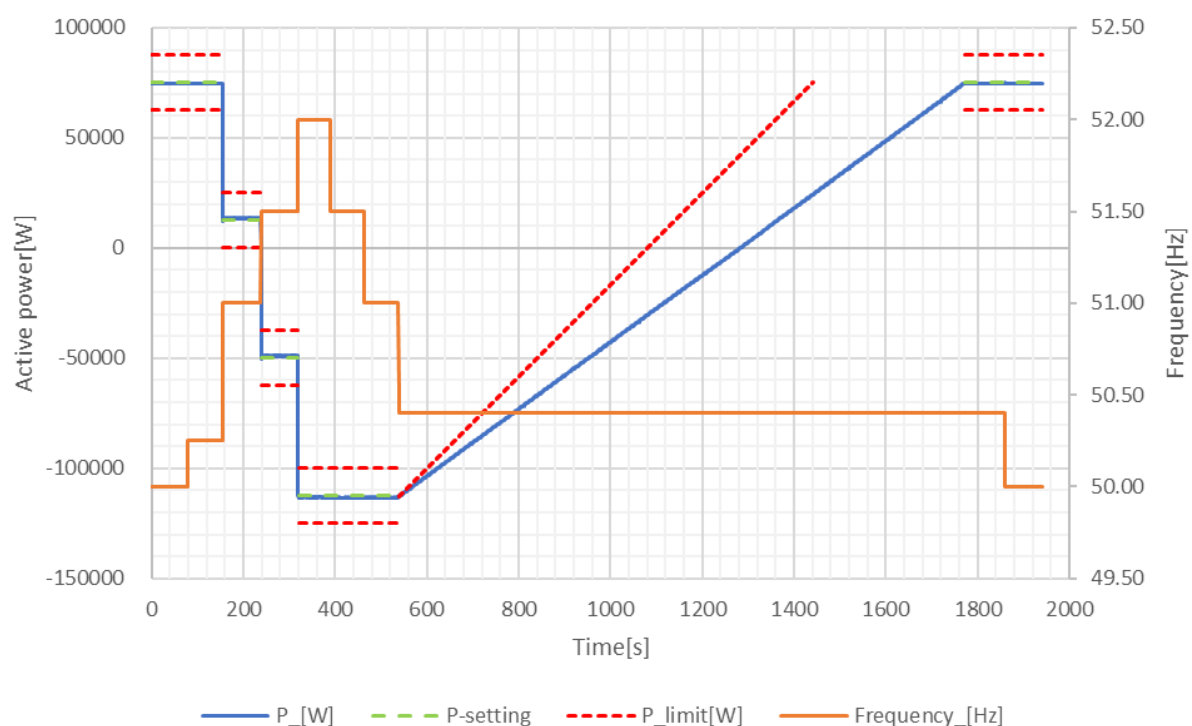
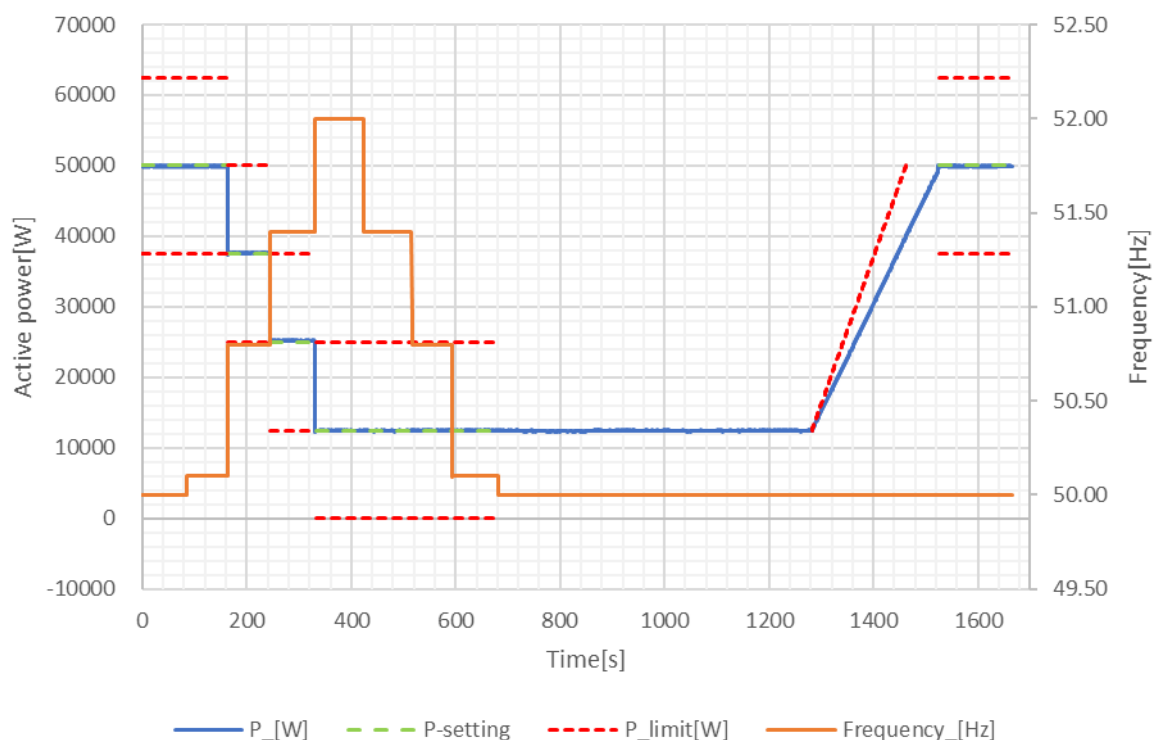
5. Measurement a) to i): Active power output 40%

s=12% (16.67% P_{ref} / Hz), threshold frequency for start: 50.2Hz, for return: 50.05Hz, $P_{ref}=P_M$, hold 600s

Frequency [Hz]:	50.00	50.10	50.80	51.40	52.00	51.40	50.80	50.10	50.00
P_M [W]:	50000	50000	37500	25000	12500	12500	12500	12500	50000
P_{E60} [W]:	49968	49975	37653	25200	12453	12440	12472	12477	49941
$\Delta P_{E60}/P_M$ [%]:	0.03	0.02	0.12	0.16	0.04	0.05	0.02	0.02	0.05
Dead time(≤2s)	-	-	0.2	0.2	0.2	-	-	-	-
Step response time (≤30s)	-	-	0.6	1.0	1.0	-	-	-	-
Setting time(s)	-	-	0.6	1.0	1.0	-	-	-	-

Limit $\Delta P/P_{1min}$: $\pm 10\%$ of $P_{E_{max}}$, $\leq 10\%$ $P_{E_{max}}$ /min, hold 600s**Graph of Measurement 1.: Active power output 100% $P_{E_{max}}$** 

Graph of Measurement 2.: Active power output 50% P_{Emax} **Graph of Measurement 3.: Active power output -50% P_{Emax}** 

Graph of Measurement 4.: Active power output 60% P_{Emax} **Graph of Measurement 5.: Active power output 40% P_{Emax}** 

Notes:

Setting (a) for other non-synchronous generating technology

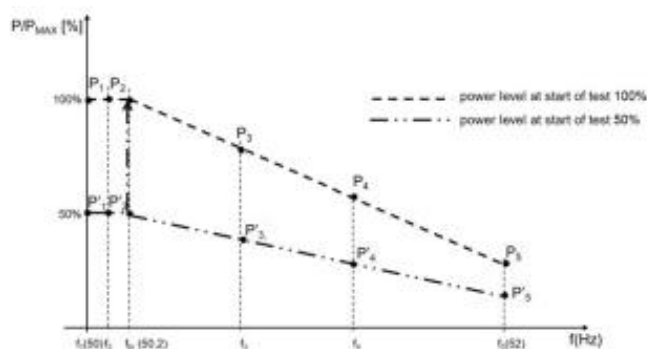


Figure 19 — Other non-synchronous generating technology ($P_{ref} = P_M$) without configured deactivation threshold, setting (a), example

Setting (b) other non-synchronous generating technology

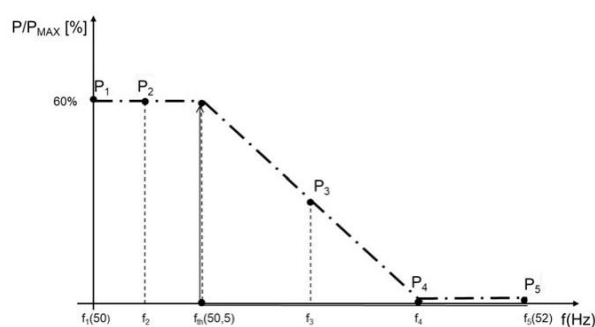


Figure 20 — Other non-synchronous generating technology ($P_{ref} = P_M$) with configured deactivation threshold, setting (b), example

Setting (c) other non-synchronous generating technology

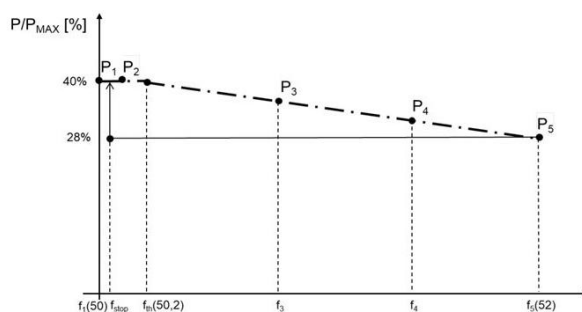


Figure 21 — Other non-synchronous generating technology ($P_{ref} = P_M$) with configured deactivation threshold, setting (c), example

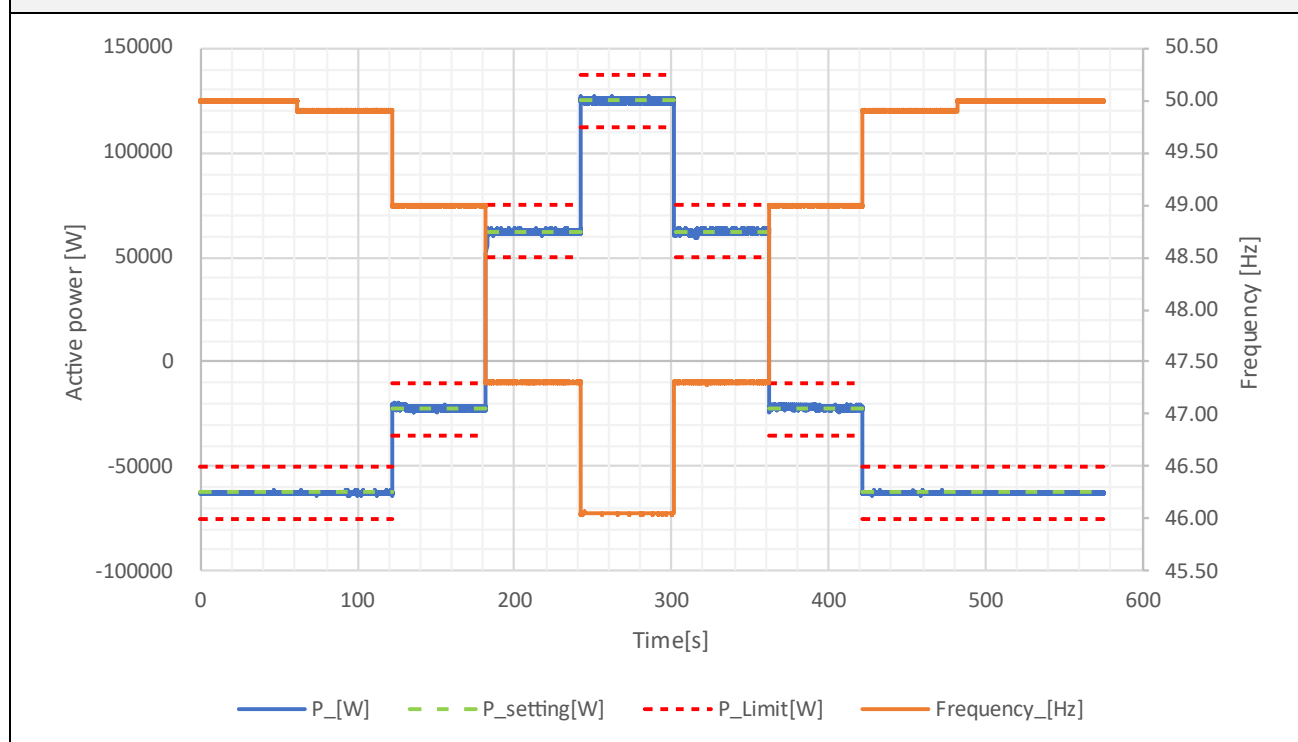
5.4.3.3 Power response to under-frequency (For system with energy storage)								P	
Test a:									
1-min mean value [Hz]:	a) 50.00 ±0.01	b) 49.90 ±0.01	c) 49.00 ±0.01	e) 47.30 ±0.01	d) 46.05 ±0.01	f) 47.30 ±0.01	g) 49.00 ±0.01	h) 49.90 ±0.01	i) 50.00 ±0.01
1. Measurement a) to i): Active power output =-50% P _{E_{max}} s=5% (40% P _{ref} / Hz), threshold frequency for start/return: 49.8Hz, P _{ref} =P _{E_{max}}									
Frequency [Hz]:	50.00	49.90	49.00	47.30	46.05	47.30	49.00	49.90	50.00
P _M [W]:	-62500	-62500	-22500	62500	125000	62500	-22500	-62500	-62500
P _{E60} [W]:	-62716	-62699	-22163	62285	124965	62411	-21907	-62677	-62696
ΔP _{E60} /P _M [%]:	0.17	0.16	0.27	0.17	0.03	0.07	0.47	0.14	0.16
Dead time(≤2s)	-	-	0.4	0.4	0.2	0.4	0.2	0.2	-
Step response time(≤30s)	-	-	2.2	2.4	2.2	1.6	1.2	1.2	-
Setting time(s)	-	-	2.2	2.4	2.2	1.6	1.2	1.2	-
Limit ΔP/P _{1min} :	± 10 % of P _{E_{max}}								
Test b:									
1-min mean value [Hz]:	a) 50.00 ±0.01	b) 49.75 ±0.01	c) 49.25 ±0.01	e) 48.75 ±0.01	d) 46.00 ±0.01	f) 48.75 ±0.01	g) 49.25 ±0.01	h) 49.75 ±0.01	i) 50.00 ±0.01
2. Measurement a) to i): Active power output =0% P _{E_{max}} s=2% (100% P _{ref} / Hz), threshold frequency for start/return: 49.5Hz, P _{ref} =P _{E_{max}}									
Frequency [Hz]:	50.00	49.75	49.25	48.75	46.00	48.75	49.25	49.75	50.00
P _M [W]:	0	0	31250	93750	125000	93750	31250	0	0
P _{E60} [W]:	383	296	31847	93870	125098	93969	31854	426	430
ΔP _{E60} /P _M [%]:	0.31	0.24	0.48	0.10	0.08	0.18	0.48	0.34	0.34
Dead time(≤2s)	-	-	0.4	0.2	0.4	0.4	0.6	0.4	-
Step response time(≤30s)	-	-	2.6	1.8	1.2	1.2	1.2	0.8	-
Setting time(s)	-	-	2.6	1.8	1.2	1.2	1.2	0.8	-
Limit ΔP/P _{1min} :	± 10 % of P _{E_{max}}								

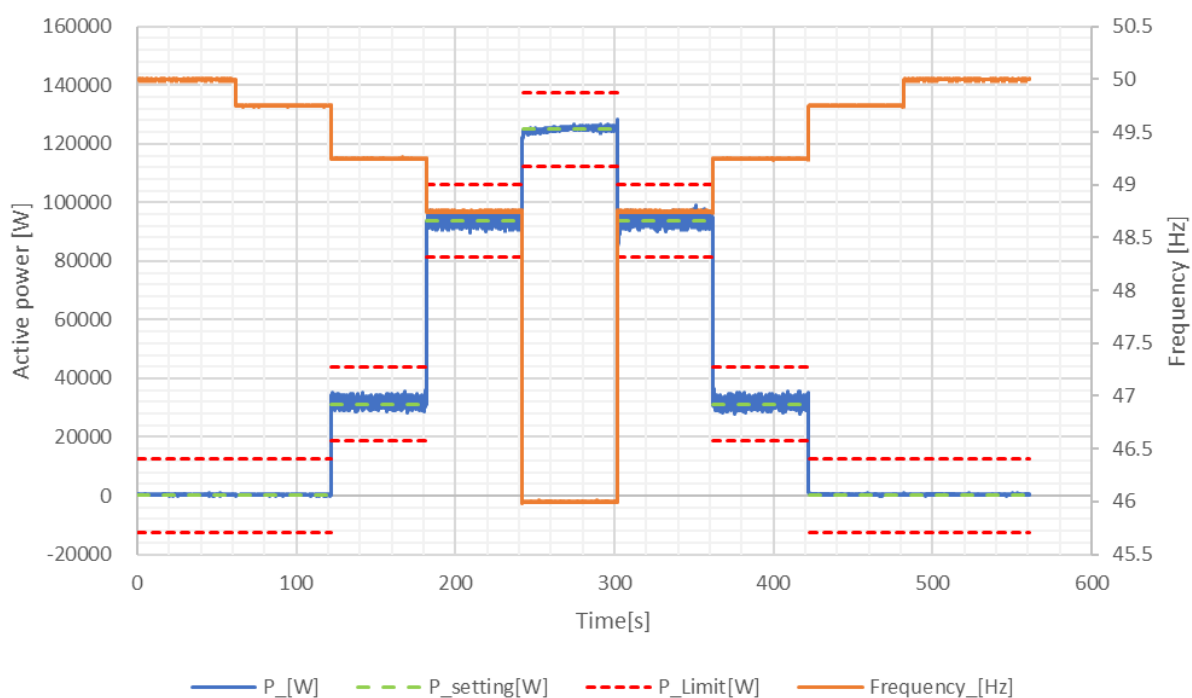
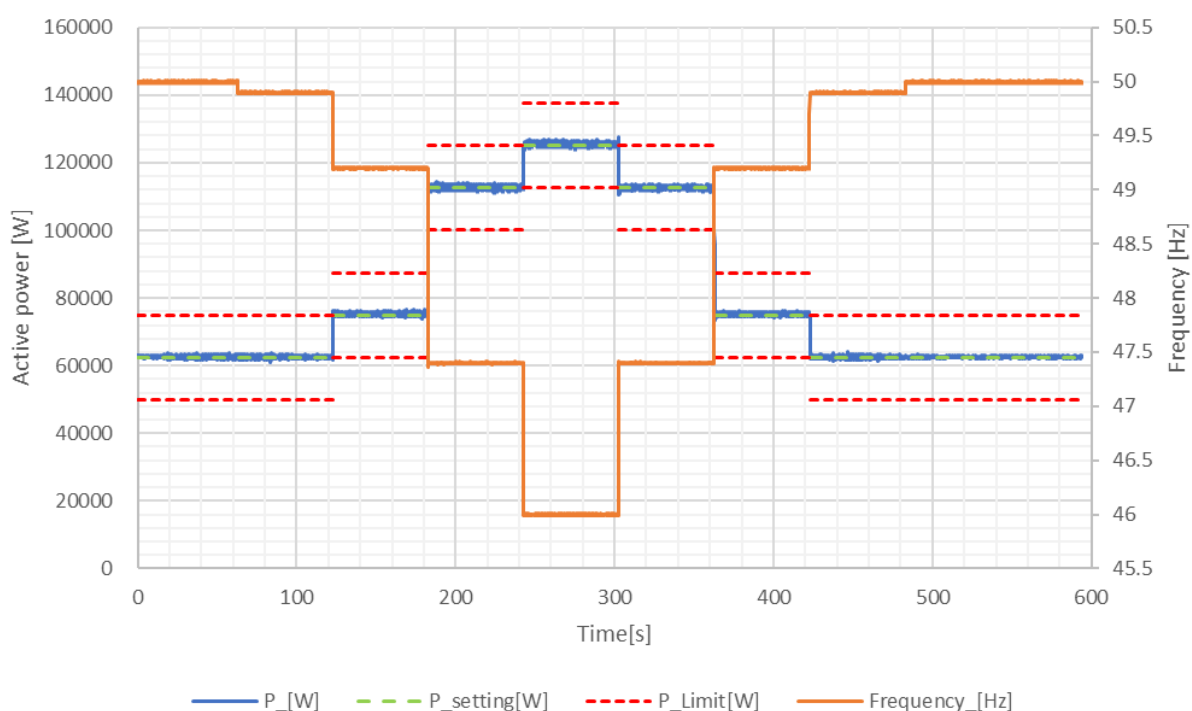
Test c:

	a)	b)	c)	e)	d)	f)	g)	h)	i)
1-min mean value [Hz]:	50.00 ±0.01	49.90 ±0.01	49.20 ±0.01	47.40 ±0.01	46.00 ±0.01	47.40 ±0.01	49.20 ±0.01	49.90 ±0.01	50.00 ±0.01

3. Measurement a) to i): Active power output =50% $P_{E\max}$ $s=12\%$ (16.67% P_{ref} / Hz), threshold frequency for start/return: 49.8Hz, $P_{ref}=P_{E\max}$

Frequency [Hz]:	50.00	49.90	49.20	47.40	46.00	47.40	49.20	49.90	50.00
P_M [W]:	62500	62500	75000	112500	125000	112500	75000	62500	62500
P_{E60} [W]:	62598	62572	75299	112824	125620	112759	75266	62531	62569
$\Delta P_{E60}/P_M$ [%]:	0.08	0.06	0.24	0.26	0.50	0.21	0.21	0.02	0.06
Dead time($\leq 2s$)	-	-	0.2	0.4	0.4	0.4	0.2	0.2	-
Step response time($\leq 30s$)	-	-	1.2	2.4	0.6	0.8	0.4	0.6	-
Setting time(s)	-	-	1.2	2.4	0.6	0.8	0.4	0.6	-

Limit $\Delta P/P_{1min}$: $\pm 10\%$ of $P_{E\max}$ **Graph of Measurement 1.: Active power output 0% $P_{E\max}$** 

Graph of Measurement 2.: Active power output 0% P_{Emax} **Graph of Measurement 3.: Active power output 50% P_{Emax}** 

Notes:

Setting (a) for other generators

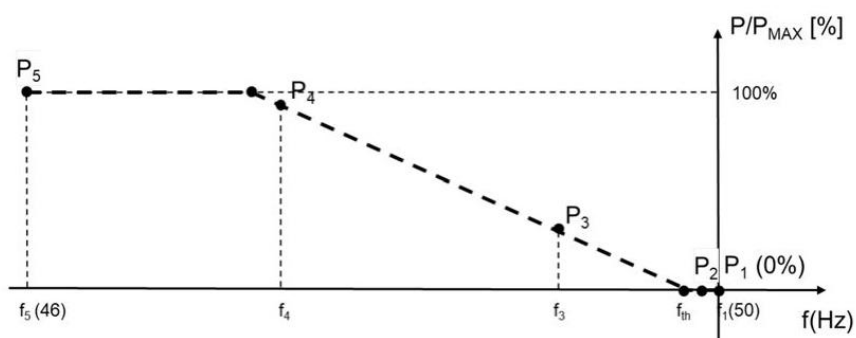


Figure 25 — Other generators: default values, example for interval frequency 46 – 50 Hz, setting (a)

Setting (b) other generators

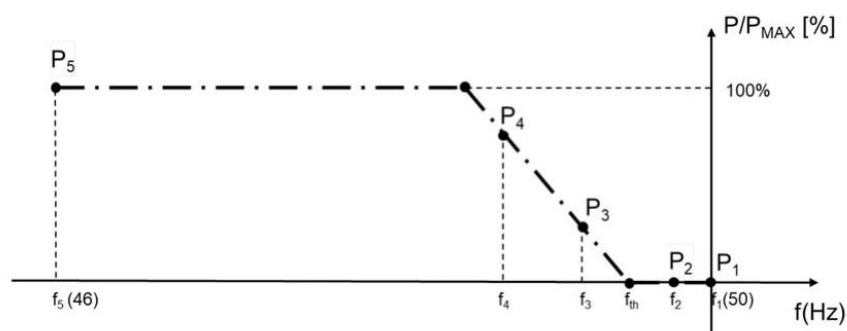


Figure 23 — Pref = Pmax, s = 2 %, fth = 49,5Hz, example for interval frequency 46 – 50 Hz, setting (b)

Setting (c) other generators

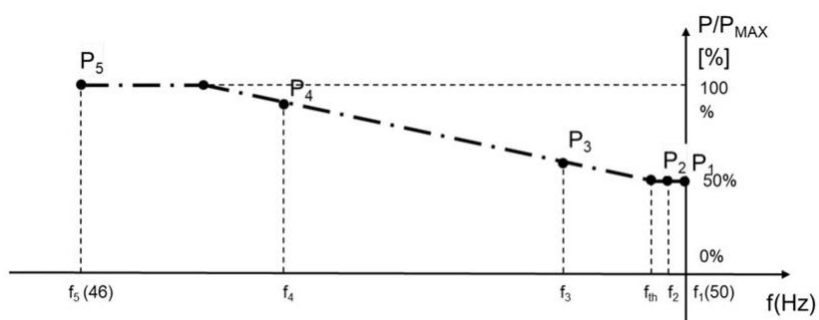


Figure 24 — Pref = Pmax, s = 12 %, fth = 49,8Hz, example for interval frequency 46 – 50 Hz, setting (c)

EN 50549-10:2022		
Clause	Test items	Remark
5.5	Power specification and response to voltage variations	
5.5.1	Power capabilities assessment - voltage support by reactive power	P
5.5.2	Voltage support by reactive power - test to determine the reactive power control modes	
5.5.2.3	Verification procedure for Set point control	P
5.5.2.4	Verification procedure for voltage related control mode for reactive power Q(U)	P
5.5.2.5	Verification procedure for power related control modes for reactive power	P

5.5.1 Power capabilities assessment - voltage support by reactive power					P
5.5.1.3 Verification procedure to determine the power capability (active, reactive, apparent) at nominal voltage					
Q = 0 reactive power					
Rating power [%]	Active power [W]	Reactive power [Var]	Power factor [cos φ]	Voltage [V]	ΔQ ≤ 2%Smax
10%	12942	1665	0.9914	229.93	1.33
20%	25590	1851	0.9972	229.96	1.48
30%	37773	1723	0.9989	229.99	1.38
40%	50646	41	0.9992	230.01	0.03
50%	63020	-685	0.9995	230.03	0.55
60%	75597	-1537	0.9997	230.05	1.23
70%	87963	-2081	0.9997	230.05	1.66
80%	100220	-2235	0.9997	230.04	1.79
90%	112996	-2262	0.9998	230.04	1.81
100%	125200	-2429	0.9998	230.02	1.94
Q _{max} * under-excited					
Rating power [%]	Active power [W]	Reactive power [Var]	Power factor [cos φ]	Voltage [V]	ΔQ ≤ 2%Smax
10%	12489	-75637	0.1629	229.85	0.51
20%	25298	-75580	0.3174	229.89	0.46
30%	37960	-75626	0.4486	229.93	0.50
40%	50563	-75605	0.5559	229.98	0.48
50%	63171	-75653	0.6409	230.02	0.52
60%	75848	-75662	0.7080	230.06	0.53
70%	88460	-75632	0.7601	230.10	0.51
80%	101170	-75671	0.8008	230.15	0.54
90%	113880	-75724	0.8327	230.19	0.58
100%**	114965	-75719	0.8351	230.20	0.58

Q_{max}* over-excited					
Rating power [%]	Active power [W]	Reactive power [Var]	Power factor [cos φ]	Voltage [V]	ΔQ ≤ 2%S _{max}
10%	12246	75180	0.1608	230.10	0.14
20%	24863	75288	0.3136	230.15	0.23
30%	37447	75345	0.4451	230.19	0.28
40%	50065	75424	0.5530	230.23	0.34
50%	62780	75609	0.6388	230.28	0.49
60%	75443	75730	0.7058	230.32	0.58
70%	88188	75803	0.7584	230.37	0.64
80%	100844	75835	0.7992	230.41	0.67
90%	113722	76127	0.8310	230.46	0.90
100%**	114709	76047	0.8335	230.46	0.84

* Q_{max} is equal to 60%P_n.

**Due to the limit of max. apparent power, it can't reach target output power.

Assessment criterion:

The following shall be considered: Measurements shall be sampled during continuous operation only. Measurements shall be recorded and evaluated as 1 min average values. For the determination of the dynamic behaviour, 1 period RMS values shall be evaluated. During the test, the generated active power shall be kept stable within $\pm 5\% \cdot S_n$ compared to the value set for the entire duration of each test sequence. The active and reactive power shall be calculated based on the positive sequence component of the fundamental of the voltages and currents at the generating unit terminals. The active factor shall be calculated based on the positive sequence component of the fundamental of the voltages and currents at the generating unit terminals. The active power, reactive power and the active factor, frequency, RMS voltage, RMS current at the generating unit terminals shall be recorded during tests.

Verification procedure to determine the power capability (active, reactive, apparent) at 5.5.1.3 The verification shall be conducted operating the EUT at nominal voltage and frequency and different active power levels as follows: The generating unit manufacturer shall provide the capability curve (Figure 26 gives a graphical representation of the minimum and optional capabilities at nominal voltage). The maximum underexcited and overexcited reactive power shall be measured over a period of 1 min for each 10 % of P_n, from 0 % to maximum active power as declared by manufacturer. The values P_{max}, S_{max} and Q_{max over} and Q_{max under} shall be measured over a period of 10 min.

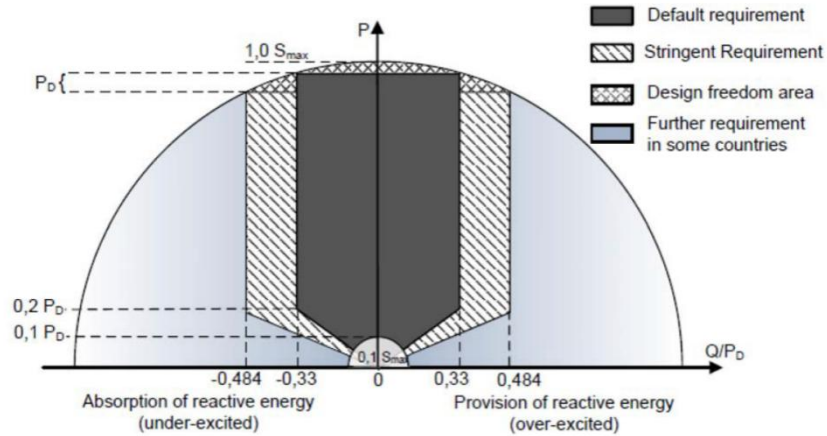


Figure 26 — reactive power capability at nominal voltage

5.5.1 Power capabilities assessment - voltage support by reactive power				P
5.5.1.6 Verification procedure for reactive power capability considering voltage range				
Test: 85%U _n				
Inductive supply reactive power				
Rating power [%]	Voltage [V]	Active power [W]	Reactive power [Var]	Power factor [cos φ]
0%	195.1	6278	-4683	0.8016
10%	195.3	12714	-9620	0.7974
20%	195.3	25239	-18947	0.7997
30%	195.4	37658	-28294	0.7995
40%	195.4	50365	-37707	0.8005
50%	195.4	62673	-46811	0.8012
60%	195.3	75153	-56114	0.8013
70%	195.3	87576	-65312	0.8016
80%*	195.3	93050	-69305	0.8020
90%*	195.3	93122	-69377	0.8019
100%*	195.3	93174	-69442	0.8018
Capacitive supply reactive power				
Rating power [%]	Voltage [V]	Active power [W]	Reactive power [Var]	Power factor [cos φ]
0%	195.1	6212	4594	0.8040
10%	195.3	12648	9285	0.8061
20%	195.2	25177	18587	0.8045
30%	195.2	37685	28017	0.8025
40%	195.2	50303	37484	0.8019
50%	195.1	62733	46705	0.8021
60%	195.1	75155	56047	0.8016
70%	195.0	87596	65206	0.8022
80%*	195.0	93028	69193	0.8024
90%*	195.0	93060	69215	0.8024
100%*	195.0	93065	69213	0.8024

Test: 90%U _n				
Inductive supply reactive power				
Rating power [%]	Voltage [V]	Active power [W]	Reactive power [Var]	Power factor [cos φ]
0%	206.6	6347	-4669	0.8055
10%	206.8	12686	-9534	0.7994
20%	206.9	25119	-18867	0.7996
30%	206.9	37675	-28231	0.8002
40%	206.9	50468	-37728	0.8009
50%	206.9	62916	-47031	0.8010
60%	206.9	75458	-56356	0.8012
70%	206.8	87925	-65617	0.8014
80%	206.8	98612	-73523	0.8017
90%*	206.8	98617	-73521	0.8017
100%*	206.8	98697	-73582	0.8017
Capacitive supply reactive power				
Rating power [%]	Voltage [V]	Active power [W]	Reactive power [Var]	Power factor [cos φ]
0%	206.6	6257	4661	0.8019
10%	206.8	12672	9294	0.8064
20%	206.8	25228	18626	0.8045
30%	206.7	37817	28149	0.8022
40%	206.7	50397	37537	0.8020
50%	206.7	62937	46934	0.8016
60%	206.6	75413	56155	0.8021
70%	206.5	87735	65390	0.8018
80%	206.5	98574	73318	0.8024
90%*	206.5	98617	73355	0.8024
100%*	206.5	98658	73398	0.8023

Test: 95%U _n				
Inductive supply reactive power				
Rating power [%]	Voltage [V]	Active power [W]	Reactive power [Var]	Power factor [cos φ]
0%	218.1	6184	-4614	0.8014
10%	218.4	12629	-9358	0.8034
20%	218.4	25271	-18658	0.8045
30%	218.4	37769	-27731	0.8061
40%	218.4	50344	-36941	0.8062
50%	218.4	62971	-46042	0.8072
60%	218.4	75463	-55233	0.8069
70%	218.4	87892	-64234	0.8074
80%	218.3	100222	-73145	0.8077
90%*	218.3	104501	-76213	0.8080
100%*	218.3	104889	-76495	0.8080
Capacitive supply reactive power				
Rating power [%]	Voltage [V]	Active power [W]	Reactive power [Var]	Power factor [cos φ]
0%	218.2	6580	4772	0.8095
10%	218.3	12835	9567	0.8017
20%	218.3	25316	18912	0.8011
30%	218.3	37832	28285	0.8009
40%	218.2	50226	37584	0.8006
50%	218.2	62830	47033	0.8005
60%	218.1	75394	56423	0.8006
70%	218.1	87727	65611	0.8008
80%	218.0	100342	74979	0.8011
90%*	218.0	104011	77717	0.8011
100%*	218.0	104062	77761	0.8011

Test: 100%U _n				
Inductive supply reactive power				
Rating power [%]	Voltage [V]	Active power [W]	Reactive power [Var]	Power factor [cos φ]
0%	229.7	6114	-4519	0.8042
10%	229.9	12646	-9409	0.8023
20%	229.9	25244	-18704	0.8035
30%	229.9	37587	-27934	0.8026
40%	229.9	50140	-37332	0.8021
50%	229.9	62670	-46759	0.8015
60%	229.9	75162	-56052	0.8016
70%	229.9	87731	-65349	0.8020
80%	229.9	100137	-74512	0.8023
90%*	229.9	109827	-81755	0.8022
100%*	229.9	109897	-81793	0.8022
Capacitive supply reactive power				
Rating power [%]	Voltage [V]	Active power [W]	Reactive power [Var]	Power factor [cos φ]
0%	229.7	6115	4561	0.8015
10%	229.8	12839	9622	0.8002
20%	229.8	25272	18890	0.8010
30%	229.8	37627	28152	0.8007
40%	229.8	50231	37547	0.8010
50%	229.7	62858	47087	0.8003
60%	229.7	75400	56349	0.8010
70%	229.6	87977	65816	0.8007
80%	229.6	100438	75065	0.8010
90%*	229.5	109386	81748	0.8010
100%*	229.5	109434	81774	0.8011

Test: 105%U _n				
Inductive supply reactive power				
Rating power [%]	Voltage [V]	Active power [W]	Reactive power [Var]	Power factor [cos φ]
0%	241.2	6102	-4545	0.8019
10%	241.4	12828	-9444	0.8053
20%	241.4	25344	-18719	0.8044
30%	241.5	37918	-27832	0.8061
40%	241.5	50503	-37027	0.8065
50%	241.5	63033	-46476	0.8049
60%	241.5	75271	-55455	0.8051
70%	241.5	87916	-64634	0.8057
80%	241.4	100214	-73683	0.8057
90%	241.4	110736	-81932	0.8039
100%*	241.4	110750	-81936	0.8039
Capacitive supply reactive power				
Rating power [%]	Voltage [V]	Active power [W]	Reactive power [Var]	Power factor [cos φ]
0%	241.3	6081	4514	0.8028
10%	241.4	12726	9360	0.8056
20%	241.4	25214	18707	0.8031
30%	241.4	37973	28244	0.8024
40%	241.3	50454	37625	0.8016
50%	241.3	62890	46986	0.8011
60%	241.3	75641	56518	0.8011
70%	241.2	87964	65737	0.8010
80%	241.2	100480	74981	0.8014
90%	241.1	111055	81868	0.8049
100%*	241.1	111059	81867	0.8049

Test: 110%U _n				
Inductive supply reactive power				
Rating power [%]	Voltage [V]	Active power [W]	Reactive power [Var]	Power factor [cos φ]
0%	252.8	6097	-4475	0.8061
10%	253.0	12844	-9467	0.8049
20%	253.0	25382	-18511	0.8080
30%	253.0	37994	-27748	0.8076
40%	253.0	50728	-37084	0.8073
50%	253.0	63117	-46304	0.8063
60%	253.0	75375	-55268	0.8064
70%	253.0	89246	-65385	0.8067
80%	253.0	100469	-73651	0.8065
90%	252.9	111203	-81458	0.8067
100%*	252.9	111269	-81487	0.8068
Capacitive supply reactive power				
Rating power [%]	Voltage [V]	Active power [W]	Reactive power [Var]	Power factor [cos φ]
0%	252.8	6077	4472	0.8053
10%	252.9	12669	9442	0.8018
20%	252.9	25148	18568	0.8045
30%	252.9	37902	27800	0.8063
40%	252.8	50441	37081	0.8057
50%	252.8	62882	46290	0.8053
60%	252.8	75580	55536	0.8058
70%	252.7	87940	64635	0.8058
80%	252.7	100480	73780	0.8060
90%	252.6	111015	81543	0.8059
100%*	252.6	111103	81618	0.8059
Note: * Due to the limit of max apparent power or max output current, it can't reach target output power.				

Assessment criterion:

The power factor resulting in each of the measurement points between 10 % and 90 % of the nominal power is equal to or lower than 0.90 both in over excited and under excited operation.

Test method:

The voltage dependency of PQ-characteristic shall be evaluated according to the following procedures:

Non-synchronous generating units:

The manufacturer shall provide a characteristic or model of the voltage dependency of the reactive power capability represented in a P-Q diagram at different voltage values (e.g. 110 % U_n , 100 % U_n and 90 % U_n); the voltage dependency can be also represented in the form of a U-Q/P diagram.

For the verification the following applies: two tests shall be conducted at the maximum power as verified according to 5.5.1.3. and for each Q and $Q_{\max \text{ under}}$. In case of generating units operating in a Q-priority mode, the test shall be conducted with an active factor not below 0,9. $\max \text{ over}$. The tests shall be conducted for under voltage and over voltage. The voltage level shall be chosen to show the declared characteristic. If the declared characteristic does not show a voltage dependency, the voltage deviation shall be at least 5 % of nominal voltage.

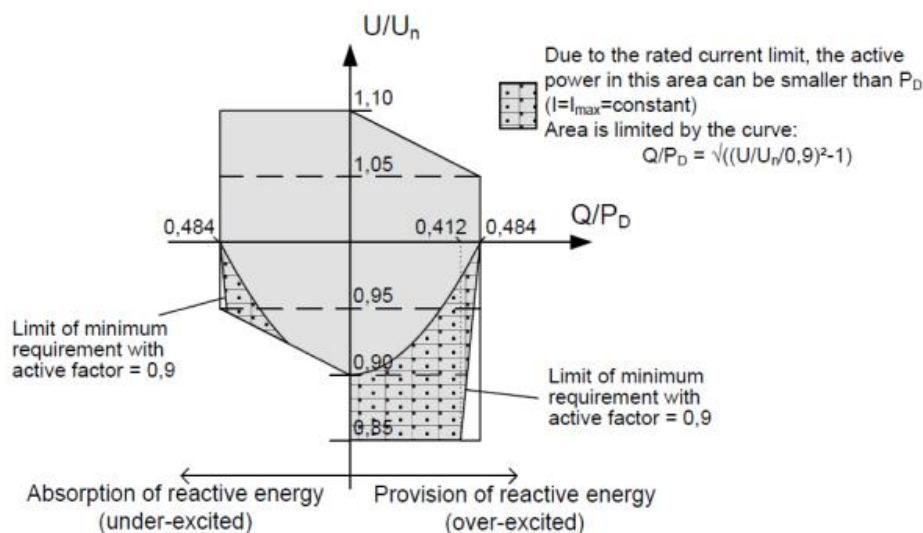


Figure 30 — Example: U-Q normalized diagram taken from the EN 50549-1

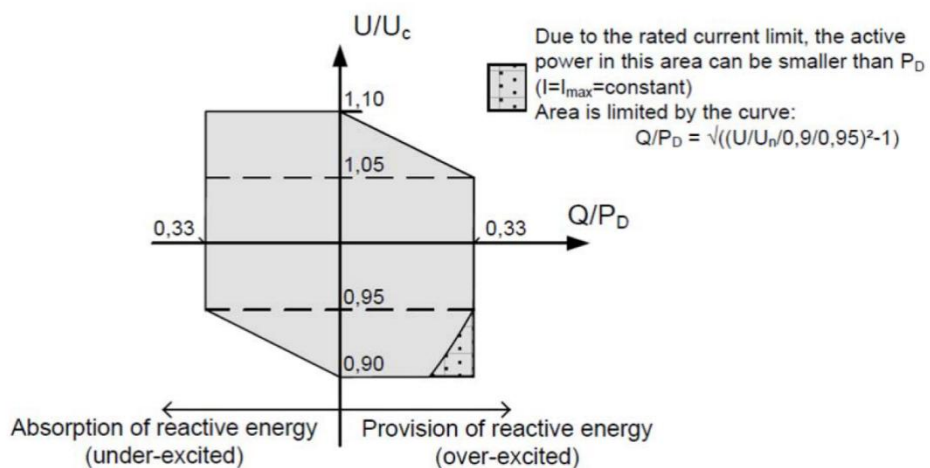


Figure 31 — Example of U-Q normalized diagram taken from the EN 50549-2

5.5.2.3.2 Reactive power set point control accuracy							P
Q _{max} [Var]	75000			Q _{min} [Var]		-75000	
Test no.1: Test for reactive power set point control accuracy							
Test: 50%P _{E_{max}} ±10%P _n							
Reactive power step Q/P _n (p.u.)	Voltage	Set point value		Measure value		Deviation	
	(%)	Var	Q/P _n (%)	Var	Q/P _n (%)	Var	Q/S _n (%)
Q=0	100.1	0	0	-798	-0.64	798	0.64
Q _{max} (over-excited)	100.1	75000	60.0	75322	60.26	322	0.26
Q _{min} (under-excited)	100.0	-75000	-60.0	-75575	-60.46	575	0.46
Q=0	100.1	0	0	-801	-0.64	801	0.64
ΔLimit Q≤2%S _{max}							
Test no.2: Test for reactive power set point control accuracy							
50±10%P _n (Tau=3s) shortest time constant (not longer than 3s)							
Reactive power set point step p.u.	Generating unit response time setting		Measured response behaviour				
			dead time		response time/ settling time		
Q=0→Q=Q _{max} over	9s		0.2		9.8		
Q=Q _{max} over →Q=Q _{min} under	9s		0.2		9.4		
Q=Q _{min} under →Q=0	9s		0.2		9.6		
50±10%P _n (Tau=60s) (if longest time>60s, then test 60s)							
Reactive power set point step p.u.	Generating unit response time setting		Measured response behaviour				
			dead time		response time/ settling time		
Q=0→Q=Q _{max} over	180s		0.4		171.8		
Q=Q _{max} over →Q=Q _{min} under	180s		0.6		176.0		
Q=Q _{min} under →Q=0	180s		0.6		181.6		
-Note:							

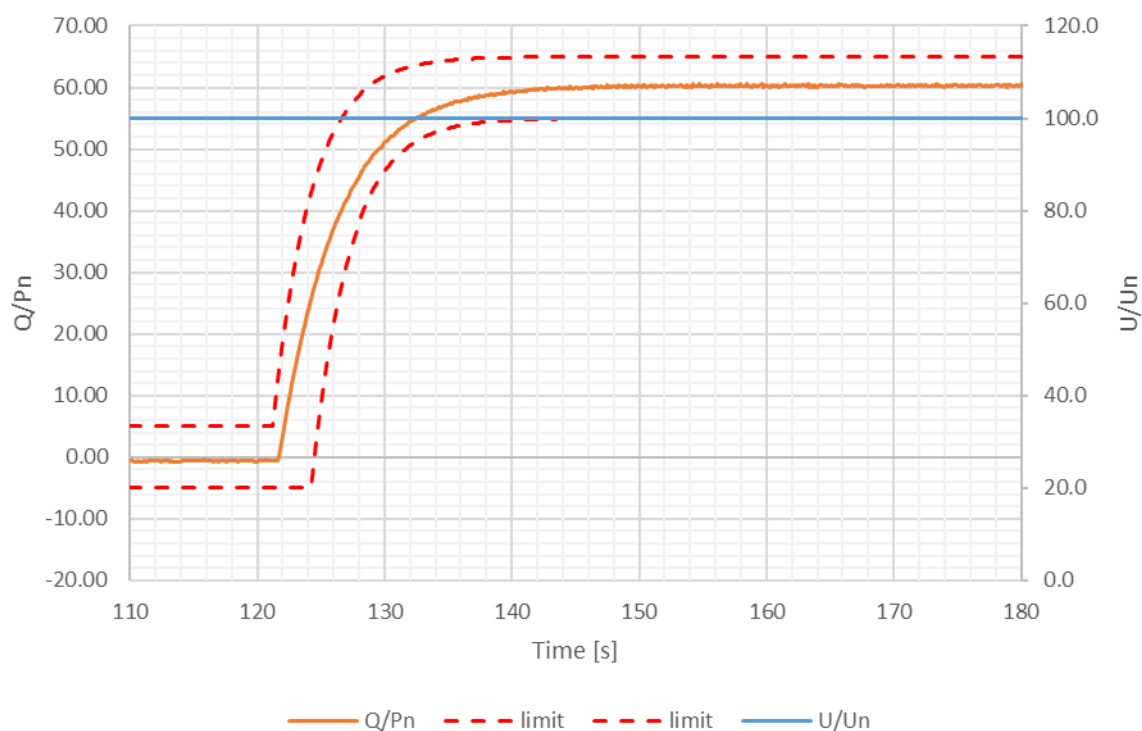
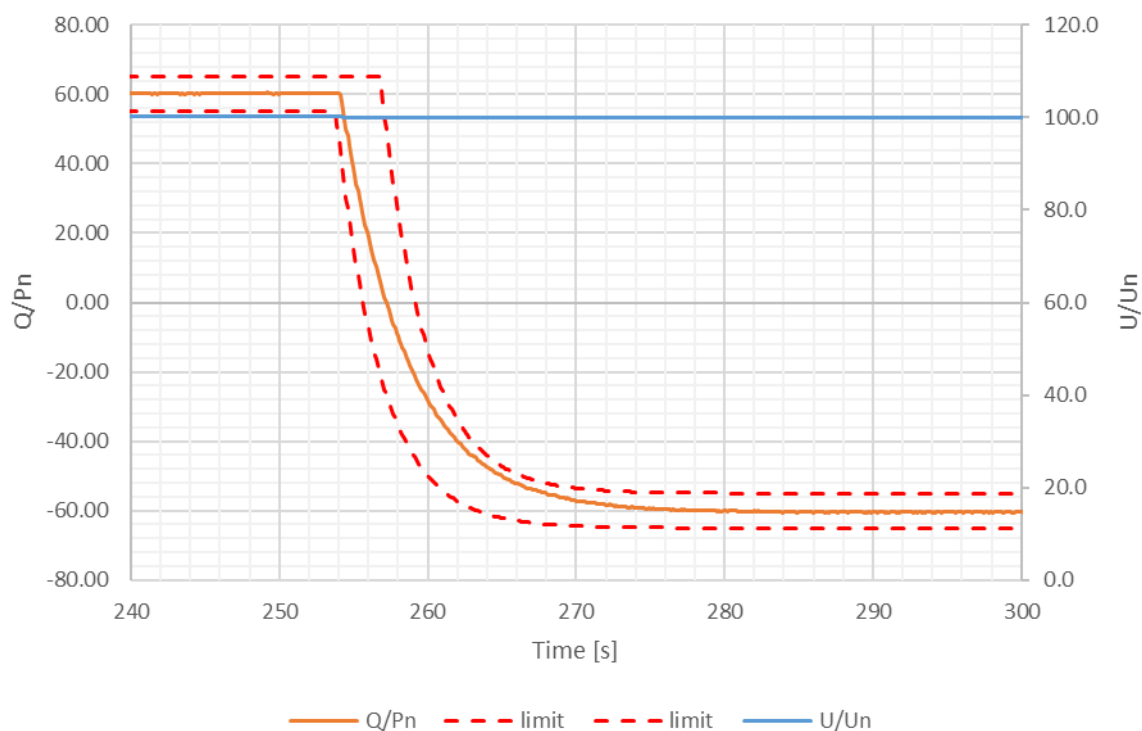
Diagram for 50%P_n (Tau=3s) 0 → Q_{max over}Diagram for 50%P_n (Tau=3s) Q_{max over} → Q_{min under}

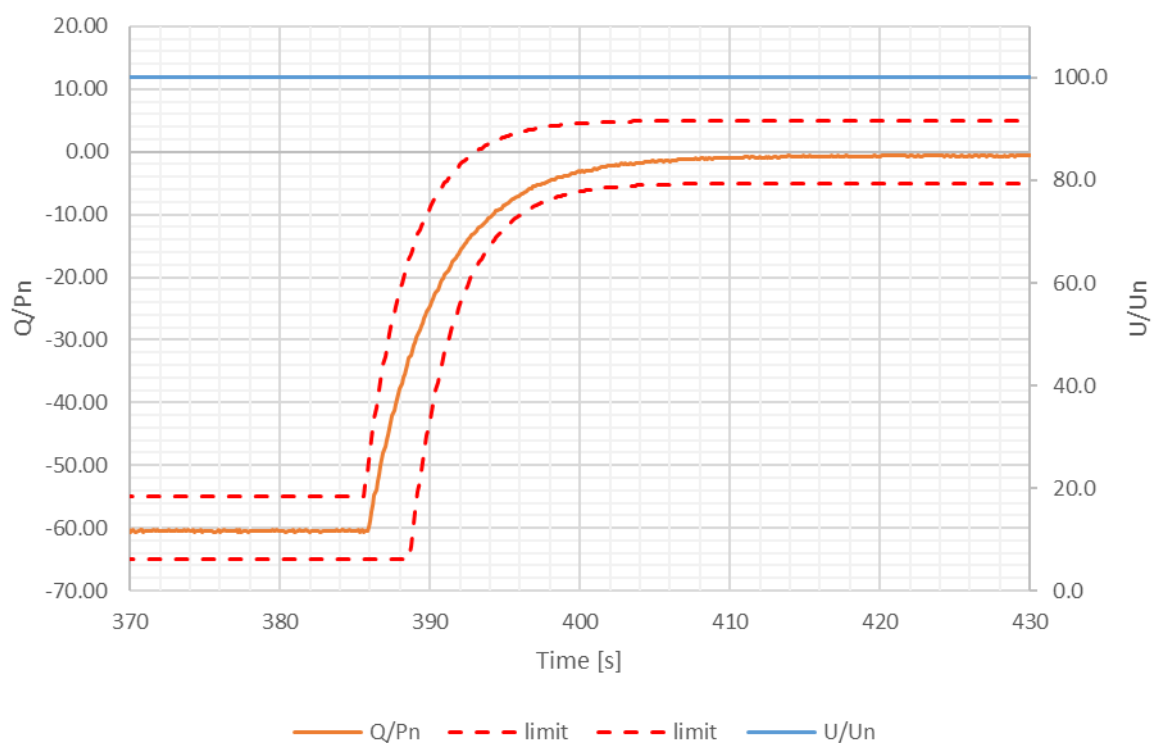
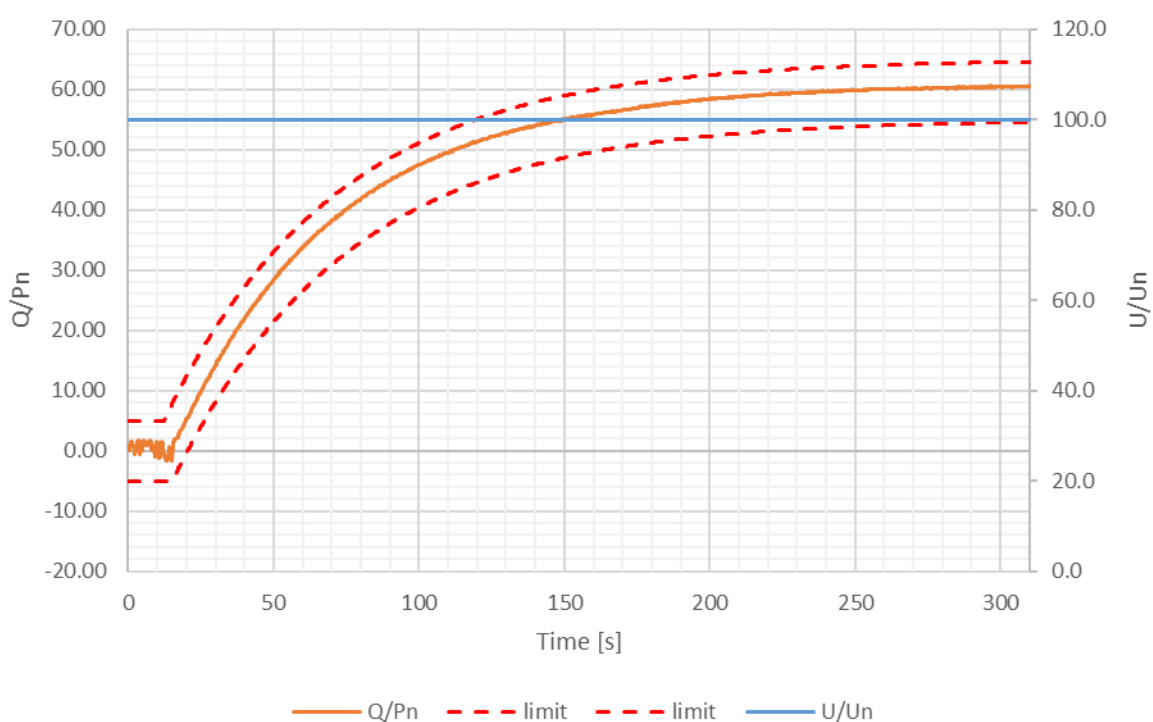
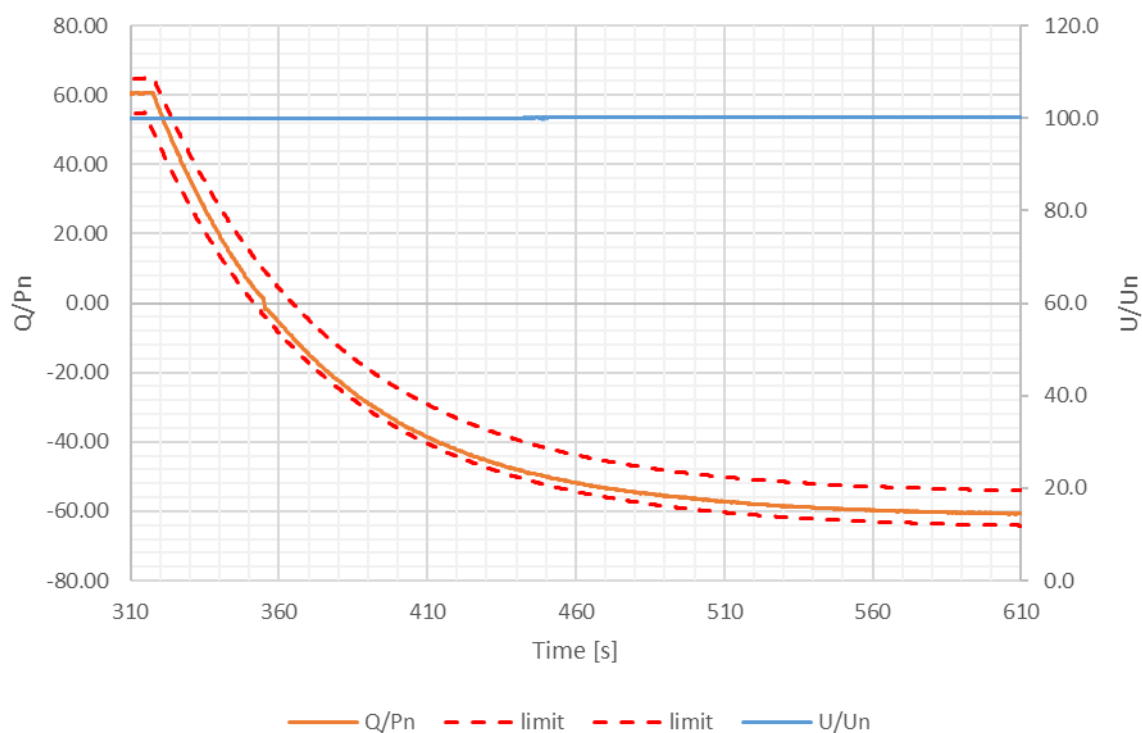
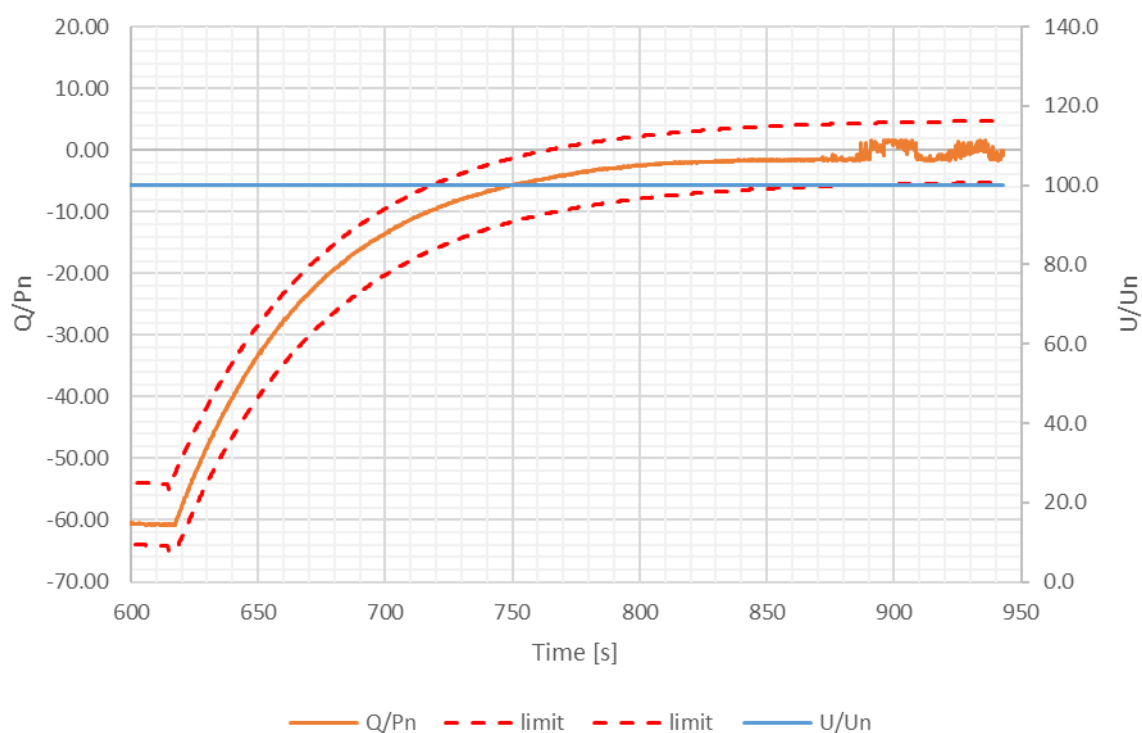
Diagram for 50%P_n (Tau=3s) Q_{min} under → 0Diagram for 50%P_n (Tau=60s) 0 → Q_{max} over

Diagram for 50%P_n (Tau=60s) Q_{max} over → Q_{min} underDiagram for 50%P_n (Tau=60s) Q_{min} under → 0

5.5.2.4 Verification procedure for voltage related control mode for reactive power Q(U)							P
5.5.2.4.2 Test procedure for steady-state behaviour							
Q _{max} [Var]		75000		Q _{min} [Var]		-75000	
Setting (a)							
Setting value		Measurement value					Limit
Un	Pn	Grid voltage(%)	Active power (%)	Reactive power (%)	Reactive power Target (%)	PF	ΔU≤±1%Un ΔQ≤±2%Sn or PF≥0.4
100%	100%	100.1	99.8	-0.7	0	0.9999	
103%	100%	103.1	100.1	-0.9	0	0.9999	
105%	100%	105.1	100.0	-1.5	0	0.9999	
106%	100%	106.1	100.0	-1.8	0	0.9999	
107%	100%	107.1	100.0	-49.5	-50%Qmax	0.9586	
108%	100%**	108.2	91.6	-99.3	-100%Qmax	0.8384	
110%	100%**	110.2	92.5	-99.2	-100%Qmax	0.8411	
112%	100%**	112.2	92.5	-99.3	-100%Qmax	0.8407	
115%	100%**	115.2	92.5	-99.0	-100%Qmax	0.8413	
112%	100%**	112.2	92.5	-99.3	-100%Qmax	0.8406	
110%	100%**	110.2	92.5	-99.2	-100%Qmax	0.8409	
108%	100%**	108.2	92.6	-99.3	-100%Qmax	0.841	
107%	100%	107.1	99.9	-49.5	-50%Qmax	0.9586	
106%	100%	106.1	100.0	-1.3	0	0.9999	
105%	100%	105.1	100.0	-0.7	0	0.9999	
103%	100%	103.1	100.1	-1.3	0	0.9999	
100%	100%	100.1	100.1	-1.8	0	0.9999	
97%	100%	97.1	100.1	-0.5	0	0.9999	
94%	100%	94.1	100.2	0.8	0	0.9999	
93%	100%	92.9	83.3	-99.4	100%Qmax	0.8131	
90%	100%	90.0	79.4	-99.2	100%Qmax	0.8004	
87%	100%	87.0	75.2	-99.2	100%Qmax	0.7841	
85%	100%	85.0	72.3	-99.3	100%Qmax	0.7718	
87%	100%	87.0	75.1	-99.3	100%Qmax	0.7834	
90%	100%	90.0	79.3	-99.3	100%Qmax	0.7996	
93%	100%	92.9	83.2	-99.3	100%Qmax	0.8133	
94%	100%	94.1	100.0	1.0	0	0.9999	
97%	100%	97.1	100.0	0.8	0	0.9999	

100%	100%	100.1	99.9	0.0	0	0.9999	
100%	20%	100.0	20.3	-1.5	0	0.9991	
103%	20%	103.0	20.3	-1.6	0	0.9989	
105%	20%	105.0	20.4	-1.6	0	0.9989	
106%	20%	106.0	20.4	-1.6	0	0.9989	
107%	20%	107.0	20.2	-49.2	-50%Qmax	0.5656	
108%	20%	108.1	20.2	-71.9	-100%Qmax*	0.4242	
110%	20%	110.1	20.3	-71.8	-100%Qmax*	0.4260	
112%	20%	112.1	20.2	-71.6	-100%Qmax*	0.4248	
115%	20%	115.1	20.2	-71.8	-100%Qmax*	0.4242	
112%	20%	112.1	20.2	-71.7	-100%Qmax*	0.4251	
110%	20%	110.1	20.3	-71.8	-100%Qmax*	0.4270	
108%	20%	108.1	20.2	-71.9	-100%Qmax*	0.4247	
107%	20%	107.0	20.3	-49.2	-50%Qmax	0.5661	
106%	20%	106.0	20.4	-1.8	0	0.9986	
105%	20%	105.0	20.3	-1.7	0	0.9988	
103%	20%	103.0	20.3	-1.6	0	0.9988	
100%	20%	100.0	20.4	-1.5	0	0.9990	
97%	20%	97.0	20.4	-1.4	0	0.9991	
94%	20%	94.0	20.2	-1.3	0	0.9992	
93%	20%	93.0	20.5	73.0	100%Qmax*	0.4241	
90%	20%	90.0	20.7	73.0	100%Qmax*	0.4267	
87%	20%	87.0	20.5	73.1	100%Qmax*	0.4240	
85%	20%	85.0	20.6	73.1	100%Qmax*	0.4256	
87%	20%	87.0	20.5	73.0	100%Qmax*	0.4240	
90%	20%	90.0	20.6	73.1	100%Qmax*	0.4252	
93%	20%	93.0	20.5	73.1	100%Qmax*	0.4234	
94%	20%	94.0	20.3	-1.4	0	0.9992	
97%	20%	97.0	20.4	-1.4	0	0.9991	
100%	20%	100.0	20.4	-1.4	0	0.9991	
Setting (c)							
Setting value		Measurement value					Limit
Un	Pn	Grid voltage(%)	Active power (%)	Reactive power (%)	Reactive power Target (%)	PF	$\Delta Q\leq\pm 2\%S_n$ $\Delta U\leq\pm 1\%U_n$
100%	100%	100.1	100.3	1.6	0	0.9999	
103%	100%	103.1	100.2	-3.2	-3%Qmax	0.9998	

105%	100%	105.1	100.2	-5.0	-5%Qmax	0.9995
106%	100%	106.1	100.2	-6.0	-6%Qmax	0.9994
107%	100%	107.0	100.2	-7.0	-7%Qmax	0.9991
108%	100%	108.1	100.2	-8.1	-8%Qmax	0.9988
110%	100%	110.1	100.2	-9.8	-10%Qmax	0.9983
112%	100%	112.1	100.1	-9.9	-10%Qmax	0.9982
115%	100%	115.1	100.2	-10.1	-10%Qmax	0.9982
112%	100%	112.1	100.2	-9.8	-10%Qmax	0.9983
110%	100%	110.1	100.2	-9.8	-10%Qmax	0.9983
108%	100%	108.1	100.2	-8.1	-8%Qmax	0.9988
107%	100%	107.0	100.2	-7.0	-7%Qmax	0.9991
106%	100%	106.1	100.3	-6.0	-6%Qmax	0.9994
105%	100%	105.1	100.2	-5.1	-5%Qmax	0.9995
103%	100%	103.1	100.3	-3.4	-3%Qmax	0.9998
100%	100%	100.1	100.3	1.6	0	0.9999
97%	100%	97.1	100.3	-2.8	3%Qmax	0.9999
94%	100%	94.1	100.3	-5.1	6%Qmax	0.9995
93%	100%	93.1	100.4	-6.1	7%Qmax	0.9993
90%	100%	90.1	98.2	-9.3	10%Qmax	0.9984
87%	100%	87.1	94.9	-9.9	10%Qmax	0.9980
85%	100%	85.1	92.8	-9.7	10%Qmax	0.9980
87%	100%	87.1	94.9	-10.1	10%Qmax	0.9980
90%	100%	90.1	98.1	-9.2	10%Qmax	0.9984
93%	100%	93.1	100.3	-6.0	7%Qmax	0.9994
94%	100%	94.1	100.2	-5.0	6%Qmax	0.9995
97%	100%	97.1	100.1	-2.7	3%Qmax	0.9999
100%	100%	100.1	100.1	1.7	0	0.9999
100%	20%	100.0	20.5	1.7	0	0.9987
103%	20%	103.0	20.5	-2.4	-3%Qmax	0.9976
105%	20%	105.0	20.5	-3.8	-5%Qmax	0.9939
106%	20%	106.0	20.5	-4.9	-6%Qmax	0.9898
107%	20%	107.0	20.5	-6.0	-7%Qmax	0.9850
108%	20%	108.0	20.5	-7.3	-8%Qmax	0.9782
110%	20%	110.0	20.5	-9.1	-10%Qmax	0.9667
112%	20%	112.0	20.5	-10.1	-10%Qmax	0.9590
115%	20%	115.0	20.4	-10.3	-10%Qmax	0.9569
112%	20%	112.0	20.5	-10.2	-10%Qmax	0.9582

110%	20%	110.0	20.5	-9.1	-10%Qmax	0.9659
108%	20%	108.0	20.5	-7.3	-8%Qmax	0.9782
107%	20%	107.0	20.5	-6.0	-7%Qmax	0.9849
106%	20%	106.0	20.5	-5.2	-6%Qmax	0.9886
105%	20%	105.0	20.5	-4.2	-5%Qmax	0.9924
103%	20%	103.0	20.5	-2.4	-3%Qmax	0.9975
100%	20%	100.0	20.6	1.7	0	0.9988
97%	20%	97.0	20.6	1.8	3%Qmax	0.9987
94%	20%	94.0	20.5	4.1	6%Qmax	0.9928
93%	20%	93.0	20.6	5.1	7%Qmax	0.9892
90%	20%	90.0	20.5	8.5	10%Qmax	0.9707
87%	20%	87.0	20.5	9.6	10%Qmax	0.9629
85%	20%	85.0	20.5	9.3	10%Qmax	0.9648
87%	20%	87.0	20.5	9.6	10%Qmax	0.9629
90%	20%	90.0	20.5	8.6	10%Qmax	0.9701
93%	20%	93.0	20.5	5.1	7%Qmax	0.9892
94%	20%	94.0	20.5	4.1	6%Qmax	0.9929
97%	20%	97.0	20.5	1.7	3%Qmax	0.9988
100%	20%	100.0	20.6	1.3	0	0.9992

Test method:

Standard	Parameter	X1	X2	X3	X4
EN 50549-10: 2022 (Setting a)	U (U/Un)	93%	94%	106%	108%
	Q (Q/Qmax)	100%	0%	0%	-100%

Standard	Parameter	X1	X2	X3
EN 50549-10: 2022 (Setting b)	U (U/Un)	90%	100%	110%
	Q (Q/Qmax)	10%	0%	-10%

Assessment criterion:

The actual tested configuration shall be chosen according the principle of 4.4. If the conformity assessment is conducted for a specific grid code, the EUT control shall be configured as defined in this grid code. If the conformity assessment is conducted in general, the configuration shall be chosen in a way that each parameter shall be tested once configured to its minimum and once to its maximum according to the principle given in Table 17 and Table 18.

Table 17 — Q(U) characteristic configuration

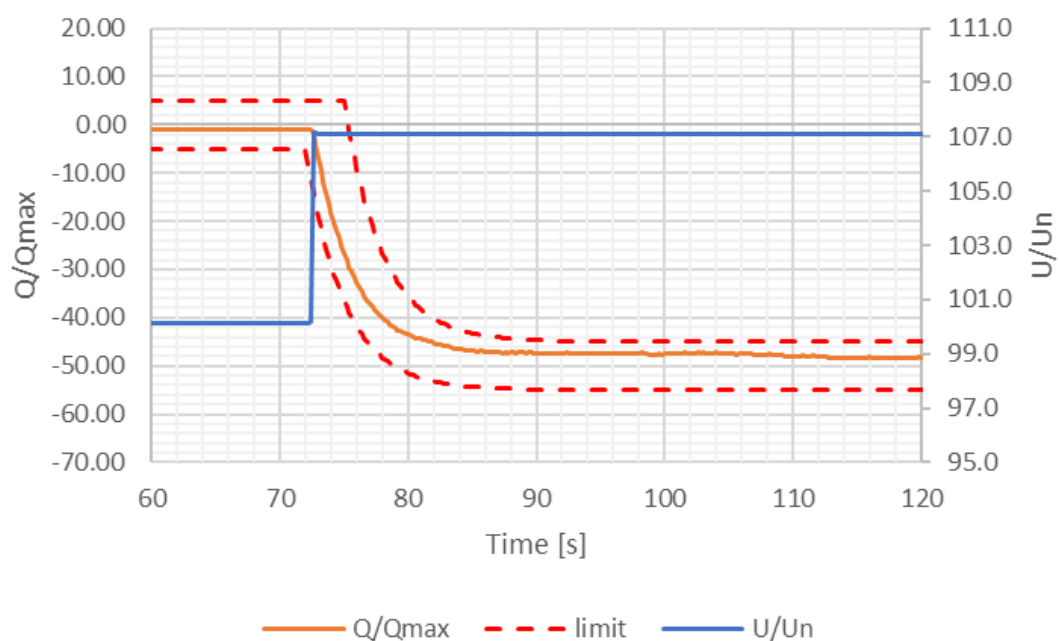
parameter of characteristic	Setting (a)	Setting (b)
dead band (horizontal section of the characteristic curve around U_n) db_{char}	as wide as possible but extending not more than from $-6\%U_n$ to $+6\%U_n$.	0
minimum and maximum reactive power of the characteristic $Q_{max\ char}^a$	$\geq 95\%$ of $Q_{max\ over}$ and $Q_{max\ under}$ respectively ^b	10 % of $Q_{max\ over}$ and $Q_{max\ under}$ respectively
Slope of the steepest section of the characteristic curve ^a	as steep as configurable, ^c	as flat as configurable ^d
^a If an unsymmetrical configuration is possible, this shall be apparent in the chosen configuration by using different steepness and different values for maximum reactive power $Q_{max\ char}$ for over voltage and under voltage. ^b if Q_{max} , based on the capability curve of the EUT, is above $60\%P_n$, $Q_{max\ char}$ shall not exceed $60\%P_n$. ^c if the maximum configurable steepness exceeds $100\%Q_{max} / 1\%U_n$ the configured value during the test shall not exceed $100\%Q_{max} / 1\%U_n$. ^d if the minimum configurable steepness is below $1\%Q_{max} / 1\%U_n$ the configured value during the test shall not be below $1\%Q_{max} / 1\%U_n$.		

Note:

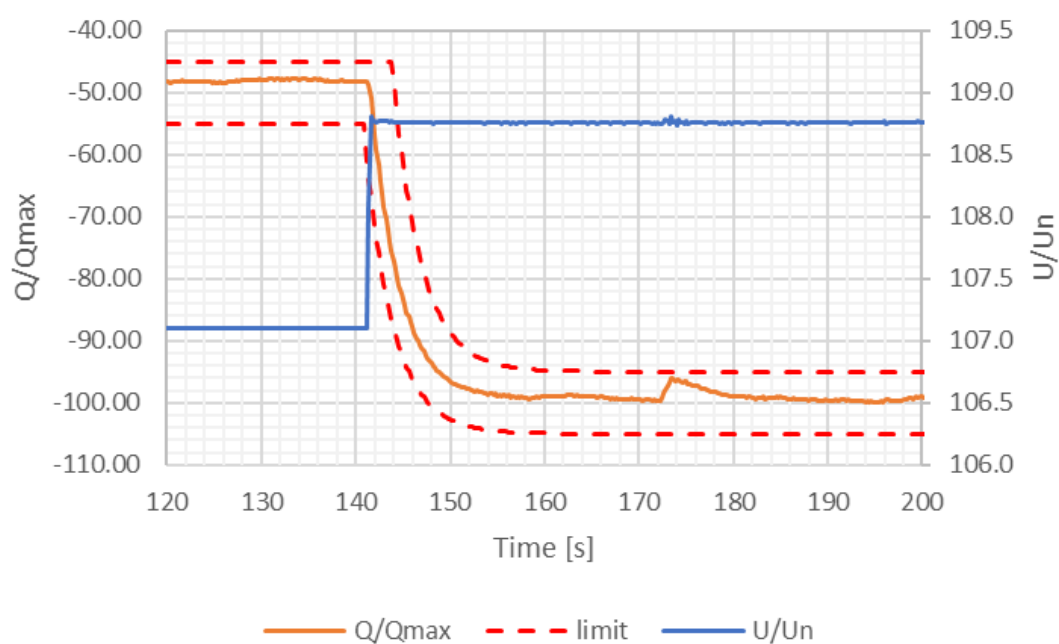
* Due to the limit of output current, it can't reach target output power.

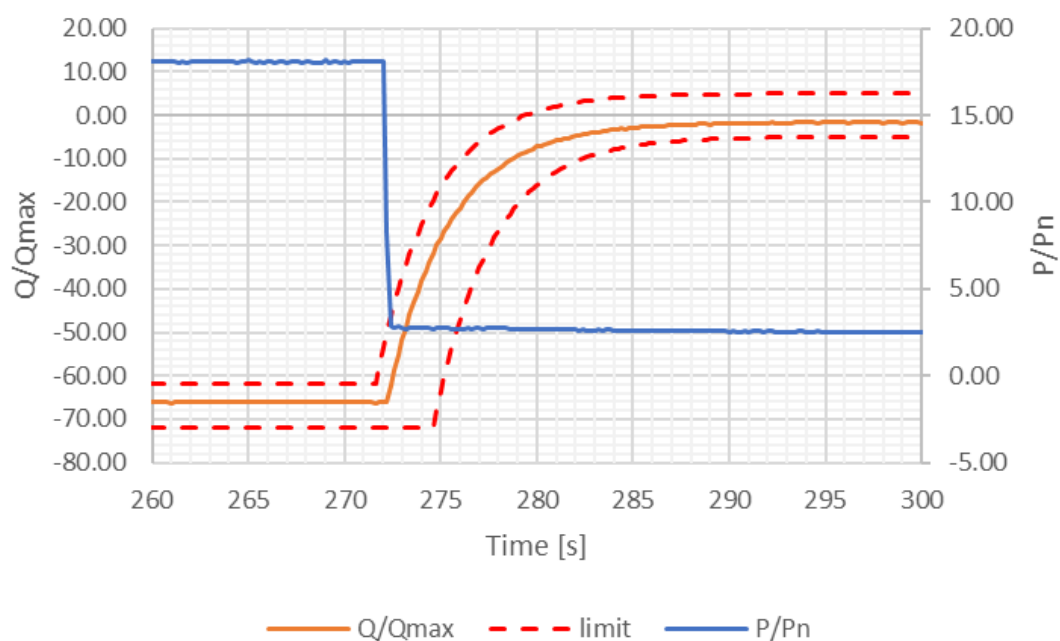
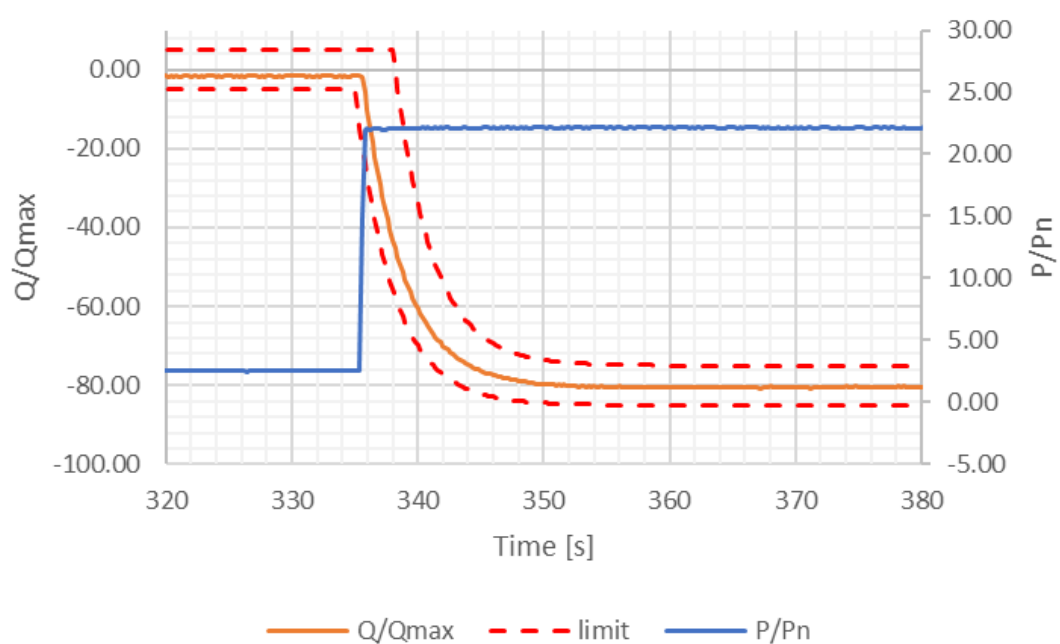
5.5.2.4 Verification procedure for voltage related control mode for reactive power Q(U)							P
5.5.2.4.3 Test procedure for dynamic behaviour							
Q _{max} [Var]		75000		Q _{min} [Var]		-75000	
Setting value		Measurement value					Limit
Un	Pn	Grid voltage(%)	Active power (%)	Reactive power (%)	Reactive power Target (%)	PF	ΔU≤±1%Un ΔQ≤±2%Sn or PF≥0.4
1: 100%	>90%	100.1	100.87	-1.06	0	0.9998	
2: 107%	>90%	107.1	100.86	-47.84	-50%Qmax	0.9616	
3: 109%	>90%	108.8	93.61	-98.89	-100%Qmax	0.8444	
4: 109%	18%	108.6	18.07	-66.06*	-100%Qmax	0.4146	
5: 109%	3%	108.6	2.53	-1.81	0	0.8674	
6: 109%	22%	108.6	22.14	-80.41*	-100%Qmax	0.4169	
7: 109%	>90%	108.8	94.60	-96.59	-100%Qmax	0.8525	
8: 100%	>90%	100.1	100.89	-1.02	0%Qmax	0.9998	
9: 92%	>90%	92.2	81.80**	99.71	100%Qmax	0.8070	
10: 92%	18%	92.0	17.82	63.57*	100% Qmax	0.4231	
11: 92%	3%	92.0	2.58	-1.03	0%Qmax	0.9397	
12: 92%	22%	92.0	21.86	77.62*	100%Qmax	0.4248	
13: 92%	>90%	92.2	81.77**	99.94	100%Qmax	0.8063	
14: 93%	>90%	93.2	83.22**	99.90	100%Qmax	0.8113	
15: 100%	>90%	100.1	100.87	-0.92	0%	0.9998	
Note:							
*cos φ≥0.4 Priority							
** Due to the limit of max output current, it can't reach target output power.							
P lock-in: 20%Pn; P lock-out: 5%Pn.							

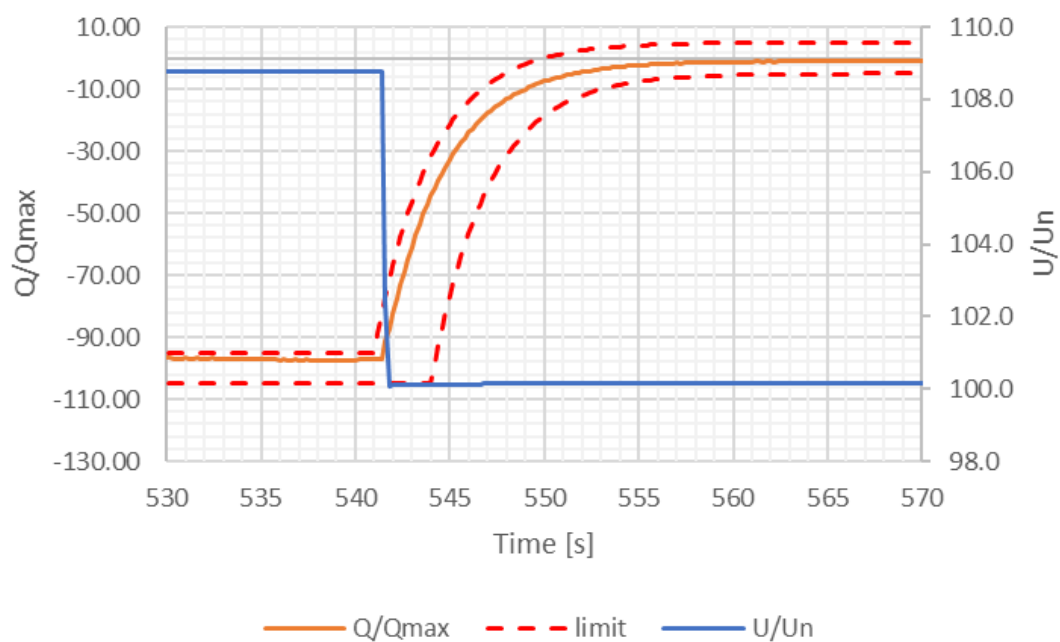
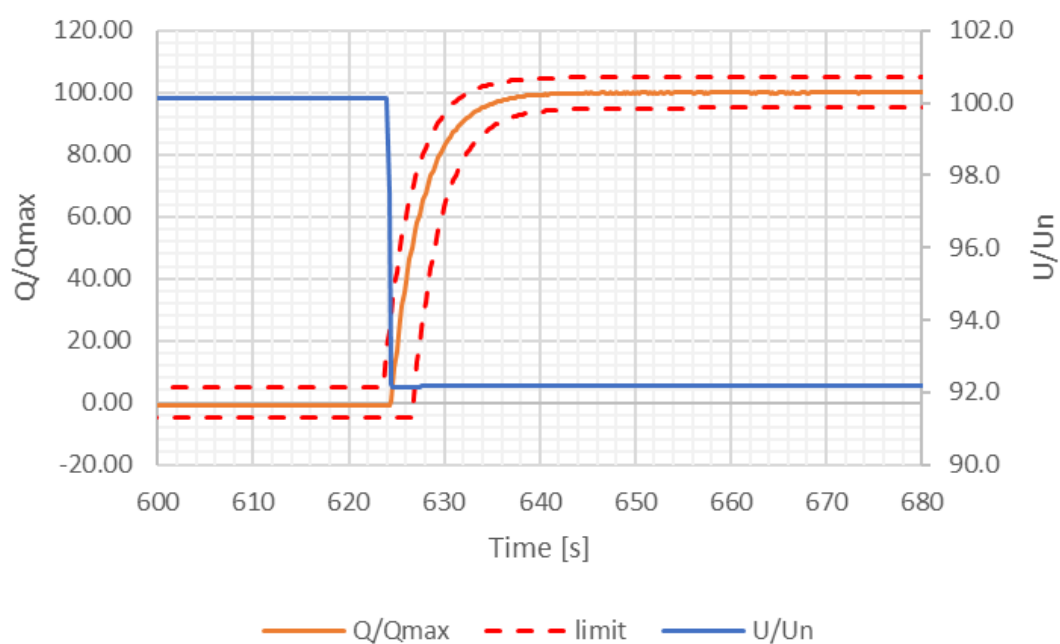
U/Un: 100%-107%

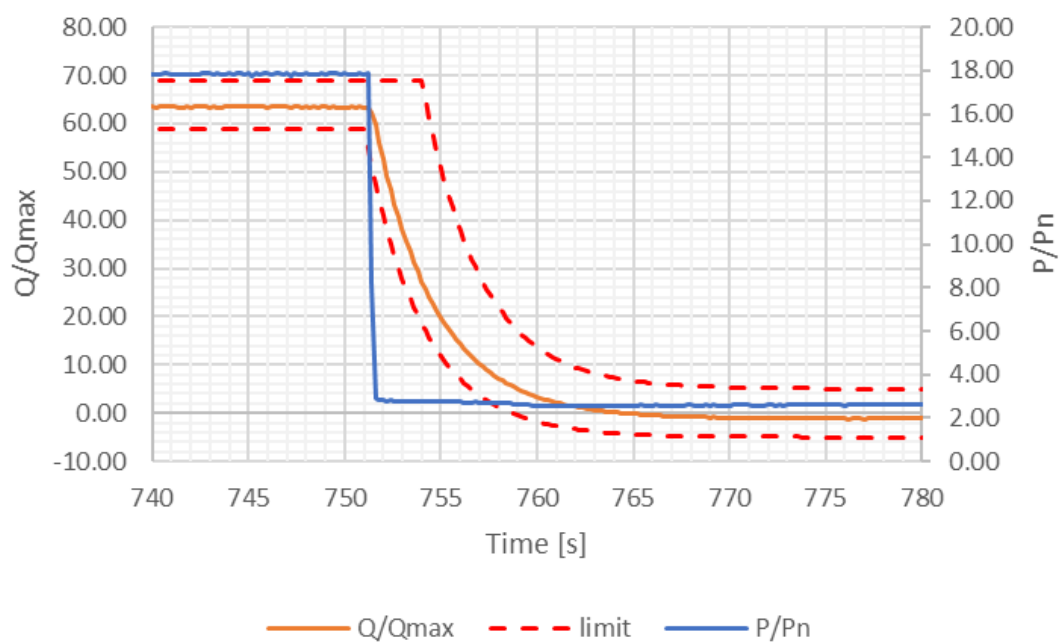
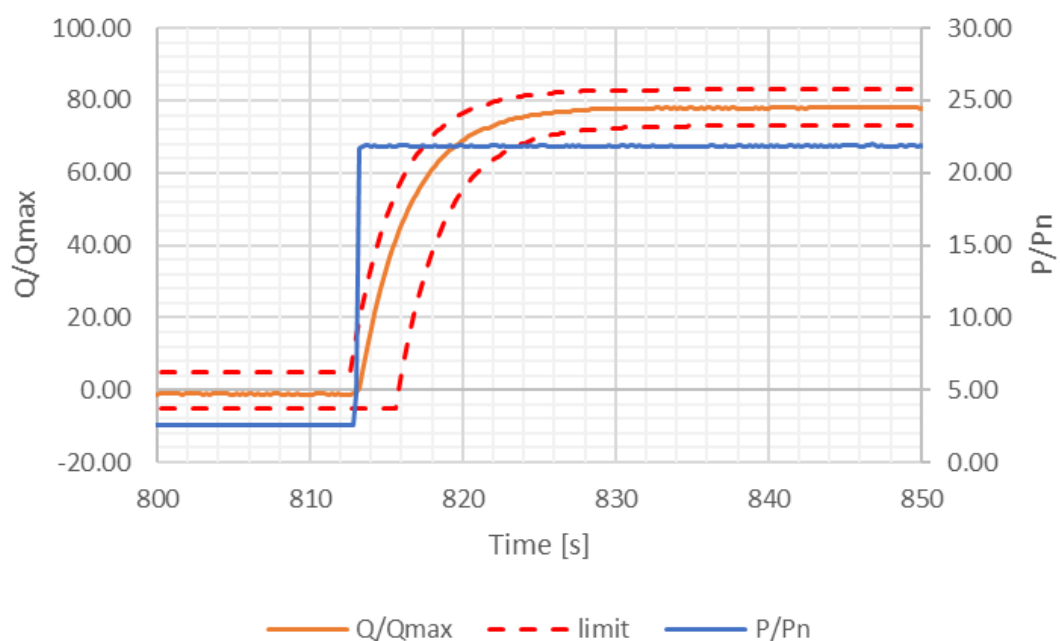


U/Un: 107%-109%

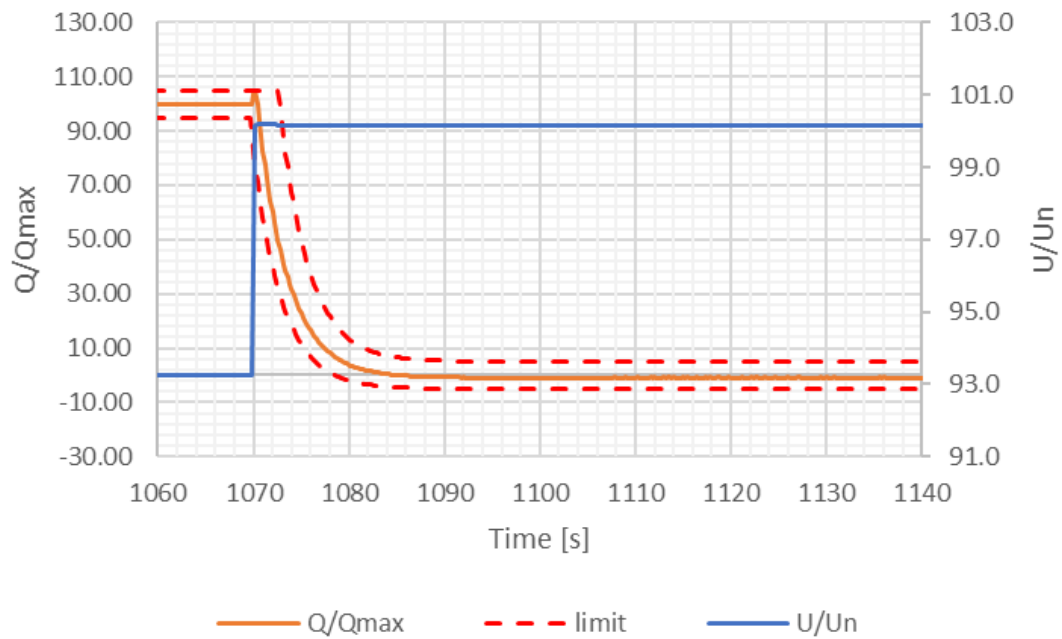


P/Pn:18%-3%**P/Pn:3%-22%**

U/Un: 109%-100%**U/Un: 100%-92%**

P/Pn:18%-3%**P/Pn:3%-22%**

U/Un: 93%-100%



Test method:

Standard	Parameter	X1	X2	X3	X4
EN 50549-10: 2022 (Setting a)	U (U/U _n)	93%	94%	106%	108%
	Q (Q/Q _{max})	100%	0%	0%	-100%

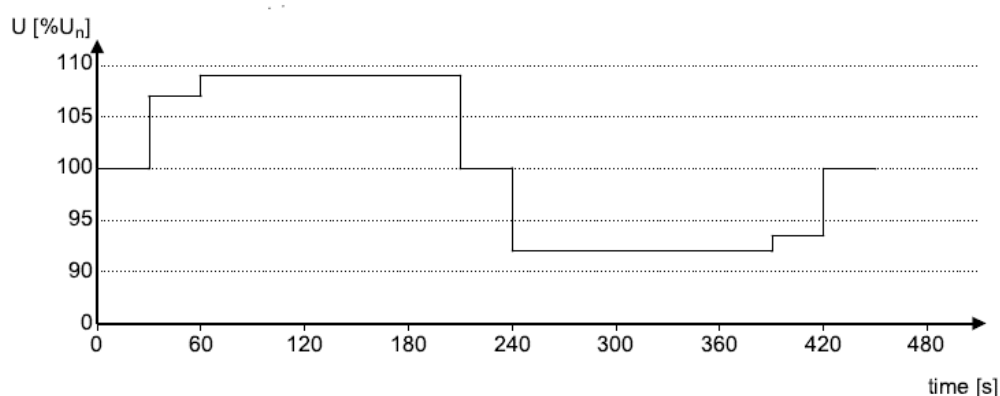


Figure 34 — Example voltage profile for setting (a) applied for the test of the dynamic behaviour of the Q(U), assuming < 30 s for stabilization of P and Q

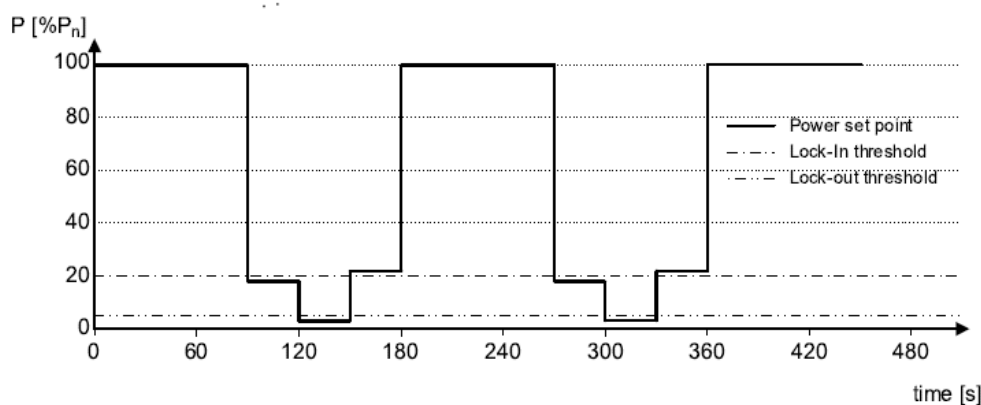


Figure 35 — Example active power set point profile for setting (a) applied for the test of the dynamic behaviour of the Q(U) control, assuming < 30 s for stabilization of P and Q

5.5.2.5 Verification procedure for power related control modes for reactive power										P
5.5.2.5.2 Test procedure for quasi-stationary behaviour										
Test a):										
P _n /P [%]	10	20	30	40	50	60	70	80	90	100
30 s mean value	10% to 100% P _n									
U [V]:	230.0	229.9	229.9	230.0	230.0	230.0	230.1	230.1	230.0	230.0
P _{E30} [W]:	12711	25118	37891	50352	63004	75651	87956	100324	112892	115141
P _{E30} of P _n [%]:	10.17	20.09	30.31	40.28	50.40	60.52	70.36	80.26	90.31	92.11*
Q _{E30} [Var]:	75345	-1791	-1760	-1762	-1134	-773	252	724	-75179	-75121
Q _{E30} of P _n [%]:	60.28	-1.43	-1.41	-1.41	-0.91	-0.62	0.20	0.58	-60.14	-60.10
cos φ _{E30} :	0.1664	0.9974	0.9988	0.9993	0.9995	0.9997	0.9997	0.9997	0.8323	0.8375
Q _{E30} target [%]:	60	0	0	0	0	0	0	0	-60	-60
P _n /P [%]	100	90	80	70	60	50	40	30	20	10
30 s mean value	100% to 10% P _n									
U [V]:	230.0	230.0	230.1	230.1	230.1	230.0	230.0	229.9	229.9	230.0
P _{E30} [W]:	115053	112487	100276	87669	75239	62717	50139	37854	25268	12879
P _{E30} of P _n [%]:	92.04*	89.99	80.22	70.13	60.19	50.17	40.11	30.28	20.21	10.30
Q _{E30} [Var]:	-75095	-75081	1124	123	-864	-1414	-1669	-1825	-1801	75436
Q _{E30} of P _n [%]:	-60.08	-60.06	0.90	0.10	-0.69	-1.13	-1.34	-1.46	-1.44	60.35
cos φ _{E30} :	0.8374	0.8317	0.9998	0.9997	0.9996	0.9995	0.9992	0.9986	0.9975	0.1683
Q _{E30} target [%]:	-60	-60	0	0	0	0	0	0	0	60
Limit ΔQ:	±2%S _{max}									

Test c):										
P _n /P [%]	10	20	30	40	50	60	60	70	80	90
30 s mean value	10% to 100% to 10%									
U [V]:	239.1	239.1	239.1	239.2	239.2	239.3	243.8	243.8	243.8	243.8
P _{E30} [W]:	12639	25136	37827	50461	62858	75534	75554	88097	100620	113148
P _{E30} of P _n [%]:	10.11	20.11	30.26	40.37	50.29	60.43	60.44	70.48	80.50	90.52
Q _{E30} [Var]:	634	600	628	747	951	1559	-14092	-30730	-46912	-60601
Q _{E30} of P _n [%]:	0.51	0.48	0.50	0.60	0.76	1.25	-11.27	-24.58	-37.53	-48.48
cos φ _{E30} :	0.9987	0.9997	0.9999	0.9999	0.9999	0.9998	0.9830	0.9442	0.9063	0.8815
Q _{E30} target [%]:	0	0	0	0	0	0	-12	-24	-36	-48
P _n /P [%]	100	90	80	70	60	60	50	40	30	20
30 s mean value	10% to 100% to 10%									
U [V]:	243.8	243.8	243.8	243.8	243.8	227.7	227.7	227.7	227.6	227.6
P _{E30} [W]:	119017	112976	100148	87371	74970	75345	62766	50186	37743	25261
P _{E30} of P _n [%]:	95.21*	90.38	80.12	69.90	59.98	60.28	50.21	40.15	30.19	20.21
Q _{E30} [Var]:	-69047	-60542	-46136	-30943	-14370	2030	1370	773	726	676
Q _{E30} of P _n [%]:	-55.24*	-48.43	-36.91	-24.75	-11.50	1.62	1.10	0.62	0.58	0.54
cos φ _{E30} :	0.8650	0.8814	0.9083	0.9426	0.9821	0.9996	0.9998	0.9999	0.9998	0.9996
Q _{E30} target [%]:	-60	-48	-36	-24	-12	0	0	0	0	0
P _n /P [%]	10	--	--	--	--	--	--	--	--	--
30 s mean value	10% to 100% to 10%									
U [V]:	227.6	--	--	--	--	--	--	--	--	--
P _{E30} [W]:	12794	--	--	--	--	--	--	--	--	--
P _{E30} of P _n [%]:	10.24	--	--	--	--	--	--	--	--	--
Q _{E30} [Var]:	768	--	--	--	--	--	--	--	--	--
ΔQ _{E30} /P _n [%]:	0.61	--	--	--	--	--	--	--	--	--
cos φ _{E30} :	0.9982	--	--	--	--	--	--	--	--	--
Q _{E30} target [%]:	0	--	--	--	--	--	--	--	--	--
Limit ΔQ:	±2%Smax									
* Due to the limit of output current, it can't reach target output power.										

Test method:

Table 19 — Q (P) or cos φ (P) characteristic configuration

parameter of characteristic	Setting (a)	Setting (b)	Setting (c)
minimum and maximum reactive power (Cos Φ ^b) of the characteristic $Q_{\max \text{ char}}^a$	$\geq 95\%$ of $Q_{\max \text{ over}}$ and $Q_{\max \text{ under}}$ respectively but not more than $60\% P_n$	10% of $Q_{\max \text{ over}}$ and $Q_{\max \text{ under}}$ respectively	$Q_{\max \text{ char over}} = 0\% P_n$ $Q_{\max \text{ char over}} = 33\% P_n$
Slope of the steepest section of the characteristic curve ^a	as steep as configurable, but not more than $200\% Q_{\max} / 10\% P_n$	as flat as configurable, but not less than $2\% Q_{\max} / 10\% P_n$	0 below $50\% P_n$; $20\% Q_{\max} / 10\% P_n$ above $50\% P_n$
Time constant	as fast as possible but not slower than 3 s	as slow as possible but not slower than 20 s ^c	3,33 s

^a If multiple sections with different steepness are configurable, this shall be apparent in the chosen configuration by using different steepness and different values for maximum reactive power $Q_{\max \text{ char over}}$ and $Q_{\max \text{ char under}}$.

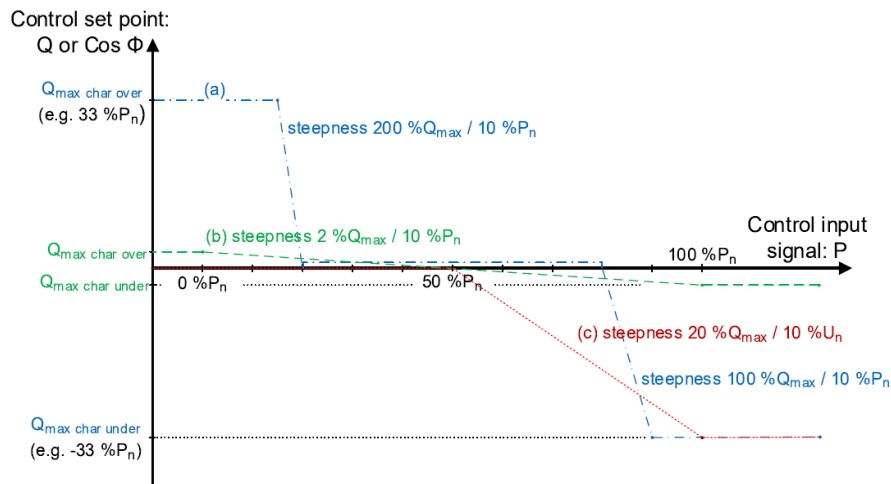
^b The cos φ (P) characteristic shall be configured accordingly with the cos φ value representing the given reactive power value at P_n .

^c The test is considered significant also for slower values of the time constant, as slow time constants are technically less demanding. 20 s is chosen to reduce test duration compared to 60 s as defined in EN 50549-2:2019.

Setting (c) represents a commonly applied characteristic.

Table 20 — cos φ (P) optional additional parameter configuration if applicable

parameter	Setting (a)	Setting (b)	
Lock-in voltage	deactivated	$105\% U_n$	
Lock-out voltage	deactivated	$100\% U_n$	

Figure 36 — Q (P) and cos φ (P) and example characteristics for setting (a), (b) and (c) for the test

Assessment criterion:

Test of the accuracy of the Q(P) cos φ (P) control shall be considered passed if:

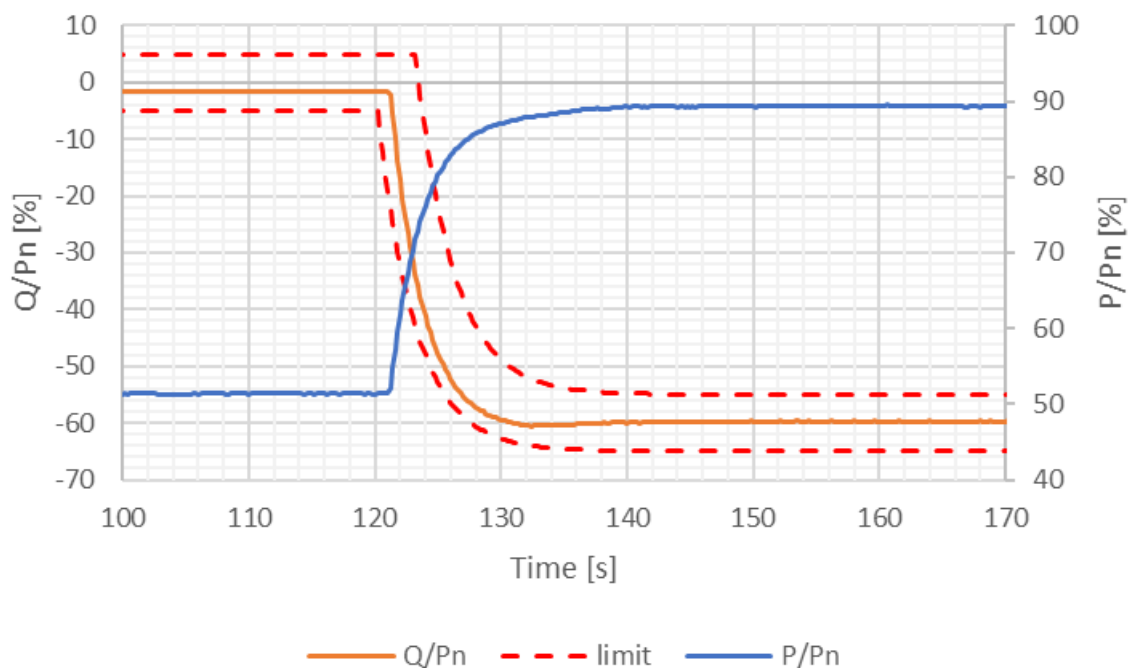
The measured 30-s averages of the reactive power during steady-state conditions are within a tolerance band of $\pm 2\% S_{\max}$ around the reactive power set point resulting from the measured active power and the given cos φ (P) or Q (P) characteristic.

5.5.2.5 Verification procedure for power related control modes for reactive power					P
5.5.2.5.3 Test procedure for dynamic behaviour					
Test a):					
P _E max/P _n [%]	20	50	90	50	20
30 s mean value	20% to 50% to 90% to 50% to 20%				
U _{target} [V]:	230.0	230.0	230.0	230.0	230.0
U [V]:	230.0	230.1	230.1	230.1	230.0
P _{E30} [W]:	25859	64255	111698	64195	25792
P _{E30} of P _n [%]:	20.69	51.40	89.36	51.36	20.63
Q _{E30} [Var]:	-1506	-2004	-74683	-1944	-1453
Q _{E30} of P _n [%]:	-1.20	-1.60	-59.75	-1.56	-1.16
Q _{E30} target [%]:	0.00	0.00	-60.00	0.00	0.00
cos ϕ _{E30} :	0.9981	0.9994	0.8142	0.9995	0.9982
Test condition	T _{dead time} [s]		T _{set time} [s]		T _{setting response time} [s]
50% P _n -90% P _n	0.2		9.6		9.6
90% P _n -50% P _n	0.2		9.6		9.6
Limit:	PT1:3s ΔQ=± 5 % P _n , delay time <3s				
Test c):					
P _E max/P _n [%]	20		50		90
30 s mean value	20% to 50% to 90% to 50% to 20%				
U _{target} [V]:	246.1		246.1		246.1
U [V]:	246.0		246.1		246.1
P _{E30} [W]:	25310		62715		112943
P _{E30} of P _n [%]:	20.25		50.17		90.35
Q _{E30} [Var]:	-628		-426		-60099
Q _{E30} of P _n [%]:	-0.50		-0.34		-48.08
Q _{E30} target [%]:	0.00		0.00		-48.00
cos ϕ _{E30} :	0.9977		0.9996		0.8607
P _E max/P _n [%]	90		90		90
U _{target} [V]:	236.9		225.4		246.1
U [V]:	237.0		225.6		246.1

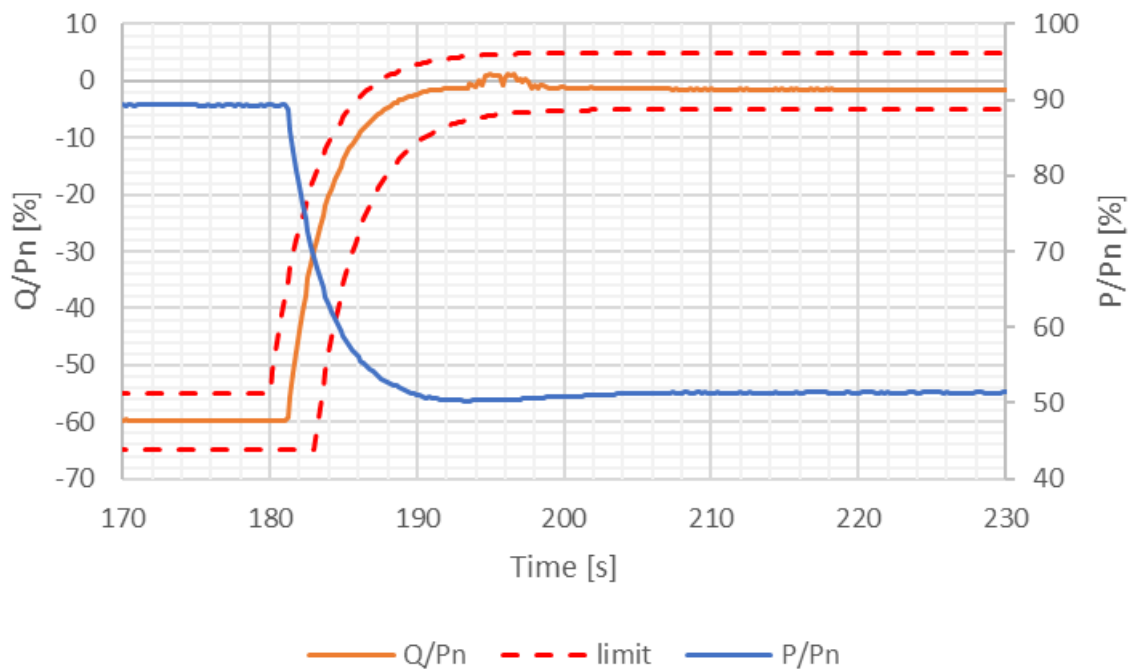
P_{E30} [W]:	112865	112476	112592
P_{E30} of P_n [%]:	90.29	89.98	90.07
Q_{E30} [Var]:	-60104	-242	-60087
Q_{E30} of P_n [%]:	-48.08	-0.19	-48.07
Q_{E30} target [%]:	-48.00	0.00	-48.00
$\cos \varphi_{E30}$:	0.8605	0.9998	0.8600
$P_{E\max}/P_n$ [%]	90	50	20
U_{target} [V]:	230.0	230.0	230.0
U [V]:	230.0	229.9	229.8
P_{E30} [W]:	112391	62696	25409
P_{E30} of P_n [%]:	89.91	50.16	20.33
Q_{E30} [Var]:	-210	-352	-495
Q_{E30} of $S_{\max}$ [%]:	-0.17	-0.28	-0.40
Q_{E30} target [%]:	0.00	0.00	0.00
$\cos \varphi_{E30}$:	0.9998	0.9997	0.9982
Test condition	$T_{\text{dead time}}[\text{s}]$	$T_{\text{set time}}[\text{s}]$	$T_{\text{setting response time}}[\text{s}]$
50% P_n -90% P_n (107% U_n -107% U_n)	0.2	9.8	9.8
90% P_n (103% U_n -98% U_n)	0.2	9.6	9.6
90% P_n (98% U_n -107% U_n)	0.4	9.6	9.6
90% P_n (107% U_n -100% U_n)	0.2	9.2	9.2
Limit:	Q PT1:3.33s, $\Delta Q = \pm 5\%$ P_n , delay time <3s		
Note:			

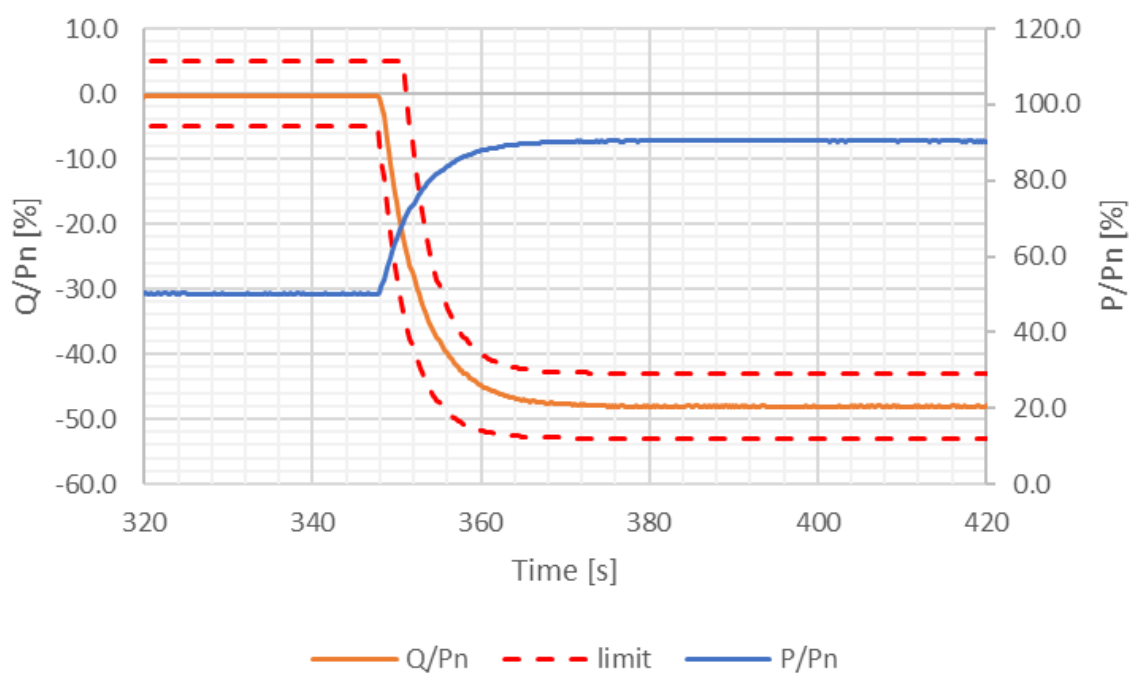
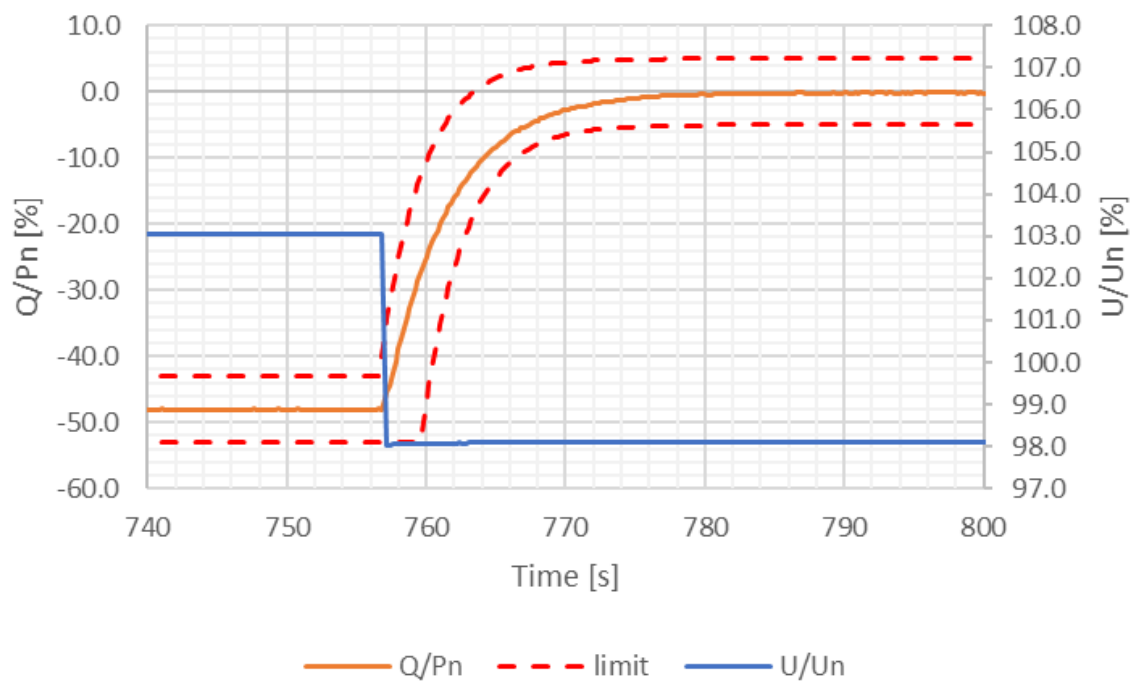
Test a:

50%Pn -> 90%Pn:

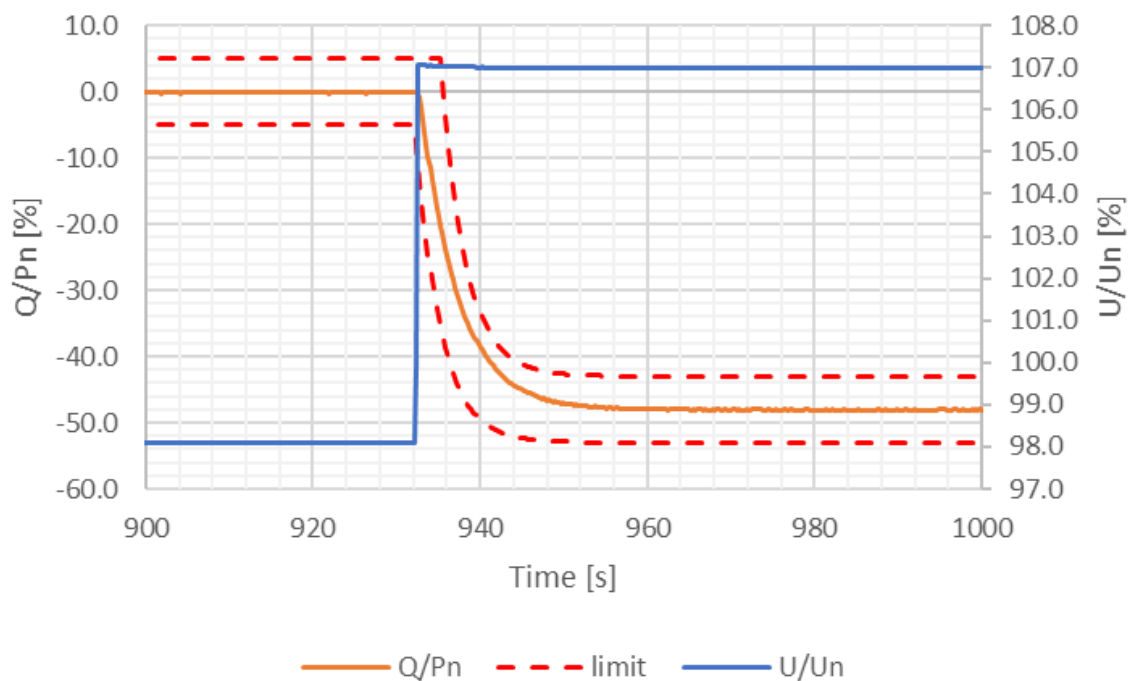


90%Pn -> 50%Pn:

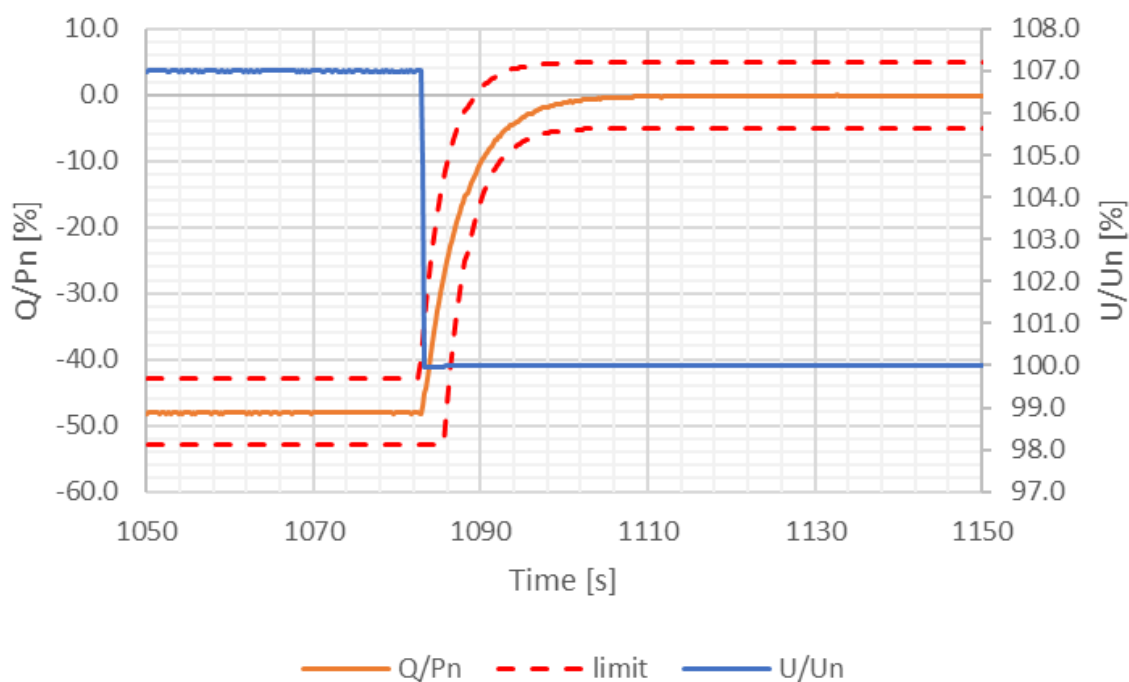


Test c:**50%Pn -> 90%Pn****103%Un -> 98%Un**

98%Un-> 107%Un



107%Un-> 100%Un



EN 50549-10:2022		
Clause	Test items	Remark
5.6	Voltage related active power reduction - P(U)	
5.6.3	Test procedure for steady-state behaviour	P
5.6.4	Test procedure for dynamic behaviour	P

5.6 Voltage related active power reduction (P(U) function)					P
5.6.3 Test procedure for steady-state behaviour					
Test condition	100% Pn				
V _{Target} [%]	100	103	106	109	
V _{measured} [%]	100.12	103.03	106.02	109.07	
P _{measured} [%]	100.11	100.03	99.99	92.64*	
Q _{measured} [%]	1.59	1.59	1.60	-60.59	
P _{Target} [%]	100.0	100.0	100.0	100.0	
Q _{Target} [%]	0.0	0.0	0.0	-60.0	
V _{Target} [%]	112	115	112	109	
V _{measured} [%]	112.04	114.97	112.04	109.07	
P _{measured} [%]	59.82	0.04	60.04	92.79*	
Q _{measured} [%]	-60.87	-60.98	-60.88	-60.66	
P _{Target} [%]	60.0	0.0	60.0	100.0	
Q _{Target} [%]	-60.0	-60.0	-60.0	-60.0	
V _{Target} [%]	106		103		
V _{measured} [%]	106.02		103.02		
P _{measured} [%]	100.05		100.10		
Q _{measured} [%]	1.64		1.58		
P _{Target} [%]	100.0		100.0		
Q _{Target} [%]	0.0		0.0		
Limits	ΔP=±2%P _n , ΔQ=±2%S _{max} , ΔU=±1%U _n				
Assessment criterion:					
P(U) setting					
U/U _n (%)	110		115		
P/P _n (%)	100		0		
Unless further requirements apply for a specific grid code, the test shall be considered passed if: The measured 30-s averages of the active power (positive sequence components) during steady-state conditions are within a tolerance band of ± 2% S _{max} and ± 1 %U _n , of the set P(U) characteristic. the time response of the active power during the measurement represents the applied configuration within the					

tolerance band resulting from the behaviour of an equivalent first order filter element and the configured time constant τ . Accepted tolerances are

-5 % P_n , for the active power values and 3 s for the time in case of a power decrease.

+5 % P_n , for the active power values and 3 s for the time in case of a power increase.

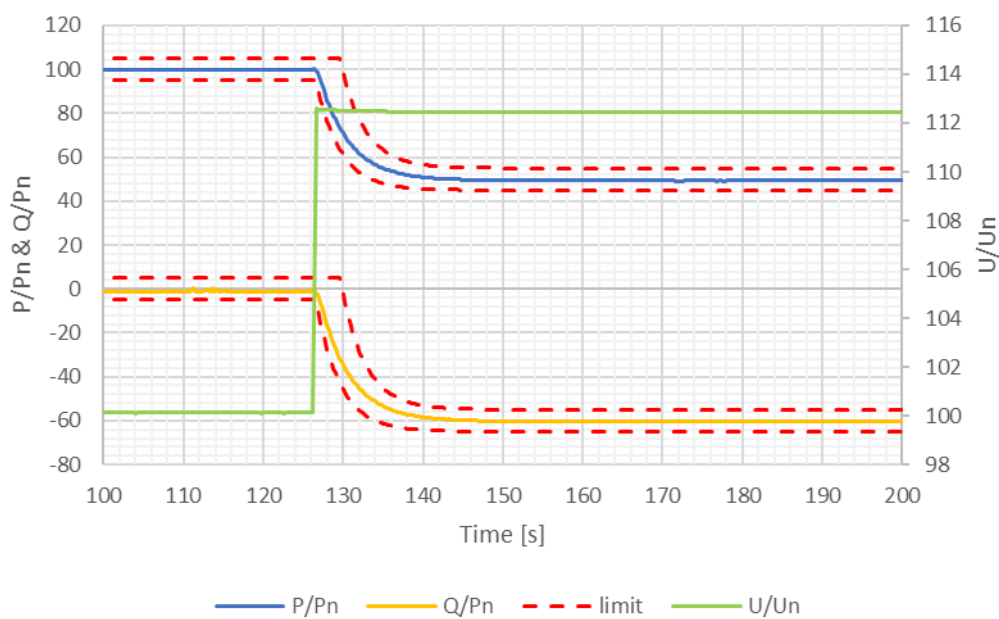
No discontinuities in the characteristic curve are detected and no oscillations of the active power after the end of the transient period of 5τ as configured and no disconnection of the EUT occurs.

The settings of the function, as well as the enabling and disabling of the function, are field adjustable and means are provided to protect these from unpermitted interference (e.g. password or seal).

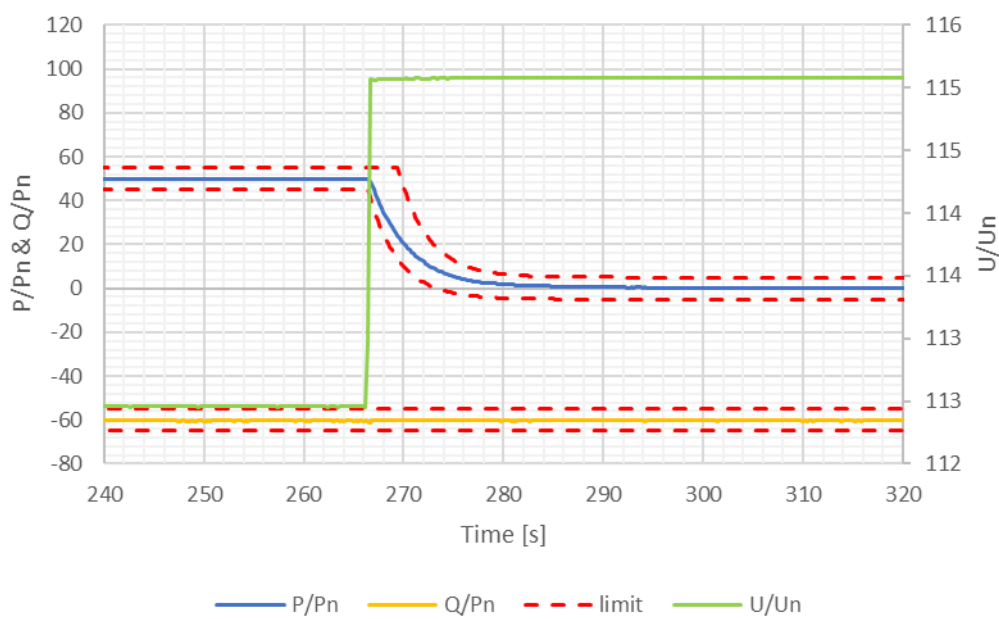
Note: *the active power can't reach because of the limit of output current.

5.6 Voltage related active power reduction (P(U) function)						P
5.6.4 Test procedure for dynamic behaviour						
Test condition	100% Pn					
	Step 1	Step 2	Step 3	Step 4	Step 5	
V _{Target} [%]	100.0	112.5	115.0	112.5	100.0	
V _{measured} [%]	100.11	112.46	115.08	112.46	100.11	
P _{measured} [%]	99.83	49.47	0.12	49.79	99.89	
Q _{measured} [%]	-1.28	-60.34	-60.35	-60.36	-1.20	
P _{Target} [%]	100.0	50.0	0.0	50.0	100.0	
Q _{Target} [%]	0.0	-60.0	-60.0	-60.0	0.0	
Limit	P: Tau=3s, Q: Tau=3s, ΔP=±2%Pn, ΔQ=±2%Smax, ΔU=±1%Un					
Test condition	100% Pn					
	Dead time[s]		response time[s]		settling time[s]	
	P	Q	P	Q	P	Q
Step 1- Step 2	0.8	0.8	9.8	9.8	9.8	9.8
Step 2- Step 3	0.4	-	9.2	-	9.2	-
Step 3- Step 4	0.4	-	9.2	-	9.2	-
Step 4- Step 5	0.4	0.4	9.8	9.8	9.8	9.8

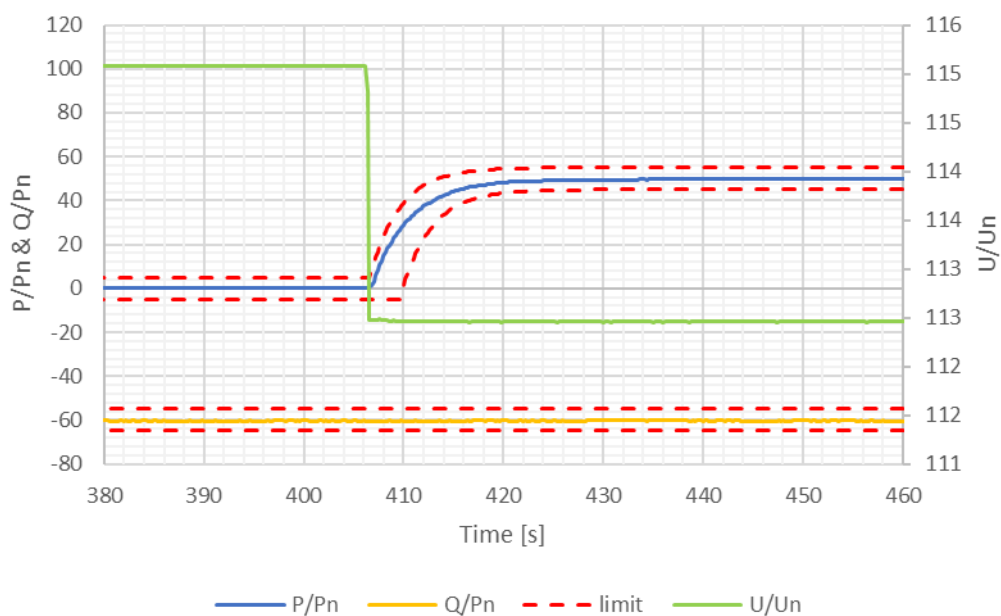
Step 1- Step 2



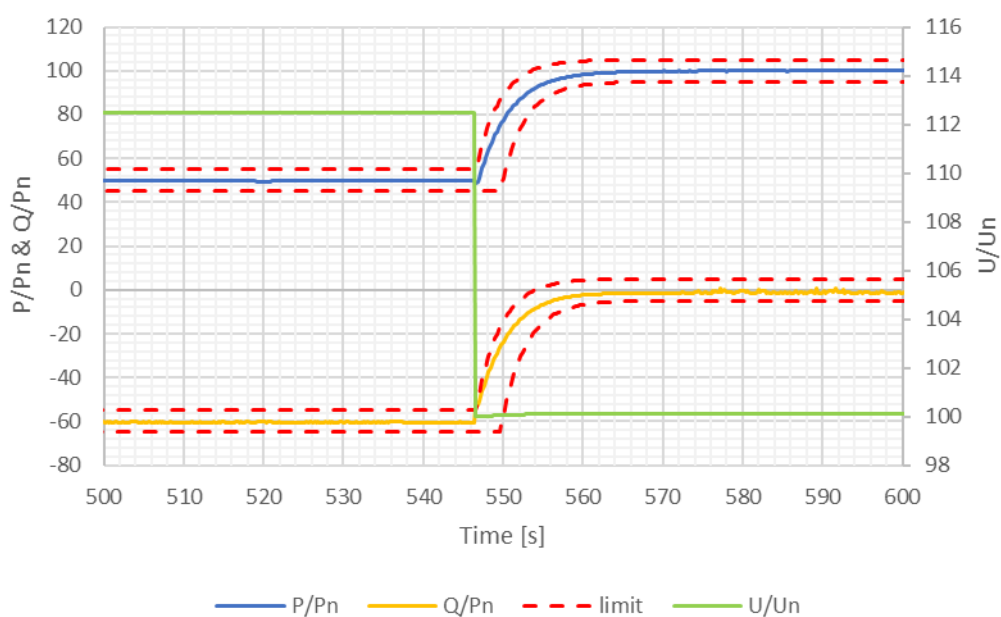
Step 2- Step 3



Step 3- Step 4



Step 4- Step 5



Assessment criterion:**P(U) setting**

U/Un(%)	110	115
P/Pn(%)	100	0

Unless further requirements apply for a specific grid code, the test shall be considered passed if:

The measured 30-s averages of the active power (positive sequence components) during steady-state conditions are within a tolerance band of $\pm 2\%$ S_{max} and $\pm 1\%$ U_n , of the set P(U) characteristic.

the time response of the active power during the measurement represents the applied configuration within the tolerance band resulting from the behaviour of an equivalent first order filter element and the configured time constant τ . Accepted tolerances are

-5 % P_n , for the active power values and 3 s for the time in case of a power decrease.

+5 % P_n , for the active power values and 3 s for the time in case of a power increase.

No discontinuities in the characteristic curve are detected and no oscillations of the active power after the end of the transient period of 5τ as configured and no disconnection of the EUT occur.

The settings of the function, as well as the enabling and disabling of the function, are field adjustable and means are provided to protect these from unpermitted interference (e.g. password or seal).

Note:

EN 50549-10:2022		
Clause	Test items	Remark
5.7	EMC and power quality	
5.7.1	EMC	P
5.7.2	Power Quality	
5.7.2.2	Verification procedure for harmonics, interharmonics and higher frequencies up to 9 kHz	P
5.7.2.3	Verification procedure for flicker and rapid voltage changes	P
5.7.2.3.2	Flicker - Generation units with a rated currents 75 A	N/A
5.7.2.3.3	Flicker - Generation units with a rated currents >75 A	P
5.7.2.3.4	Rapid voltage changes - Generation units with a rated current 75A	N/A
5.7.2.3.5	Rapid voltage changes - Generation units with a rated current > 75 A	P
5.7.2.4	Verification procedure for DC injection	P

5.7 EMC and power quality 5.7.2.2 Verification procedure for harmonics, interharmonics and higher frequencies up to 9 kHz							P
Model: TAA-125KW-11							
Power Level	33%		66%		100%		
Harmonic	Current Magnitude (A)	% of Fundamental	Current Magnitude (A)	% of Fundamental	Current Magnitude (A)	% of Fundamental	Harmonic Current Limits (%)
2nd	0.206	0.346	0.365	0.304	0.406	0.224	8.000
3rd	0.266	0.447	0.165	0.138	0.319	0.176	N/A
4th	0.052	0.087	0.230	0.192	0.338	0.186	4.000
5th	0.209	0.351	0.177	0.148	0.187	0.103	10.700
6th	0.030	0.051	0.173	0.144	0.223	0.123	2.670
7th	0.277	0.465	0.203	0.170	0.274	0.151	7.200
8th	0.029	0.048	0.049	0.041	0.071	0.039	2.000
9th	0.284	0.476	0.131	0.109	0.150	0.083	N/A
10th	0.031	0.052	0.048	0.040	0.082	0.045	1.600
11th	0.850	1.428	0.909	0.758	1.219	0.672	3.100
12th	0.023	0.038	0.039	0.032	0.050	0.027	1.330
13th	0.565	0.949	0.523	0.437	0.671	0.370	2.000
14th	0.015	0.025	0.030	0.025	0.041	0.023	N/A
15th	0.353	0.592	0.384	0.321	0.442	0.244	N/A
16th	0.010	0.018	0.031	0.026	0.031	0.017	N/A
17th	0.337	0.566	0.378	0.315	0.372	0.205	N/A
18th	0.017	0.028	0.032	0.026	0.036	0.020	N/A
19th	0.307	0.515	0.323	0.270	0.303	0.167	N/A
20th	0.015	0.025	0.029	0.024	0.035	0.019	N/A
21st	0.220	0.369	0.268	0.224	0.253	0.140	N/A
22nd	0.009	0.016	0.028	0.024	0.028	0.015	N/A
23rd	0.172	0.289	0.254	0.212	0.251	0.138	N/A
24th	0.008	0.014	0.023	0.020	0.030	0.016	N/A
25th	0.139	0.233	0.215	0.180	0.222	0.122	N/A

26th	0.008	0.013	0.022	0.018	0.025	0.014	N/A
27th	0.109	0.183	0.176	0.147	0.189	0.104	N/A
28th	0.010	0.016	0.021	0.018	0.027	0.015	N/A
29th	0.095	0.160	0.164	0.137	0.182	0.101	N/A
30th	0.008	0.014	0.017	0.014	0.026	0.014	N/A
31st	0.094	0.159	0.145	0.121	0.163	0.090	N/A
32nd	0.008	0.013	0.017	0.014	0.021	0.012	N/A
33rd	0.081	0.136	0.127	0.106	0.140	0.077	N/A
34th	0.006	0.010	0.015	0.012	0.021	0.011	N/A
35th	0.061	0.102	0.126	0.105	0.137	0.075	N/A
36th	0.007	0.011	0.015	0.013	0.019	0.011	N/A
37th	0.060	0.101	0.122	0.102	0.126	0.070	N/A
38th	0.008	0.013	0.015	0.012	0.021	0.011	N/A
39th	0.055	0.092	0.114	0.095	0.114	0.063	N/A
40th	0.006	0.010	0.014	0.012	0.019	0.010	N/A
41st	0.040	0.066	0.111	0.092	0.121	0.067	N/A
42nd	0.007	0.012	0.013	0.011	0.019	0.011	N/A
43rd	0.044	0.074	0.104	0.087	0.124	0.069	N/A
44th	0.008	0.014	0.013	0.011	0.023	0.012	N/A
45th	0.054	0.091	0.100	0.084	0.123	0.068	N/A
46th	0.007	0.012	0.012	0.010	0.019	0.010	N/A
47th	0.035	0.058	0.091	0.076	0.129	0.071	N/A
48th	0.005	0.008	0.011	0.009	0.022	0.012	N/A
49th	0.028	0.047	0.078	0.065	0.131	0.072	N/A
50th	0.010	0.016	0.017	0.015	0.028	0.015	N/A
THD [%]	--	2.283	--	1.241	--	1.012	13
PWHD [%]	--	5.212	--	3.493	--	2.474	22

Note:

Samplerate : 20 kHz

Choose the maximum value of three phases.

5.7 EMC and power quality**5.7.2.2 Verification procedure for harmonics, interharmonics and higher frequencies up to 9 kHz****P**

The currents of the interharmonics to 2 kHz must be measured in accordance with DIN EN 61000-4-7 (VDE 0817-4-7), Annex A. The measurements of higher-frequency harmonic currents between 2 kHz and 9 kHz must be conducted in line with DIN EN 61000-4-7 (VDE 0847-4-7), Annex B.

Model: TAA-125KW-11**Harmonics**

P/P _n [%]	0/5	10	20	30	40	50	60	70	80	90	100
Order	I [%]	I [%]	I [%]	I [%]	I [%]	I [%]	I [%]	I [%]	I [%]	I [%]	I [%]
2	0.071	0.139	0.101	0.138	0.128	0.180	0.107	0.228	0.210	0.231	0.236
3	0.299	0.253	0.147	0.145	0.192	0.131	0.202	0.096	0.130	0.143	0.171
4	0.069	0.061	0.043	0.028	0.023	0.132	0.021	0.131	0.156	0.175	0.182
5	0.156	0.198	0.100	0.120	0.108	0.067	0.088	0.079	0.072	0.098	0.093
6	0.093	0.020	0.021	0.017	0.017	0.095	0.012	0.096	0.099	0.117	0.121
7	0.029	0.049	0.110	0.147	0.042	0.091	0.165	0.118	0.125	0.143	0.154
8	0.057	0.079	0.019	0.016	0.013	0.041	0.021	0.029	0.039	0.038	0.042
9	0.046	0.036	0.053	0.150	0.196	0.077	0.134	0.080	0.069	0.076	0.086
10	0.016	0.022	0.017	0.016	0.012	0.025	0.010	0.027	0.028	0.046	0.046
11	0.041	0.084	0.280	0.437	0.445	0.433	0.453	0.521	0.573	0.625	0.674
12	0.018	0.015	0.011	0.011	0.009	0.029	0.007	0.021	0.022	0.024	0.029
13	0.089	0.048	0.224	0.275	0.341	0.291	0.290	0.296	0.310	0.336	0.370
14	0.021	0.010	0.004	0.009	0.007	0.027	0.005	0.016	0.020	0.020	0.024
15	0.066	0.116	0.094	0.197	0.232	0.227	0.204	0.212	0.218	0.226	0.245
16	0.019	0.032	0.008	0.007	0.007	0.030	0.004	0.015	0.014	0.019	0.017
17	0.036	0.148	0.083	0.202	0.204	0.218	0.212	0.206	0.204	0.204	0.206
18	0.013	0.013	0.006	0.008	0.006	0.025	0.005	0.017	0.017	0.019	0.020
19	0.038	0.045	0.076	0.165	0.164	0.176	0.192	0.175	0.171	0.168	0.166
20	0.011	0.007	0.006	0.006	0.005	0.022	0.004	0.015	0.015	0.018	0.020
21	0.039	0.019	0.041	0.101	0.115	0.137	0.144	0.148	0.145	0.142	0.140
22	0.009	0.006	0.006	0.003	0.005	0.020	0.005	0.015	0.015	0.016	0.016
23	0.038	0.077	0.043	0.077	0.103	0.120	0.137	0.142	0.143	0.142	0.138
24	0.008	0.017	0.004	0.004	0.005	0.015	0.004	0.013	0.013	0.016	0.017
25	0.027	0.077	0.053	0.073	0.095	0.097	0.122	0.122	0.126	0.124	0.122

26	0.007	0.007	0.003	0.005	0.004	0.014	0.003	0.012	0.013	0.015	0.014
27	0.023	0.032	0.045	0.061	0.073	0.080	0.091	0.100	0.105	0.104	0.104
28	0.007	0.007	0.004	0.005	0.005	0.013	0.004	0.012	0.013	0.015	0.016
29	0.023	0.026	0.043	0.048	0.060	0.073	0.083	0.094	0.098	0.102	0.100
30	0.007	0.005	0.003	0.003	0.004	0.013	0.003	0.008	0.010	0.012	0.014
31	0.020	0.044	0.049	0.045	0.059	0.066	0.080	0.083	0.087	0.090	0.090
32	0.006	0.010	0.003	0.004	0.003	0.013	0.003	0.008	0.010	0.013	0.012
33	0.017	0.043	0.045	0.040	0.050	0.060	0.067	0.073	0.076	0.077	0.077
34	0.006	0.007	0.003	0.003	0.003	0.012	0.004	0.008	0.010	0.012	0.018
35	0.015	0.027	0.039	0.029	0.040	0.056	0.062	0.072	0.075	0.074	0.075
36	0.005	0.005	0.003	0.003	0.003	0.012	0.002	0.009	0.009	0.010	0.011
37	0.014	0.028	0.040	0.029	0.040	0.051	0.065	0.069	0.072	0.071	0.069
38	0.005	0.005	0.003	0.004	0.003	0.011	0.002	0.008	0.009	0.011	0.012
39	0.013	0.032	0.039	0.029	0.036	0.047	0.058	0.066	0.068	0.068	0.063
40	0.006	0.008	0.003	0.003	0.003	0.012	0.003	0.008	0.009	0.012	0.011
41	0.012	0.027	0.033	0.022	0.025	0.045	0.051	0.066	0.071	0.071	0.067
42	0.006	0.006	0.003	0.003	0.003	0.012	0.002	0.008	0.010	0.011	0.011
43	0.012	0.022	0.032	0.022	0.028	0.044	0.052	0.062	0.071	0.072	0.068
44	0.006	0.004	0.002	0.003	0.004	0.013	0.003	0.007	0.009	0.012	0.013
45	0.012	0.027	0.029	0.019	0.036	0.045	0.049	0.060	0.068	0.070	0.067
46	0.004	0.005	0.003	0.004	0.004	0.013	0.003	0.006	0.009	0.012	0.011
47	0.013	0.029	0.025	0.017	0.029	0.043	0.039	0.055	0.067	0.071	0.071
48	0.006	0.010	0.003	0.003	0.004	0.010	0.004	0.006	0.009	0.012	0.013
49	0.019	0.028	0.020	0.020	0.026	0.039	0.045	0.046	0.065	0.074	0.071
50	0.006	0.008	0.005	0.004	0.004	0.011	0.004	0.006	0.012	0.015	0.016

Interharmonics at continuous operation

P/P _n [%]	0	10	20	30	40	50	60	70	80	90	100
f [Hz]	I _h [%]	I _h [%]	I _h [%]	I _h [%]	I _h [%]	I _h [%]	I _h [%]	I _h [%]	I _h [%]	I _h [%]	I _h [%]
75	0.082	0.193	0.107	0.268	0.307	0.288	0.303	0.294	0.178	0.194	0.182
125	0.070	0.138	0.123	0.184	0.146	0.259	0.147	0.318	0.181	0.116	0.118
175	0.060	0.100	0.091	0.105	0.095	0.177	0.092	0.182	0.207	0.133	0.120
225	0.047	0.071	0.071	0.079	0.081	0.088	0.074	0.096	0.141	0.089	0.076
275	0.041	0.059	0.039	0.048	0.051	0.085	0.080	0.145	0.143	0.148	0.129

325	0.027	0.048	0.029	0.034	0.032	0.050	0.032	0.054	0.066	0.108	0.069
375	0.027	0.041	0.026	0.030	0.028	0.047	0.028	0.052	0.055	0.082	0.081
425	0.026	0.034	0.025	0.028	0.026	0.037	0.025	0.038	0.041	0.050	0.079
475	0.020	0.034	0.018	0.025	0.022	0.038	0.020	0.041	0.042	0.044	0.066
525	0.016	0.027	0.016	0.021	0.021	0.022	0.019	0.026	0.026	0.026	0.035
575	0.015	0.022	0.014	0.020	0.019	0.019	0.015	0.021	0.022	0.023	0.025
625	0.013	0.019	0.013	0.017	0.018	0.018	0.014	0.019	0.018	0.020	0.019
675	0.013	0.018	0.013	0.016	0.018	0.017	0.013	0.017	0.016	0.018	0.018
725	0.013	0.018	0.013	0.016	0.017	0.017	0.014	0.017	0.017	0.018	0.018
775	0.014	0.017	0.013	0.015	0.016	0.016	0.013	0.016	0.016	0.017	0.018
825	0.015	0.021	0.019	0.015	0.015	0.016	0.012	0.015	0.015	0.015	0.017
875	0.013	0.017	0.011	0.014	0.014	0.014	0.012	0.014	0.014	0.014	0.017
925	0.013	0.015	0.010	0.013	0.012	0.013	0.010	0.013	0.012	0.012	0.014
975	0.013	0.014	0.011	0.014	0.013	0.014	0.010	0.013	0.012	0.011	0.013
1025	0.010	0.012	0.010	0.012	0.011	0.013	0.011	0.013	0.013	0.013	0.014
1075	0.008	0.012	0.009	0.011	0.010	0.011	0.009	0.011	0.011	0.011	0.011
1125	0.008	0.013	0.009	0.012	0.013	0.014	0.009	0.014	0.011	0.011	0.011
1175	0.008	0.011	0.009	0.010	0.010	0.011	0.009	0.011	0.011	0.010	0.011
1225	0.007	0.011	0.009	0.010	0.010	0.010	0.008	0.011	0.010	0.010	0.010
1275	0.008	0.011	0.008	0.010	0.010	0.009	0.008	0.010	0.010	0.010	0.014
1325	0.007	0.011	0.008	0.010	0.010	0.010	0.008	0.010	0.010	0.010	0.010
1375	0.008	0.010	0.010	0.011	0.010	0.009	0.008	0.010	0.010	0.009	0.010
1425	0.007	0.010	0.008	0.009	0.009	0.009	0.008	0.010	0.010	0.011	0.010
1475	0.008	0.011	0.008	0.009	0.010	0.009	0.007	0.009	0.009	0.010	0.009
1525	0.009	0.013	0.008	0.008	0.009	0.009	0.007	0.009	0.009	0.009	0.009
1575	0.008	0.013	0.008	0.009	0.010	0.009	0.008	0.009	0.009	0.009	0.009
1625	0.008	0.012	0.007	0.008	0.008	0.019	0.007	0.009	0.009	0.016	0.010
1675	0.009	0.010	0.008	0.009	0.009	0.010	0.009	0.009	0.021	0.022	0.011
1725	0.007	0.010	0.007	0.008	0.008	0.008	0.008	0.013	0.014	0.009	0.028
1775	0.007	0.009	0.008	0.008	0.008	0.008	0.007	0.020	0.009	0.009	0.009
1825	0.007	0.009	0.007	0.008	0.008	0.008	0.007	0.009	0.009	0.008	0.010
1875	0.007	0.009	0.008	0.008	0.008	0.008	0.007	0.008	0.008	0.008	0.008
1925	0.009	0.017	0.017	0.020	0.020	0.021	0.007	0.008	0.008	0.009	0.009
1975	0.008	0.010	0.010	0.010	0.010	0.009	0.009	0.009	0.009	0.009	0.010

Higher Frequencies components											
P/P _n [%]	0	10	20	30	40	50	60	70	80	90	100
f [kHz]	I _h [%]	I _h [%]	I _h [%]	I _h [%]	I _h [%]	I _h [%]	I _h [%]	I _h [%]	I _h [%]	I _h [%]	I _h [%]
2.1	0.025	0.041	0.049	0.036	0.042	0.067	0.075	0.093	0.103	0.105	0.099
2.3	0.025	0.047	0.043	0.032	0.050	0.067	0.064	0.084	0.099	0.104	0.101
2.5	0.042	0.048	0.039	0.034	0.038	0.060	0.078	0.066	0.092	0.112	0.114
2.7	0.050	0.061	0.048	0.038	0.041	0.069	0.072	0.099	0.058	0.072	0.110
2.9	0.032	0.043	0.031	0.027	0.030	0.061	0.051	0.092	0.090	0.061	0.046
3.1	0.022	0.023	0.023	0.023	0.023	0.038	0.036	0.055	0.085	0.086	0.061
3.3	0.017	0.017	0.019	0.019	0.019	0.019	0.035	0.023	0.048	0.068	0.068
3.5	0.025	0.028	0.024	0.023	0.022	0.021	0.041	0.030	0.028	0.055	0.070
3.7	0.039	0.040	0.034	0.041	0.031	0.039	0.034	0.055	0.048	0.037	0.058
3.9	0.043	0.042	0.039	0.046	0.040	0.048	0.042	0.056	0.071	0.064	0.059
4.1	0.052	0.056	0.048	0.060	0.049	0.052	0.093	0.086	0.131	0.124	0.104
4.3	0.053	0.066	0.052	0.050	0.050	0.047	0.065	0.065	0.085	0.102	0.094
4.5	0.118	0.100	0.069	0.065	0.071	0.068	0.072	0.086	0.081	0.097	0.090
4.7	0.083	0.074	0.049	0.066	0.073	0.078	0.053	0.081	0.087	0.093	0.095
4.9	0.106	0.114	0.098	0.106	0.115	0.167	0.147	0.237	0.266	0.258	0.226
5.1	0.081	0.088	0.078	0.081	0.085	0.108	0.103	0.149	0.173	0.171	0.154
5.3	0.018	0.020	0.014	0.016	0.017	0.018	0.016	0.021	0.029	0.036	0.043
5.5	0.011	0.017	0.014	0.015	0.016	0.016	0.011	0.016	0.021	0.027	0.031
5.7	0.011	0.012	0.011	0.011	0.011	0.010	0.009	0.013	0.016	0.021	0.022
5.9	0.009	0.010	0.011	0.011	0.011	0.009	0.010	0.010	0.011	0.014	0.015
6.1	0.015	0.013	0.015	0.014	0.014	0.014	0.012	0.013	0.014	0.016	0.020
6.3	0.066	0.063	0.062	0.066	0.071	0.085	0.085	0.105	0.119	0.136	0.158
6.5	0.058	0.056	0.057	0.059	0.063	0.073	0.074	0.089	0.099	0.111	0.126
6.7	0.011	0.010	0.012	0.011	0.011	0.010	0.010	0.010	0.010	0.010	0.011
6.9	0.006	0.006	0.007	0.007	0.007	0.006	0.007	0.007	0.008	0.009	0.009
7.1	0.006	0.006	0.006	0.007	0.007	0.006	0.007	0.007	0.008	0.009	0.009
7.3	0.005	0.006	0.007	0.007	0.007	0.007	0.007	0.007	0.008	0.009	0.010
7.5	0.005	0.005	0.006	0.006	0.007	0.006	0.007	0.007	0.007	0.007	0.007
7.7	0.005	0.005	0.006	0.006	0.006	0.006	0.006	0.007	0.007	0.008	0.008
7.9	0.005	0.005	0.006	0.006	0.007	0.006	0.007	0.008	0.009	0.009	0.009

8.1	0.005	0.005	0.006	0.006	0.006	0.005	0.006	0.006	0.007	0.007	0.007
8.3	0.005	0.005	0.006	0.006	0.006	0.005	0.006	0.006	0.006	0.006	0.006
8.5	0.005	0.005	0.006	0.007	0.007	0.005	0.007	0.006	0.006	0.007	0.007
8.7	0.006	0.006	0.007	0.007	0.007	0.006	0.007	0.007	0.007	0.007	0.006
8.9	0.005	0.006	0.006	0.006	0.006	0.006	0.006	0.007	0.007	0.006	0.006

Note:

The normalization current is 181.15A.

$R_A=0.15 \Omega$ $j_{XA}=0.15 \Omega$

$R_N=0.16 \Omega$ $j_{XN}=0.10 \Omega$

Samplerate: 20 kHz

Choose the maximum value of three phases

5.7.2.3 Verification procedure for flicker and rapid voltage change								P	
5.7.2.3.3 Flicker - Generation units with a rated current > 75 A									
Model: TAA-125KW-11									
Test:									
Test conditions:		The objective of the test is to determine the system flicker coefficient $c(\Psi_k)$ at the system impedance angles 30°, 50°, 70° and 85°. The flicker coefficient is determined according to the equation in 3.1.8.2. The system flicker coefficient $c(\Psi_k)$ is determined for continuous operation without disturbances. It shall be normalized to the nominal apparent power. The apparent short-circuit power S_k shall be referenced to a symmetrical short-circuit of the test source for a three-phase EUT, and to the single-phase short-circuit power in the case of a single-phase EUT.							
Test:									
%Pn		Pst				Ficker coefficient, $c(\psi_k)$			
		30°	50°	70°	85°	30°	50°	70°	85°
0	L1	0.0010	0.0008	0.0009	0.0011	0.0330	0.0264	0.0297	0.0363
	L2	0.0010	0.0008	0.0010	0.0012	0.0330	0.0264	0.0330	0.0396
	L3	0.0010	0.0008	0.0009	0.0011	0.0330	0.0264	0.0297	0.0363
10	L1	0.0010	0.0010	0.0010	0.0010	0.0330	0.0330	0.0330	0.0330
	L2	0.0010	0.0010	0.0010	0.0009	0.0330	0.0330	0.0330	0.0297
	L3	0.0008	0.0008	0.0008	0.0008	0.0264	0.0264	0.0264	0.0264
20	L1	0.0009	0.0009	0.0009	0.0009	0.0297	0.0297	0.0297	0.0297
	L2	0.0008	0.0008	0.0008	0.0008	0.0264	0.0264	0.0264	0.0264
	L3	0.0008	0.0009	0.0009	0.0009	0.0264	0.0297	0.0297	0.0297
30	L1	0.0011	0.0011	0.0011	0.0011	0.0363	0.0363	0.0363	0.0363
	L2	0.0011	0.0011	0.0011	0.0011	0.0363	0.0363	0.0363	0.0363
	L3	0.0009	0.0009	0.0009	0.0009	0.0297	0.0297	0.0297	0.0297
40	L1	0.0009	0.0009	0.0009	0.0009	0.0297	0.0297	0.0297	0.0297
	L2	0.0009	0.0009	0.0009	0.0009	0.0297	0.0297	0.0297	0.0297
	L3	0.0009	0.0009	0.0009	0.0009	0.0297	0.0297	0.0297	0.0297
50	L1	0.0010	0.0010	0.0010	0.0010	0.0330	0.0330	0.0330	0.0330
	L2	0.0010	0.0010	0.0010	0.0009	0.0330	0.0330	0.0330	0.0297
	L3	0.0009	0.0009	0.0009	0.0009	0.0297	0.0297	0.0297	0.0297

60	L1	0.0011	0.0010	0.0010	0.0010	0.0363	0.0330	0.0330	0.0330
	L2	0.0010	0.0010	0.0010	0.0010	0.0330	0.0330	0.0330	0.0330
	L3	0.0010	0.0010	0.0009	0.0009	0.0330	0.0330	0.0297	0.0297
70	L1	0.0011	0.0010	0.0010	0.0010	0.0363	0.0330	0.0330	0.0330
	L2	0.0011	0.0010	0.0010	0.0010	0.0363	0.0330	0.0330	0.0330
	L3	0.0010	0.0010	0.0009	0.0009	0.0330	0.0330	0.0297	0.0297
80	L1	0.0010	0.0010	0.0010	0.0010	0.0330	0.0330	0.0330	0.0330
	L2	0.0010	0.0010	0.0010	0.0010	0.0330	0.0330	0.0330	0.0330
	L3	0.0009	0.0009	0.0009	0.0009	0.0297	0.0297	0.0297	0.0297
90	L1	0.0011	0.0010	0.0009	0.0009	0.0363	0.0330	0.0297	0.0297
	L2	0.0011	0.0010	0.0009	0.0009	0.0363	0.0330	0.0297	0.0297
	L3	0.0010	0.0010	0.0009	0.0009	0.0330	0.0330	0.0297	0.0297
100	L1	0.0012	0.0011	0.0010	0.0010	0.0396	0.0363	0.0330	0.0330
	L2	0.0011	0.0010	0.0010	0.0009	0.0363	0.0330	0.0330	0.0297
	L3	0.0011	0.0010	0.0009	0.0009	0.0363	0.0330	0.0297	0.0297

Test method:

Although EN IEC 61000-3-11 applies only to rated currents ≤ 75 A, flicker of the EUT with rated currents > 75 A is also determined according to DIN EN 61000-3-11.

For controllable generating units with rated currents > 75 A, at least 12 measurements of 10 min each shall be performed. One measurement shall be made for each of the 10 % active power bins from 0 % Pn or minimum regulating level, whichever is the higher to 90 % Pn and three measurements shall be made for the 10 % active power bin from 100 % Pn.

NOTE 2 This means the midpoints of the bins are near 0 % Pn, 10 % Pn, 20 % Pn to 100 % Pn of the EUT. A measurement consists of determining the short-term flicker strength Pst as a 3-tuple (phases L1, L2 and L3).

The maximum over all Pst shall be selected as the value for the long-term flicker strength Plt.

Note:

Determination of the flicker coefficient:

$$c(\Psi_k) = P_{st, fic} \times \frac{S_{k, fic}}{S_n}$$

Sk.fic/Sn set to 33.

5.7.2.3 Verification procedure for flicker and rapid voltage change					P
5.7.2.3.5 Rapid voltage changes - Generation units with a rated current > 75 A					
Model: TAA-125KW-11					
Max, number of switching operations, N ₁₀	10				
Max, number of switching operations, N ₁₂₀	120				
Case of switching operation	Start up Cut-in at 10% of rated power				
Grid impedance angle, ψ _k	30°	50°	70°	85°	
Flicker step factor, k _f (ψ _k)	0.0012	0.0012	0.0011	0.0011	
Voltage change factor, k _u (ψ _k)	0.0777				
Maximum inrush current factor k _{imax}	0.101				
Case of switching operation	Start-up Cut-in at 100% of rated power				
Grid impedance angle	30°	50°	70°	85°	
Flicker step factor, k _f (ψ _k)	0.0037	0.0030	0.0020	0.0016	
Voltage change factor, k _u (ψ _k)	0.1597				
Maximum inrush current factor k _{imax}	0.671				
Case of switching operation	Service disconnection at rated power				
Grid impedance angle	30°	50°	70°	85°	
Flicker step factor, k _f (ψ _k)	0.0010	0.0009	0.0008	0.0008	
Voltage change factor, k _u (ψ _k)	0.137				
Maximum inrush current factor k _{imax}	0.700				
Worst-case over-all switching operations, k _{imax}	0.700				
Note:					
• The maximum number N _{10m} of the switching operation within a 10-minute period.					
• The maximum number N _{120m} of the switching operation within a 2-hour period.					
• The flicker step factor k _f (ψ _k) for the network impedance phase angles ψ _k = 30°, 50°, 70° and 85°.					
• The voltage change factor k _u (ψ _k) for the network impedance phase angles ψ _k = 30°, 50°, 70° and 85°.					
$k_f(\psi_k) = \frac{1}{130} \times \frac{S_{k, \text{fic}}}{S_n} \times P_{\text{st, fic}} \times T_p^{0,31}$					
Flicker step factor k _f (ψ _k)					
$k_u(\psi_k) = \sqrt{3} \times \frac{U_{\text{fic, max}} - U_{\text{fic, min}}}{U_n} \times \frac{S_{k, \text{fic}}}{S_n}$					
Voltage change factor k _u (ψ _k)					
U _{fic.min} is the minimum one period RMS value of the voltage on the fictitious grid during the switching					

operation;

$U_{fic,max}$ is the maximum one period RMS value of the voltage on the fictitious grid during the switching operation.

$S_{k,fic}/S_n$ in the fictitious grid was set to: 20.83

Choose the maximum value of three phases

5.7.2.4 verification procedure for DC injection**P****Model: TAA-125KW-11**

Protection limit	Tested at four power levels limit 0.5% of $I_{AC;nom}$ (0.9A)		
Output power limit	30%-40%	60%-70%	>95%
Output power(W)	43948	81534	125332
Max. test value [A] L1	0.786	0.751	0.710
Max. test value [A] L2	0.765	0.817	0.777
Max. test value [A] L3	0.744	0.745	0.784

Diagram of permanent dc-injection**Note:**

The average rms value of the mains currents and mains voltages for each phase during the measurement period and the averaged values of the individual DC current components for each phase as percentages of I_{rated} must be written down in the protocol.

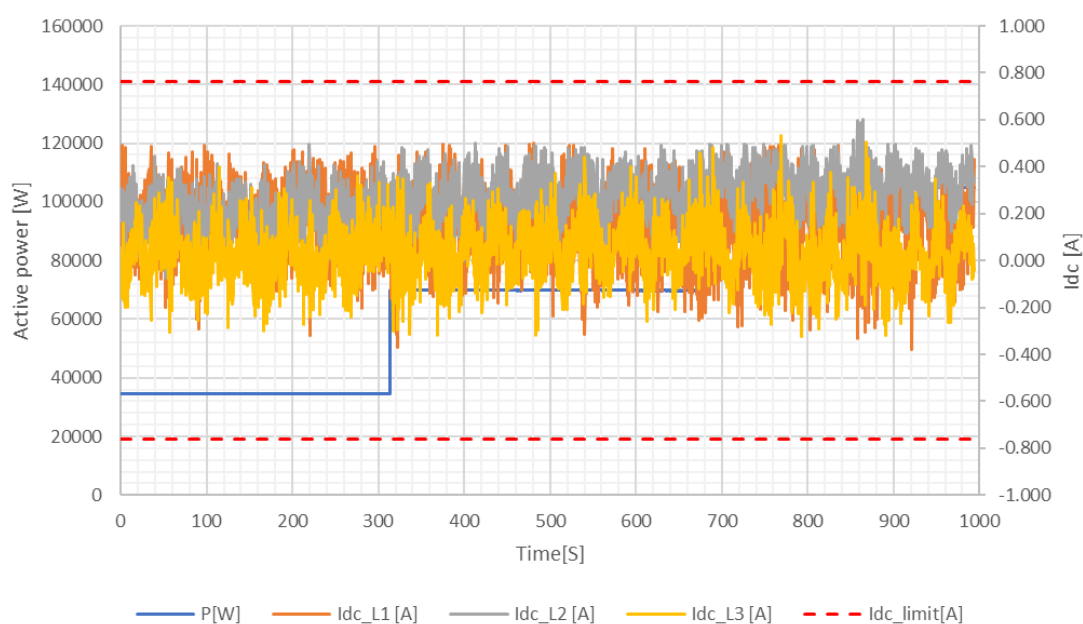
5.7.2.4 verification procedure for DC injection

P

Model: TAA-105KW-11

Protection limit	Tested at four power levels limit 0.5% of $I_{AC;nom}$ (0.761A)		
Output power limit	30%-40%	60%-70%	>95%
Output power(W)	34697	69789.27	104959
Max. test value [A] L1	0.492	0.498	0.492
Max. test value [A] L2	0.495	0.499	0.601
Max. test value [A] L3	0.397	0.457	0.534

Diagram of permanent dc-injection

**Note:**

The average rms value of the mains currents and mains voltages for each phase during the measurement period and the averaged values of the individual DC current components for each phase as percentages of I_{rated} must be written down in the protocol.

5.7.2	EMC and power quality Unbalance					P
Setting values	cos φ = 1			1.00		
	cos φ over-excited			+0.80		
	cos φ under-excited			-0.80		
Test:						
1-min mean value	L1	L2	L3	L1 – L2	L2 – L3	L3 – L1
a) cos φ = 1 at 100 % P _{rE} ± 5 % P _E max						
S _{E60} [VA]	41646	41904	41935	258	31	289
	41640	41886	41925	246	39	285
	41601	41861	41906	260	45	305
	41613	41865	41917	252	52	304
	41575	41861	41911	286	50	336
cos φ _{E60}	0.9999					
max. asymmetry [VA]	336					
b) maximum under-excited (i) at 100 % P _{rE} ± 5 % P _E max						
S _{E60} [VA]	45933	46175	46193	242	18	260
	45945	46198	46224	253	26	279
	45996	46207	46235	211	28	239
	45953	46189	46217	236	28	264
	45928	46192	46204	264	12	276
cos φ _{E60}	0.8025					
max. asymmetry [VA]	279					
c) maximum over-excited (c) at 100 % P _{rE} ± 5 % P _E max						
S _{E60} [VA]	45740	46079	46177	339	98	437
	45732	46077	46159	345	82	427
	45738	46075	46168	337	93	430
	45731	46078	46170	347	92	439
	45719	46081	46158	362	77	439
cos φ _{E60}	0.8012					
max. asymmetry [VA]	439					
d) cos φ = 1 at 50 % P _{rE} ± 5 % P _E max						
S _{E60} [VA]	20711	21020	21162	309	142	451
	20699	21020	21149	321	129	450

	20688	21030	21155	342	125	467
	20689	21034	21149	345	115	460
	20684	21025	21142	341	117	458
cos φ _{E60}	0.9996					
max. asymmetry [VA]	467					
e) maximum under-excited (i) at 50 % P _{rE} ± 5 % P _{E_{max}}						
S _{E60} [VA]	26097	26486	26594	389	108	497
	26089	26478	26602	389	124	513
	26078	26465	26598	387	133	520
	26084	26460	26599	376	139	515
	26096	26468	26601	372	133	505
cos φ _{E60}	0.7968					
max. asymmetry [VA]	520					
f) maximum over-excited (c) at 50 % P _{rE} ± 5 % P _{E_{max}}						
S _{E60} [VA]	25654	26021	26174	367	153	520
	25650	26023	26160	373	137	510
	25650	26031	26156	381	125	506
	25646	26031	26168	385	137	522
	25638	26025	26180	387	155	542
cos φ _{E60} :	0.8056					
max. asymmetry [VA]	542					
Limit:	≤5% S _{E_{max}} and ≤4600VA					
Test: The maximum absolute difference between the apparent powers of the three phases is determined for each of the five measurements (1-min means) in the respective operating point. The maximum of these five values is again determined.						
Assessment criterion: The test is passed if the maximum value from the above measurements does not exceed 5 % S _{E_{max}} and ≤ 4600 VA.						

4.9.3 Requirements on voltage and frequency protection Checklist			--	
Several points to check				
Clause(s) / subclause(s) of this EN 50549-2:2019	Parameter*	Configurable value range	Value default	
5.2 Operating frequency range	47.0 – 47.5 Hz Duration	0 – 20 s	0s	
	47.5 – 48.5 Hz Duration	30 – 90 min	30min	
	48.5 – 49.0 Hz Duration	30 – 90 min	30min	
	49.0 – 51.0 Hz Duration	not configurable	unlimited	
	51.0 – 51.5 Hz Duration	30 – 90 min	30min	
	51. 5 – 52 Hz Duration	0 – 15 min	0s	
4.4.3 Minimal requirement for active power delivery at underfrequency	Reduction threshold	not configurable	49.5Hz	
	Maximum reduction rate	not configurable	10% P _M /Hz	
4.4.4 Continuous operating voltage range	Upper limit	not configurable	110% U _n	
	Lower limit	not configurable	90% U _n	
4.5.2 Rate of change of frequency (ROCOF) immunity	ROCOF withstand capability (defined with a sliding measurement window of 500 ms) non-synchronous generating technology: synchronous generating technology:	not configurable	2 Hz/s	
4.5.3.2 Under-voltage ride through (UVRT) Generating plant with non-synchronous generating technology	Maximum power resumption time	not configurable	1 s	
	Voltage-Time-Diagram	see Figure 6	Time [s]	U [p.u.]
			0.0	0.2
			0.15	0.2
			1.5	0.85
			180	0.85
			180	0.9
4.5.3.3 Under-voltage ride through (UVRT)	Maximum power resumption time	not configurable	N/A	

Generating plant with synchronous generating technology	Voltage-Time-Diagram	see Figure 7	N/A	
4.5.4 Over-voltage ride through (OVRT)	Voltage-Time-Diagram	not configurable see Figure 8	Time [s]	Time [s]
			0.0	1.25
			0.1	1.25
			0.1	1.20
			5.0	1.20
			5.0	1.15
			60	1.15
			60	1.10
4.6.1 Power response to overfrequency	Threshold frequency f_1	50.2 Hz – 52 Hz	50.2 Hz	
	Droop	2 % – 12 %	5 %	
	Power reference	P_M P_{max}	P_{Max} for synchronous generating technology and EESS P_M for other non-synchronous generating technology	
	Intentional delay	0 – 2 s	0s	
	Deactivation threshold f_{stop}	50.0 Hz – f_1	deactivated	
	Deactivation time t_{stop}	0 – 600 s	-	
	Acceptance of staged disconnection	yes no	yes	
4.6.2 Power response to underfrequency	Threshold frequency f_1	49.8 Hz – 46 Hz	49.8 Hz	
	Droop	2 – 12 %	5 %	
	Power reference	P_M P_{max}	P_{max}	
	Intentional delay	0 – 2 s	0 s	
4.7.2.2 voltage support by Capabilities	Active factor/ reactive power (%Pd) range overexcited	0.9 – 1 / 48% P_d -0 0.95 – 1 / 33% P_d -0	0.90 – 1 / 48% P_d -0	
	Active factor/ reactive power (%Pd) range underexcited	0.9 – 1 / 48% P_d -0 0.95 – 1 / 33% P_d -0	0.90 – 1 / 48% P_d -0	
4.7.2.3 voltage support by reactive	Enabled control mode	Q setp.	Q set point	

power - Control modes		Q(U) Q(P) cos φ setp. cos φ (P)	
4.7.2.3.2 voltage support by reactive power - Set point control modes	Q set point and excitation	0 – 48 % P_D 0 – 33 % P_D	0
	cos φ setpoint and excitation	1 – 0.9	1
4.7.2.3.3 voltage support by reactive power - Voltage related control modes	Characteristic curve	-	Indicate default characteristic
	Time constant	3 s – 60 s	10 s
	Min cos φ	0.0 – 1	0.9
	Lock in power	0 % – 20 %	deactivated
	Lock out power	0 % – 20 %	deactivated
4.7.2.3.4 voltage support by reactive power - Power related control mode	Characteristic curve	-	Indicate default characteristic
4.7.4.2.2 Zero current mode for converter connected generating technology / Generating Plant with non-synchronous generator	Enabling	enable disable	disabled
	Static voltage range overvoltage	100 % U_n – 120 % U_n	120 % U_n
	Static voltage range undervoltage	20 % U_n – 100 % U_n	50 % U_n
4.9.3 Requirements on voltage and frequency protection**	Threshold for protection as dedicated device [in A or kW. kVA]	16 A – 250 kVA	Interface protection integrated
	Undervoltage threshold stage 1	0.2 U_n – 1 U_n	0.85 U_n
	Undervoltage operate time stage 1	0.1 s – 100 s	99 s
	Undervoltage threshold stage 2	0.2 U_n – 1 U_n	0.5 U_n
	Undervoltage operate time stage 2	0.1 s – 5 s	4 s
	Overvoltage threshold stage 1	1.0 U_n – 1.2 U_n	1.2 U_n
	Overvoltage operate time stage 1	0.1 s – 100 s	99 s
	Overvoltage threshold stage 2	1.0 U_n – 1.3 U_n	1.3 U_n
	Overvoltage operate time stage 2	0.1 s – 5 s	4 s
	Overvoltage threshold 10 min mean protection	1.0 U_n – 1.15 U_n	1.10 U_n

	Underfrequency threshold stage 1	47.0 Hz – 50.0 Hz	47.5 Hz
	Underfrequency operate time stage 1	0.1 s – 100 s	99 s
	Underfrequency threshold stage 2	47.0 Hz – 50.0 Hz	47 Hz
	Underfrequency operate time stage 2	0.1 s – 5 s	4 s
	Overfrequency threshold stage 1	50.0 Hz – 52.0 Hz	51.5 Hz
	Overfrequency operate time stage 1	0.1 s – 100 s	99 s
	Overfrequency threshold stage 2	50.0 Hz – 52.0 Hz	52 Hz
	Overfrequency operate time stage 2	0.1 s – 5 s	4 s
4.10.2 Automatic reconnection after tripping	Lower frequency	47.0 Hz – 50.0 Hz	49.5 Hz
	Upper frequency	50.0 Hz – 52.0 Hz	50.2 Hz
	Lower voltage	50 % U_n – 100 % U_n	90 % U_n
	Upper voltage	100 % U_n – 120 % U_n	110 % U_n
	Observation time	10 s – 600 s	60 s
	Active power increase gradient	6 % – 3000 %/min	10 % P_n /min
4.10.2 Automatic reconnection after tripping	Lower frequency	47.0 Hz – 50.0 Hz	49.5 Hz
	Upper frequency	50.0 Hz – 52.0 Hz	50.2 Hz
	Lower voltage	50 % U_n – 100 % U_n	90 % U_n
	Upper voltage	100 % U_n – 120 % U_n	110 % U_n
	Observation time	10 s – 600 s	60 s
	Active power increase gradient	6 % – 3000 %/min	10 % /min
4.10.3 Starting to generate electrical power	Lower frequency	47.0 Hz – 50.0 Hz	49.5 Hz
	Upper frequency	50.0 Hz – 52.0 Hz	50.1 Hz
	Lower voltage	50 % – 100 % U_n	85 % U_n
	Upper voltage	100 % – 120 % U_n	110 % U_n
	Observation time	10 s – 600 s	60 s
	Active power increase gradient	6 % – 3000 %/min	disabled
4.11.1 Ceasing active power	Activation option	--	Can be achieved by PGU. (Logic interface shall be specified by DNO)
4.11.2 Reduction of active power on set	Activation option	--	Can be achieved by PGU. (Definition shall be specified

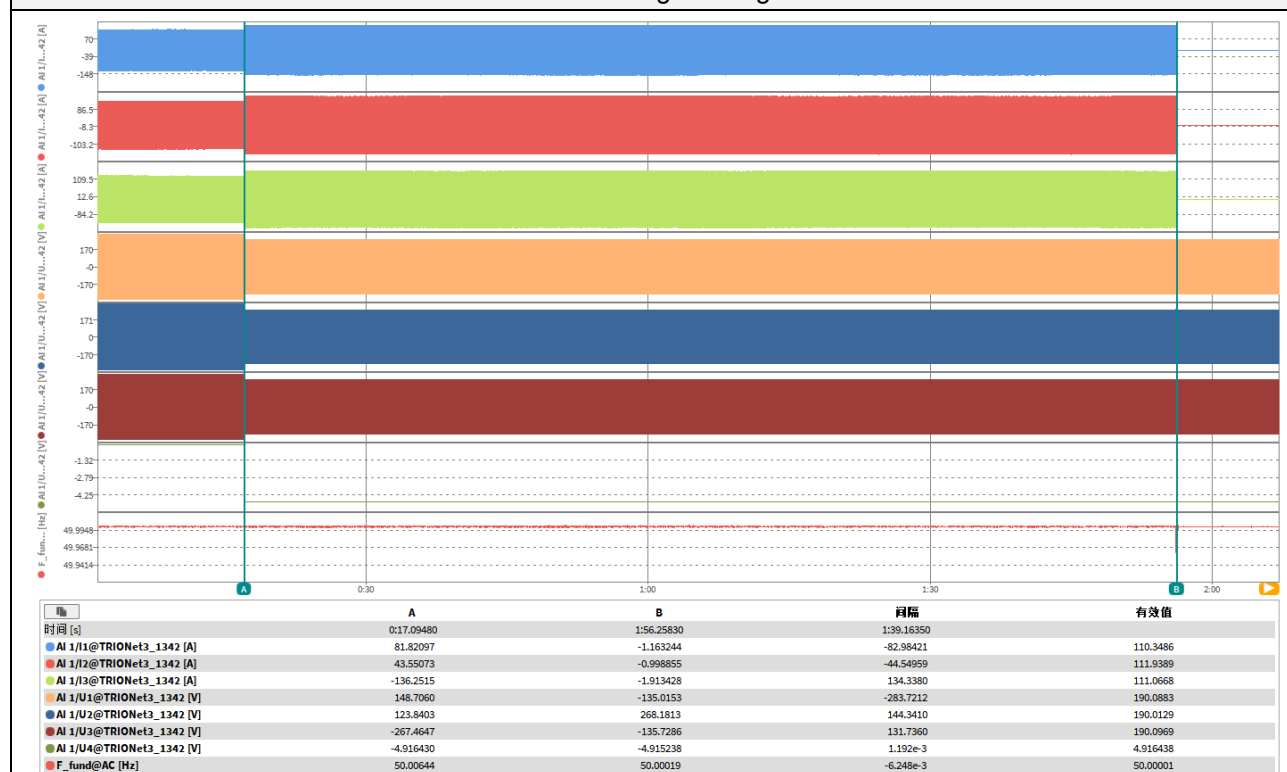
point			by DNO)
4.12 Remote information exchange	Activation option	--	Can be achieved by PGU. (Definition shall be specified by DNO)
*If additional parameters have been evaluated during test, these shall be added as additional lines in the table. **This can be used for manufacturer specific parameter descriptions.			

EN 50549-10:2022		
Clause	Test items	Remark
5.8	Interface protection	
5.8.3	Verification procedure for generating plants to be connected to a LV distribution network with Interface protection as internal device	
5.8.4	Verification procedure for generating plants to be connected to a LV distribution network with Interface protection as dedicated device	
5.8.5	Verification procedure for generating plants to be connected to a MV distribution network	
5.8.5.1	Voltage protections	P
5.8.5.2	Overvoltage 10 min mean protection	P
5.8.5.3	Frequency protections	P
5.8.6	Islanding detection	P

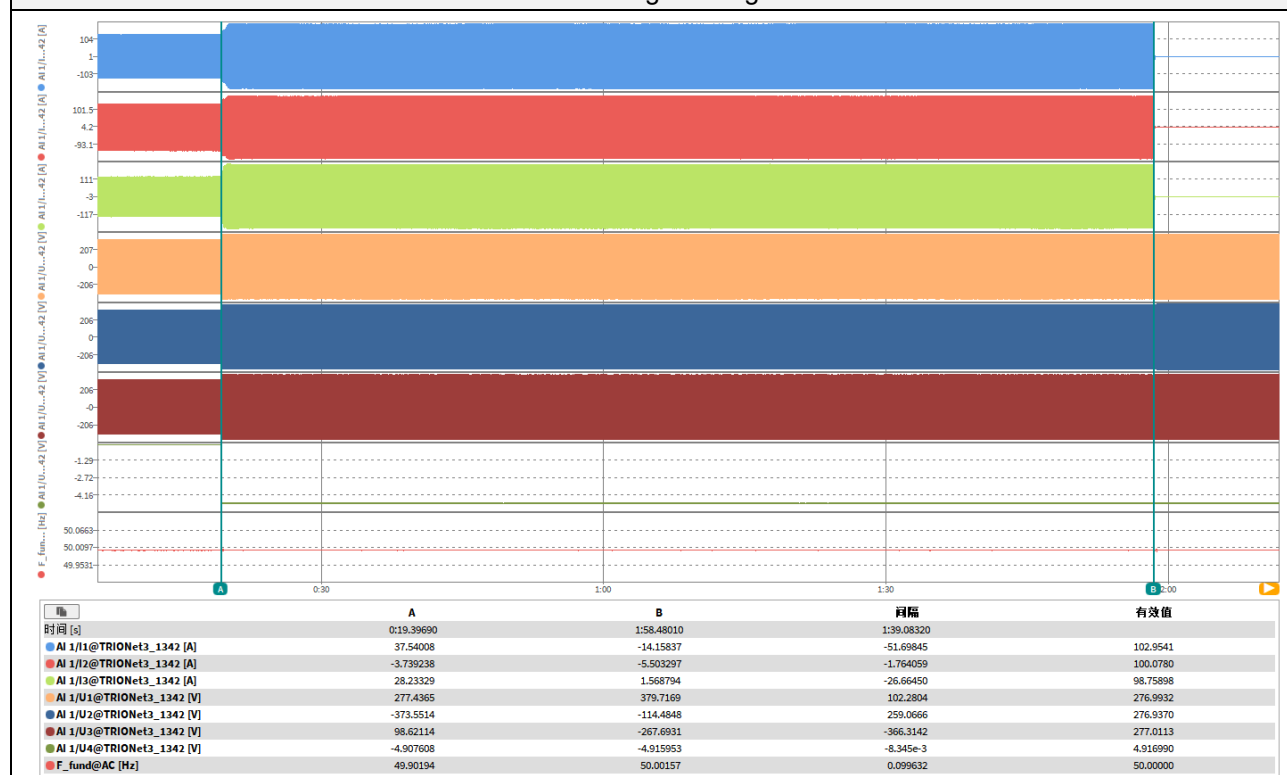
5.8 Interface protection 5.8.5.1 Voltage protections (Three-phase type, L1, L2, L3-N)				P
Test conditions	Output power: $\geq 50\%P_n$, Frequency: $50 \pm 0.2\text{Hz}$			
Stage 1	Under-voltage		Over-voltage	
Parameter	Voltage	Time	Voltage	Time
Limit	$0.85U_n \pm 1\% U_n$	$99\text{s} \leq t \leq 100\text{s}$	$1.2U_n \pm 1\% U_n$	$99\text{s} \leq t \leq 100\text{s}$
Trip value [V]	196.7		277.2	
	196.0		276.5	
	196.3		277.2	
Trip time [s]	$U_n \text{ To } U_{th-low} - 5\%U_n$	99.027	$U_n \text{ To } U_{th-high} + 5\%U_n$	99.083
		99.014		99.054
		99.164		99.025
Stage 2	Under-voltage		Over-voltage	
Parameter	Voltage	Time	Voltage	Time
Limit	$0.5U_n \pm 1\% U_n$	$4\text{s} \leq t \leq 5\text{s}$	$1.3U_n \pm 1\% U_n$	$4\text{s} \leq t \leq 5\text{s}$
Trip value [V]	116.1		299.6	
	115.8		299.7	
	115.4		299.4	
Trip time [s]	$U_n \text{ To } U_{th-low} - 5\%U_n$	4.558	$U_n \text{ To } U_{th-high} + 5\%U_n$	4.876
		4.549		4.830
		4.562		4.760

Scope pictures of the disconnection time

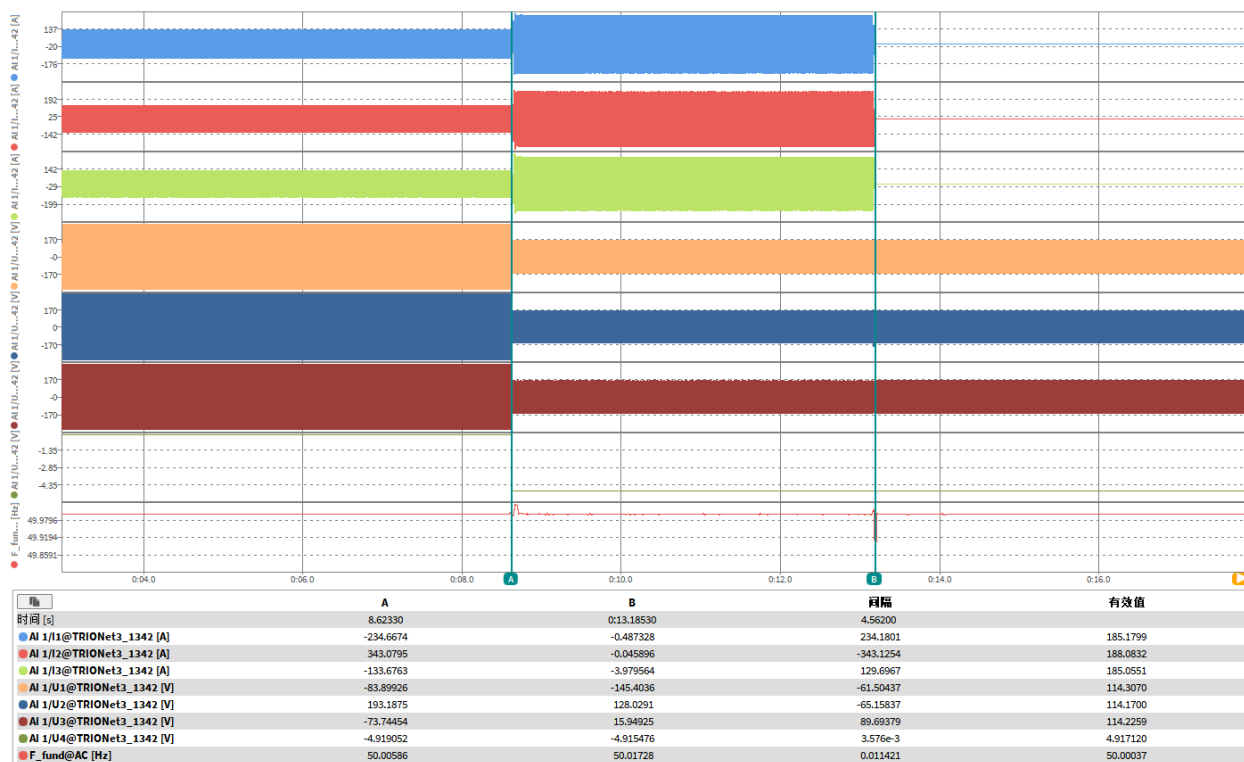
Under -voltage – Stage 1



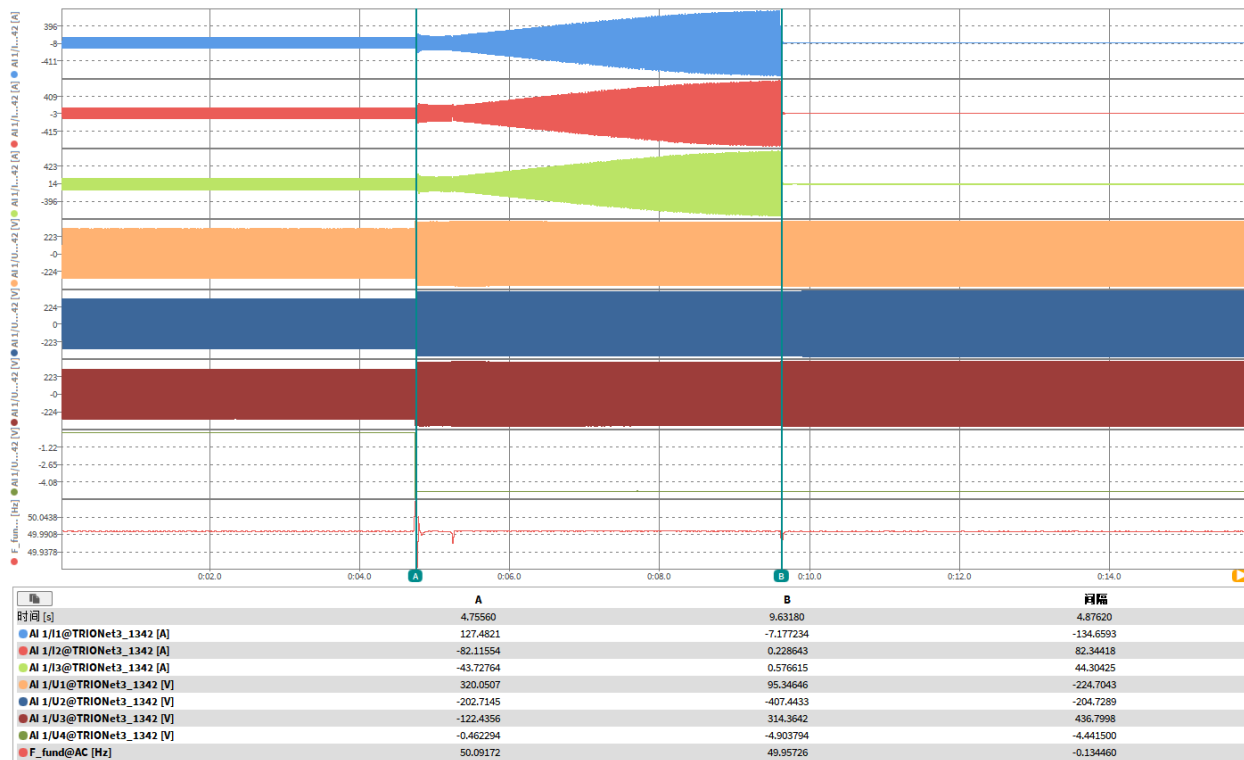
Over-voltage – Stage 1



Under -voltage – Stage 2



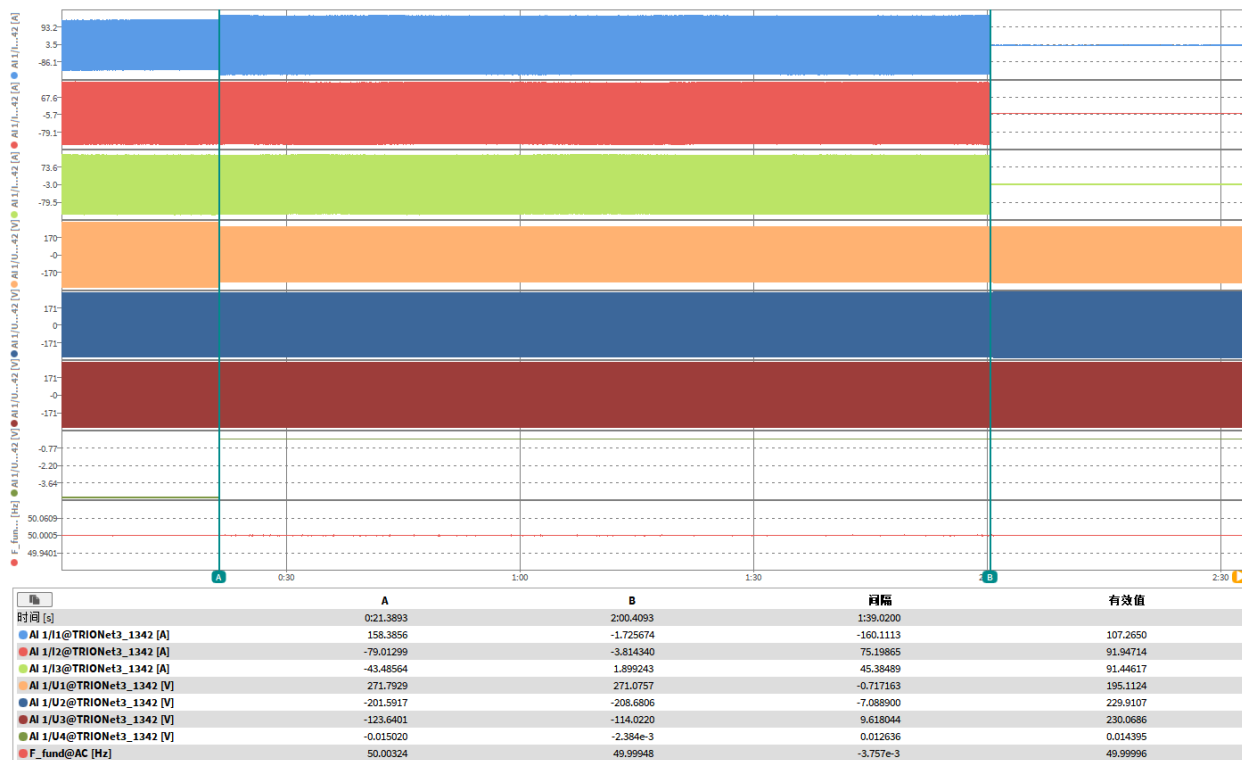
Over-voltage – Stage 2



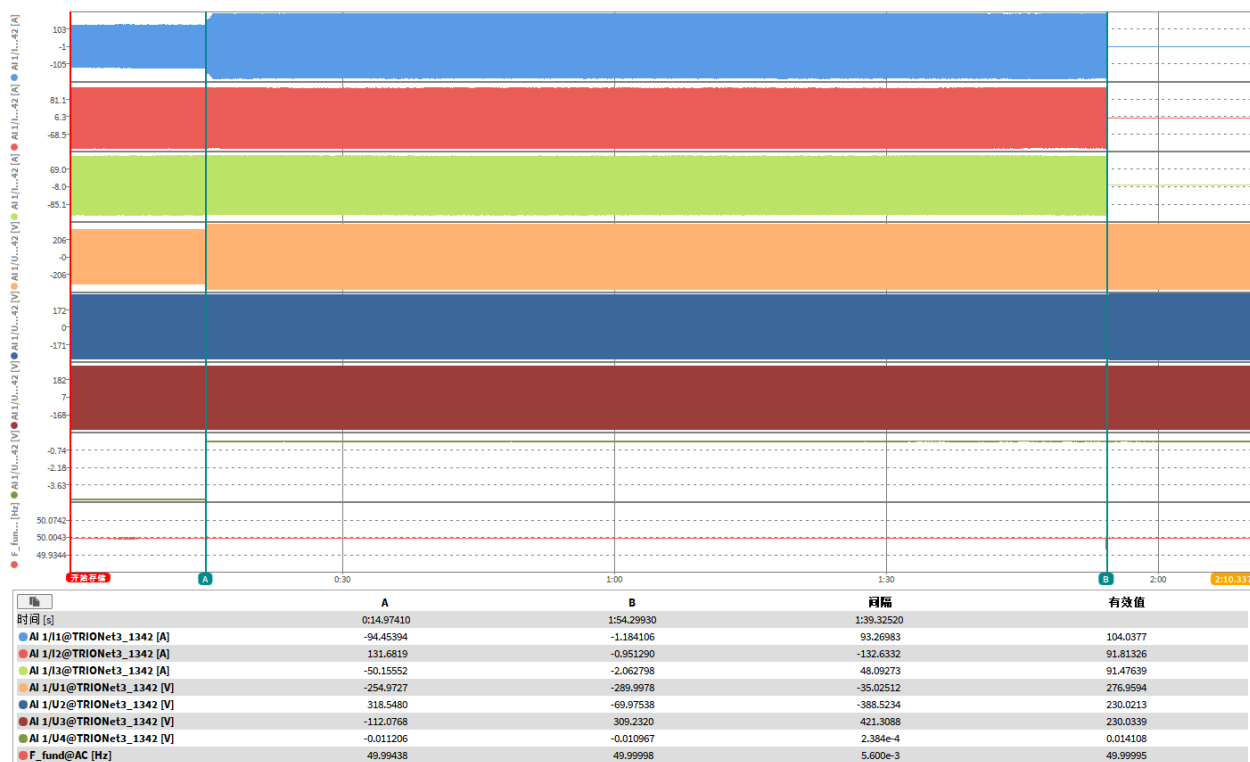
5.8 Interface protection 5.8.5.1 Voltage protections (Three-phase type, L1-N)				P
Test conditions	Output power: $\geq 50\%P_n$, Frequency: $50 \pm 0.2\text{Hz}$			
Stage 1	Under-voltage		Over-voltage	
Parameter	Voltage	Time	Voltage	Time
Limit	$0.85U_n \pm 1\% U_n$	$99\text{s} \leq t \leq 100\text{s}$	$1.2U_n \pm 1\% U_n$	$99\text{s} \leq t \leq 100\text{s}$
Trip value [V]	196.5		277.7	
	197.1		277.3	
	196.8		277.4	
Trip time [s]	$U_n \text{ To } U_{\text{th-low}} - 5\%U_n$	99.004	$U_n \text{ To } U_{\text{th-high}} + 5\%U_n$	99.031
		99.020		99.284
		99.007		99.325
Stage 2	Under-voltage		Over-voltage	
Parameter	Voltage	Time	Voltage	Time
Limit	$0.5U_n \pm 1\% U_n$	$4\text{s} \leq t \leq 5\text{s}$	$1.3U_n \pm 1\% U_n$	$4\text{s} \leq t \leq 5\text{s}$
Trip value [V]	115.9		300.0	
	116.6		299.9	
	116.7		300.5	
Trip time [s]	$U_n \text{ To } U_{\text{th-low}} - 5\%U_n$	4.582	$U_n \text{ To } U_{\text{th-high}} + 5\%U_n$	4.551
		4.564		4.524
		4.539		4.646

Scope pictures of the disconnection time

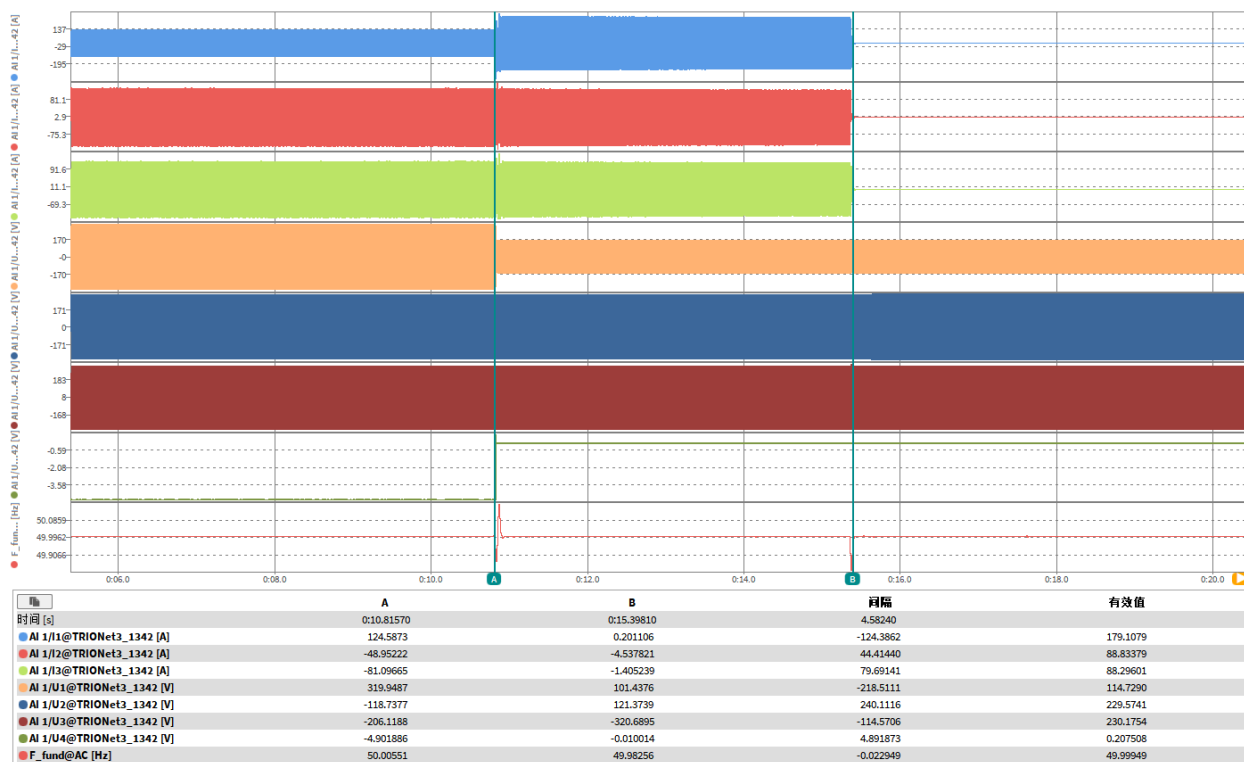
Under -voltage – Stage 1



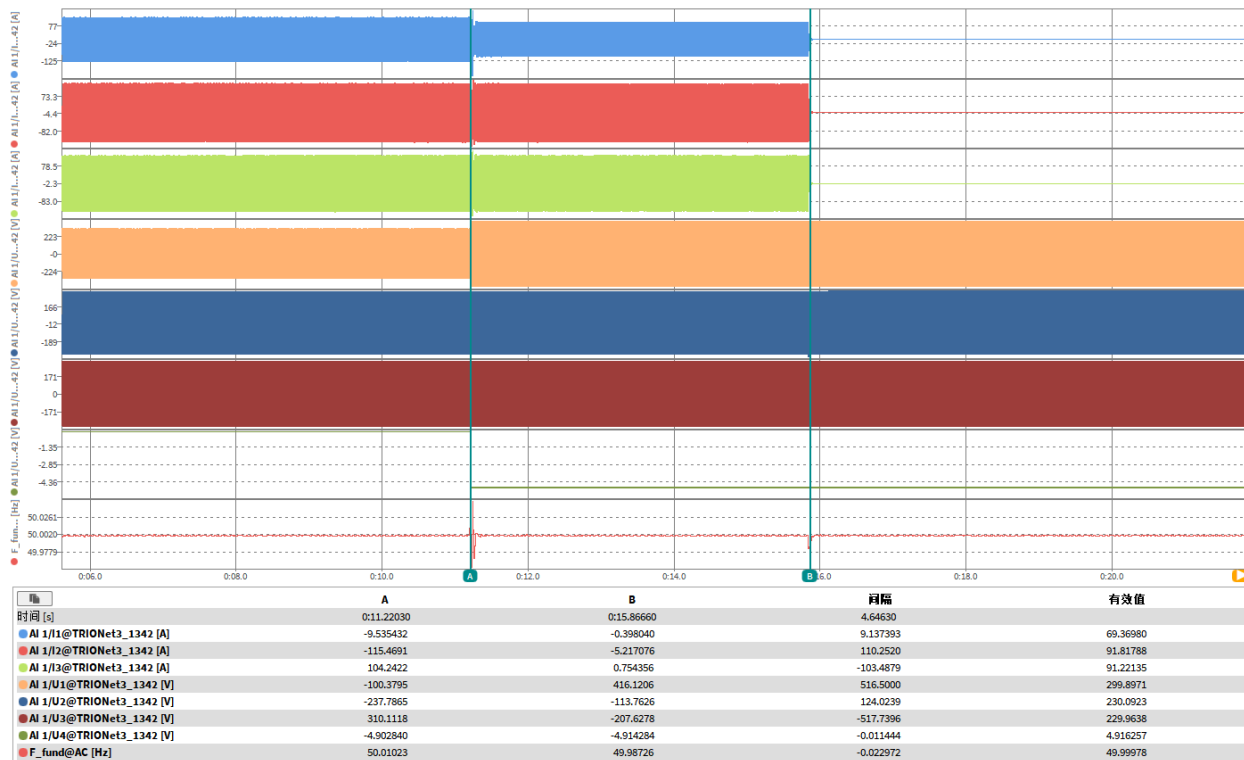
Over-voltage – Stage 1



Under -voltage – Stage 2



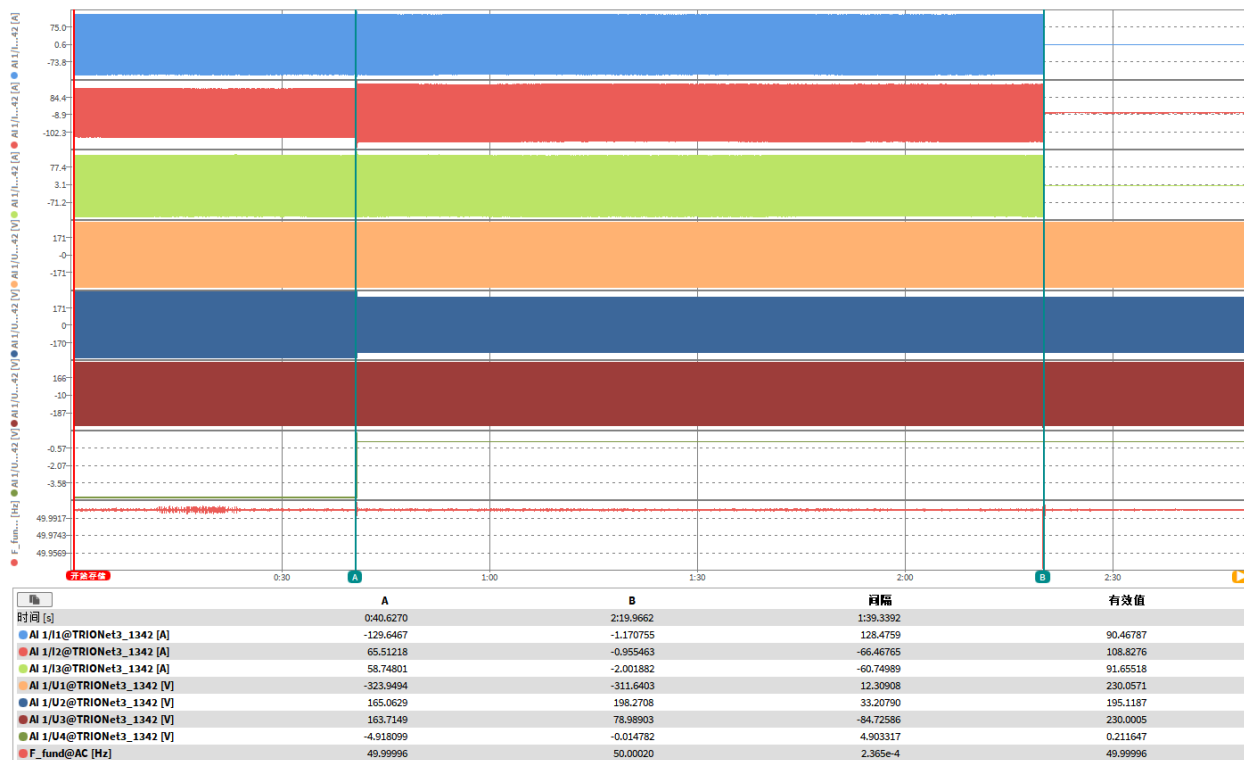
Over-voltage – Stage 2



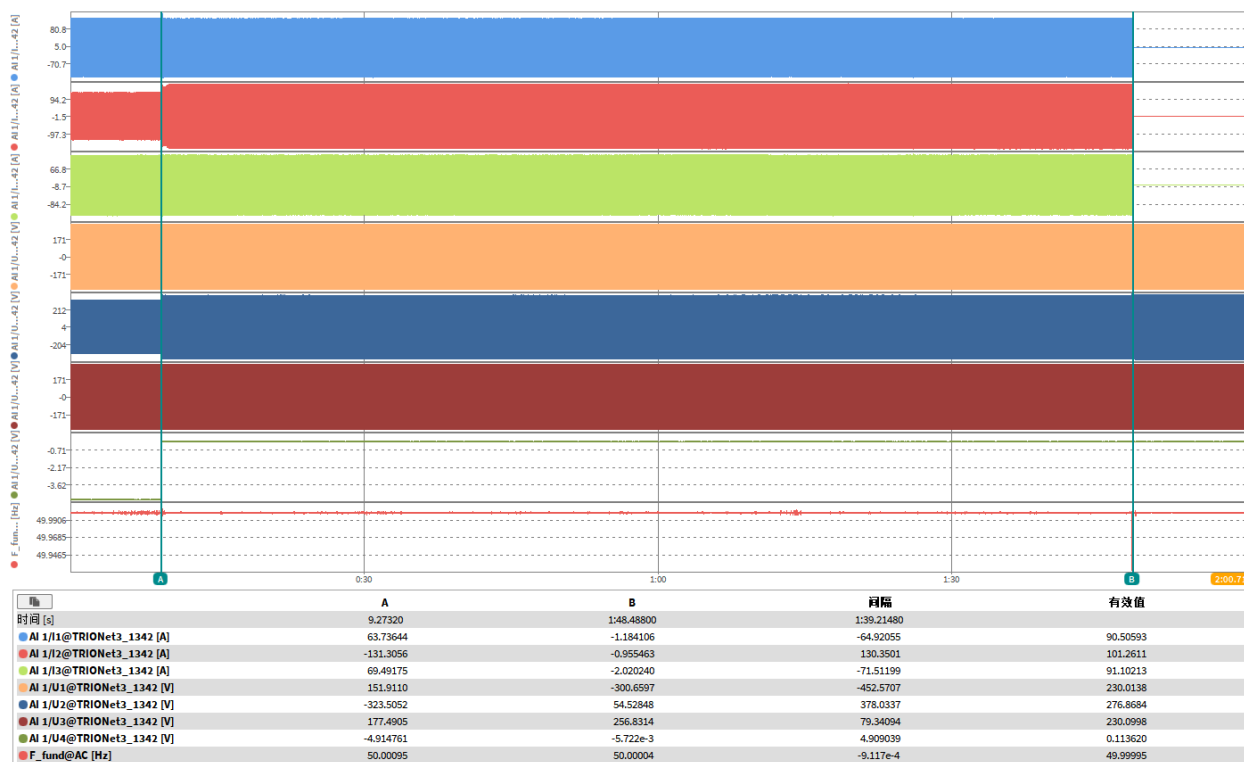
5.8 Interface protection 5.8.5.1 Voltage protections (Three-phase type, L2-N)				P
Test conditions	Output power: $\geq 50\%P_n$, Frequency: $50 \pm 0.2\text{Hz}$			
Stage 1	Under-voltage		Over-voltage	
Parameter	Voltage	Time	Voltage	Time
Limit	$0.85U_n \pm 1\% U_n$	$99\text{s} \leq t \leq 100\text{s}$	$1.2U_n \pm 1\% U_n$	$99\text{s} \leq t \leq 100\text{s}$
Trip value [V]	195.4		276.2	
	196.0		276.8	
	195.3		276.5	
Trip time [s]	$U_n \text{ To } U_{\text{th-low}} - 5\%U_n$	99.339	$U_n \text{ To } U_{\text{th-high}} + 5\%U_n$	99.215
		99.236		99.032
		99.023		99.027
Stage 2	Under-voltage		Over-voltage	
Parameter	Voltage	Time	Voltage	Time
Limit	$0.2U_n \pm 1\% U_n$	$4\text{s} \leq t \leq 5\text{s}$	$1.3U_n \pm 1\% U_n$	$4\text{s} \leq t \leq 5\text{s}$
Trip value [V]	115.3		299.3	
	115.5		299.7	
	115.5		298.9	
Trip time [s]	$U_n \text{ To } U_{\text{th-low}} - 5\%U_n$	4.551	$U_n \text{ To } U_{\text{th-high}} + 5\%U_n$	4.602
		4.559		4.532
		4.570		4.582

Scope pictures of the disconnection time

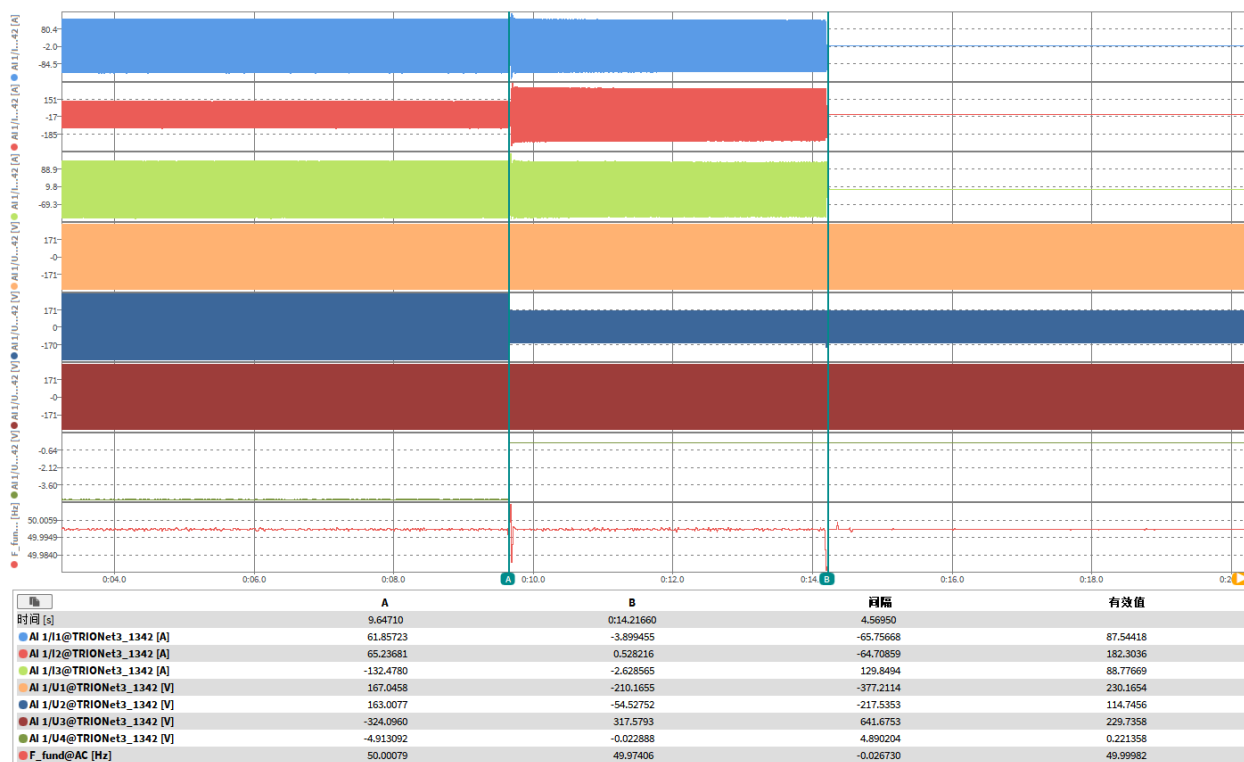
Under -voltage – Stage 1



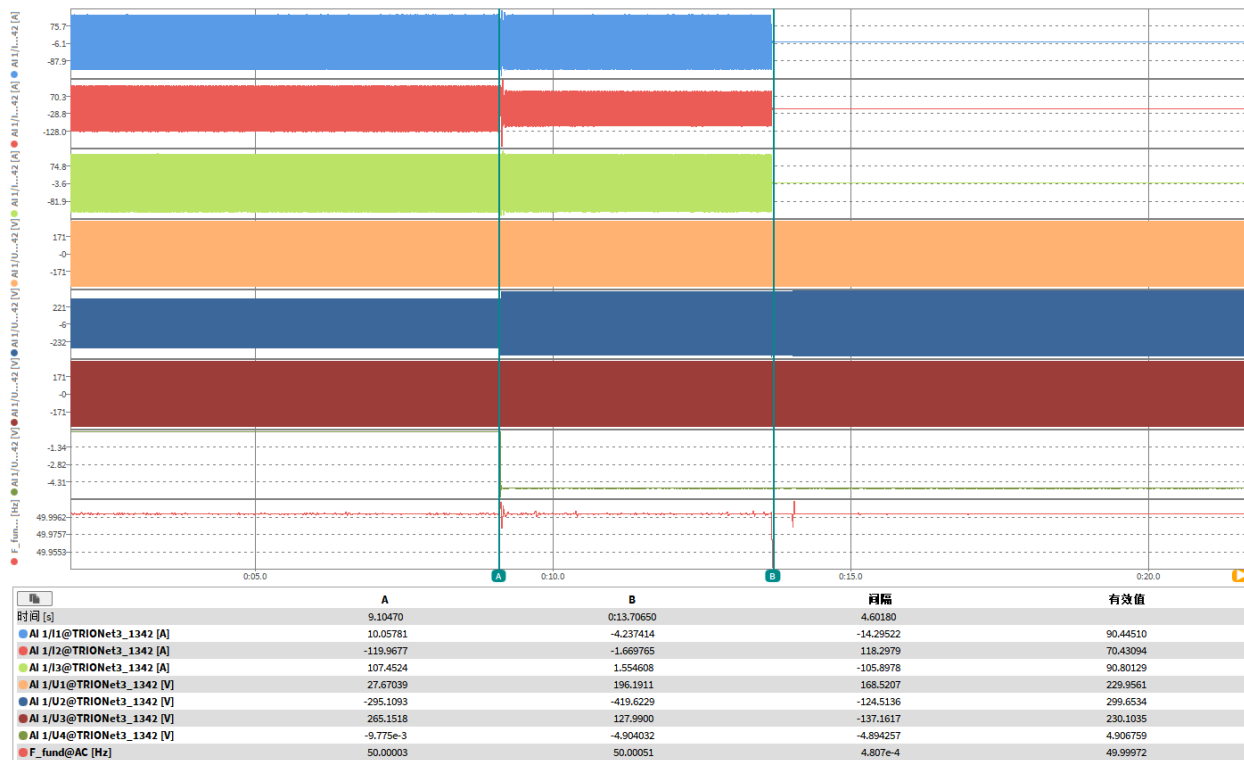
Over-voltage – Stage 1



Under -voltage – Stage 2



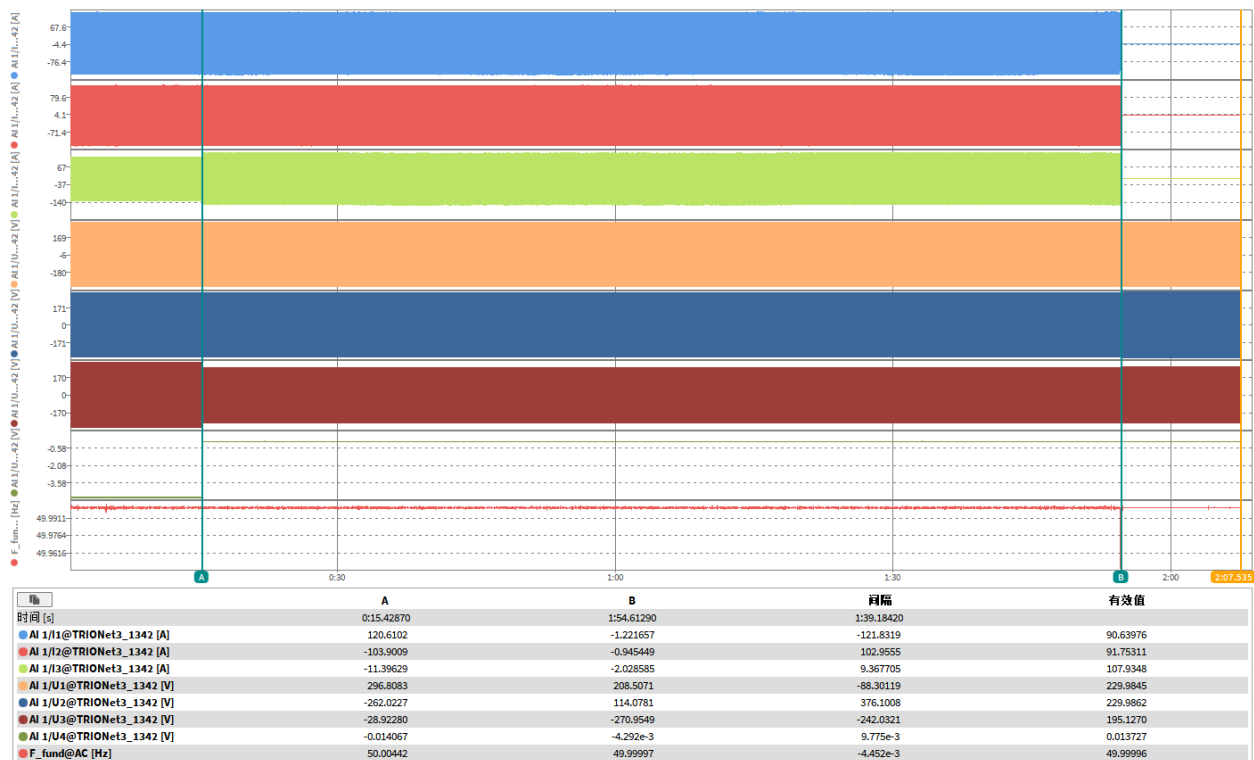
Over-voltage – Stage 2



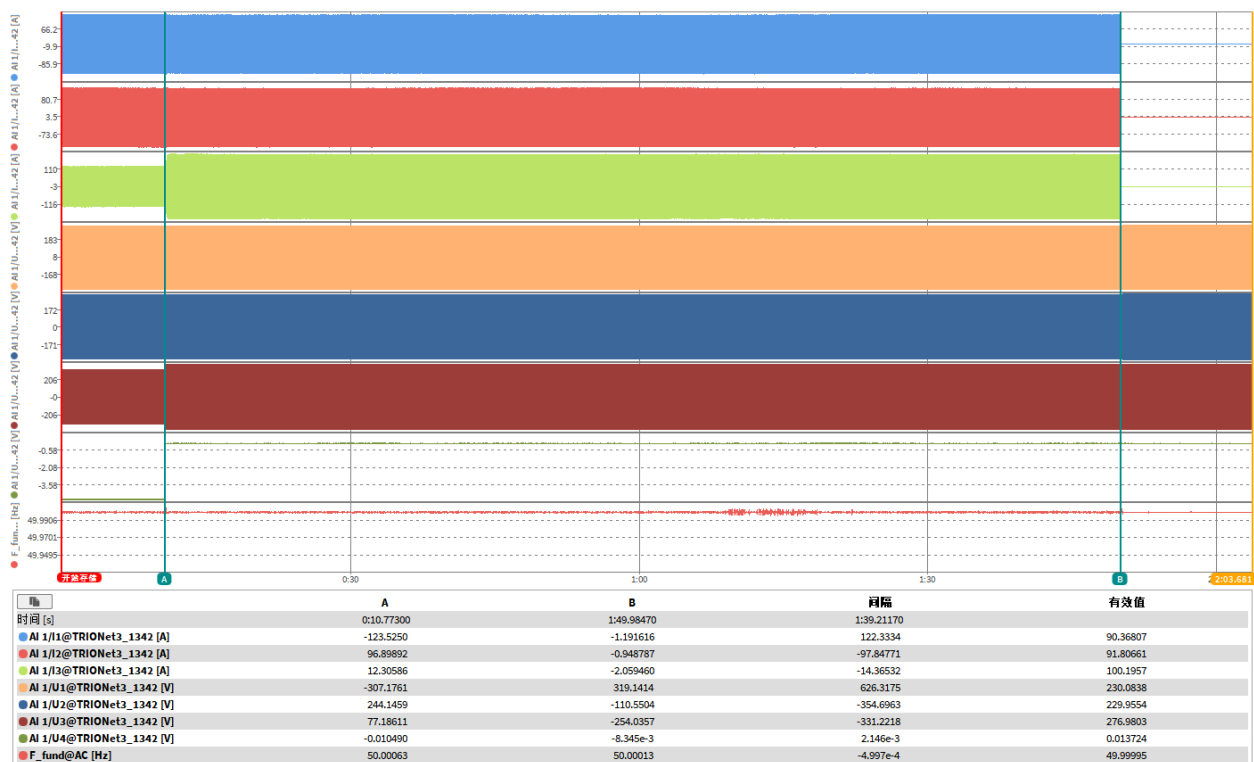
5.8 Interface protection 5.8.5.1 Voltage protections (Three-phase type, L3-N)				P
Test conditions	Output power: $\geq 50\%P_n$, Frequency: $50 \pm 0.2\text{Hz}$			
Stage 1	Under-voltage		Over-voltage	
Parameter	Voltage	Time	Voltage	Time
Limit	$0.85U_n \pm 1\% U_n$	$99s \leq t \leq 100s$	$1.2U_n \pm 1\% U_n$	$99s \leq t \leq 100s$
Trip value [V]	196.3		276.2	
	195.6		275.9	
	195.4		275.8	
Trip time [s]	$U_n \text{ To } U_{th-low} - 5\%U_n$	99.184	$U_n \text{ To } U_{th-high} + 5\%U_n$	99.212
		99.087		99.041
		99.160		99.124
Stage 2	Under-voltage		Over-voltage	
Parameter	Voltage	Time	Voltage	Time
Limit	$0.5U_n \pm 1\% U_n$	$4s \leq t \leq 5s$	$1.3U_n \pm 1\% U_n$	$4s \leq t \leq 5s$
Trip value [V]	115.2		299.3	
	115.1		298.9	
	115.6		298.9	
Trip time [s]	$U_n \text{ To } U_{th-low} - 5\%U_n$	4.539	$U_n \text{ To } U_{th-high} + 5\%U_n$	4.565
		4.547		4.582
		4.569		4.570

Scope pictures of the disconnection time

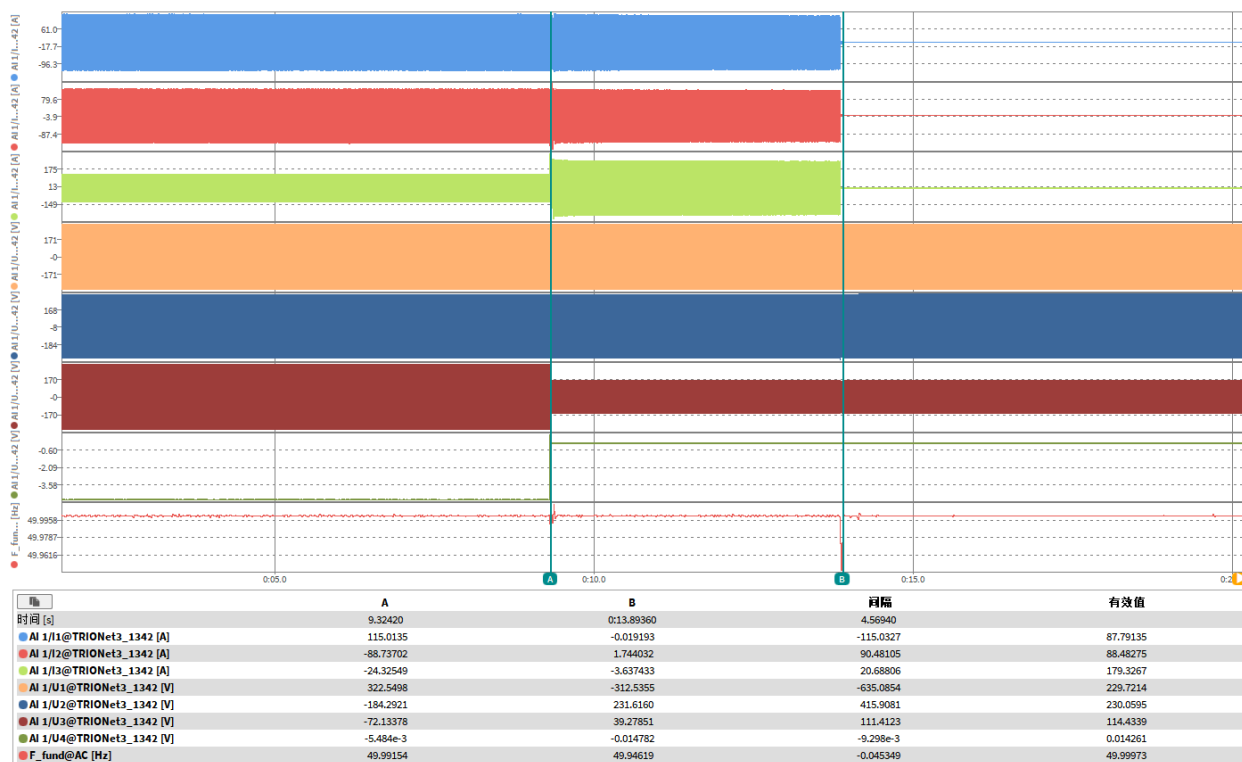
Under -voltage – Stage 1



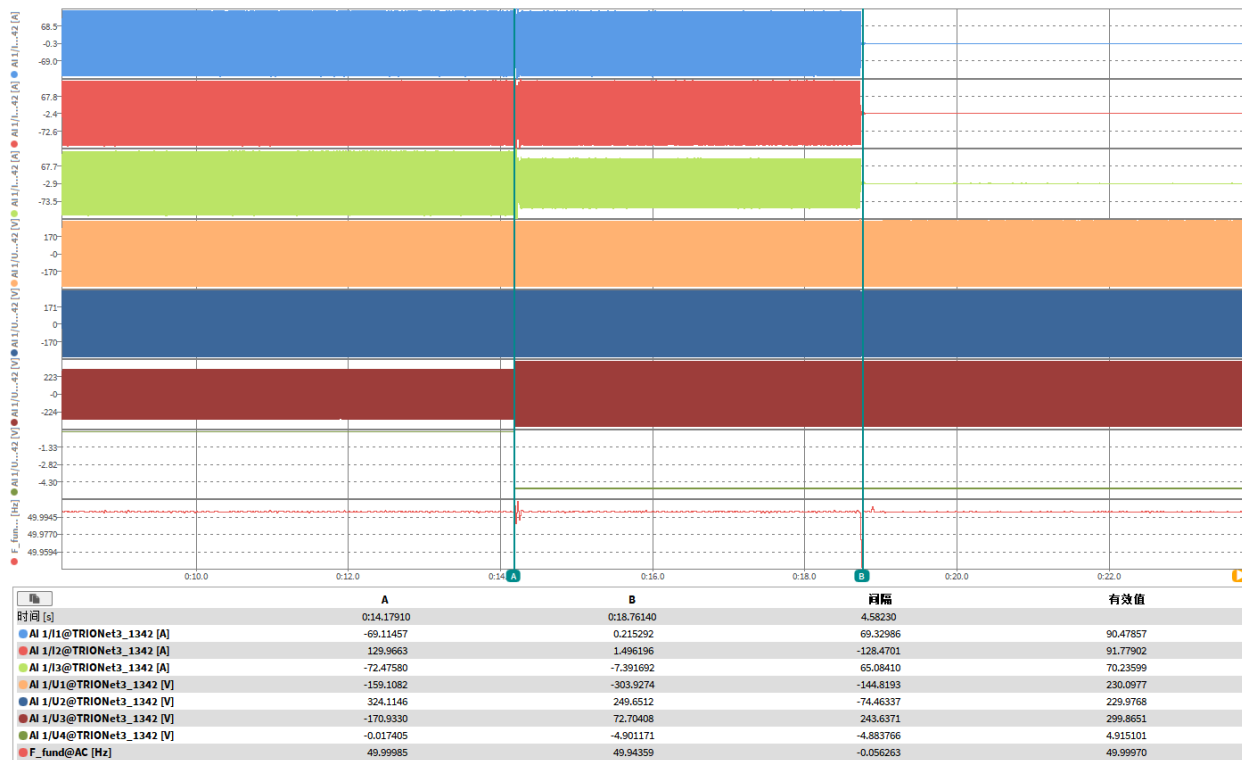
Over-voltage – Stage 1



Under -voltage – Stage 2



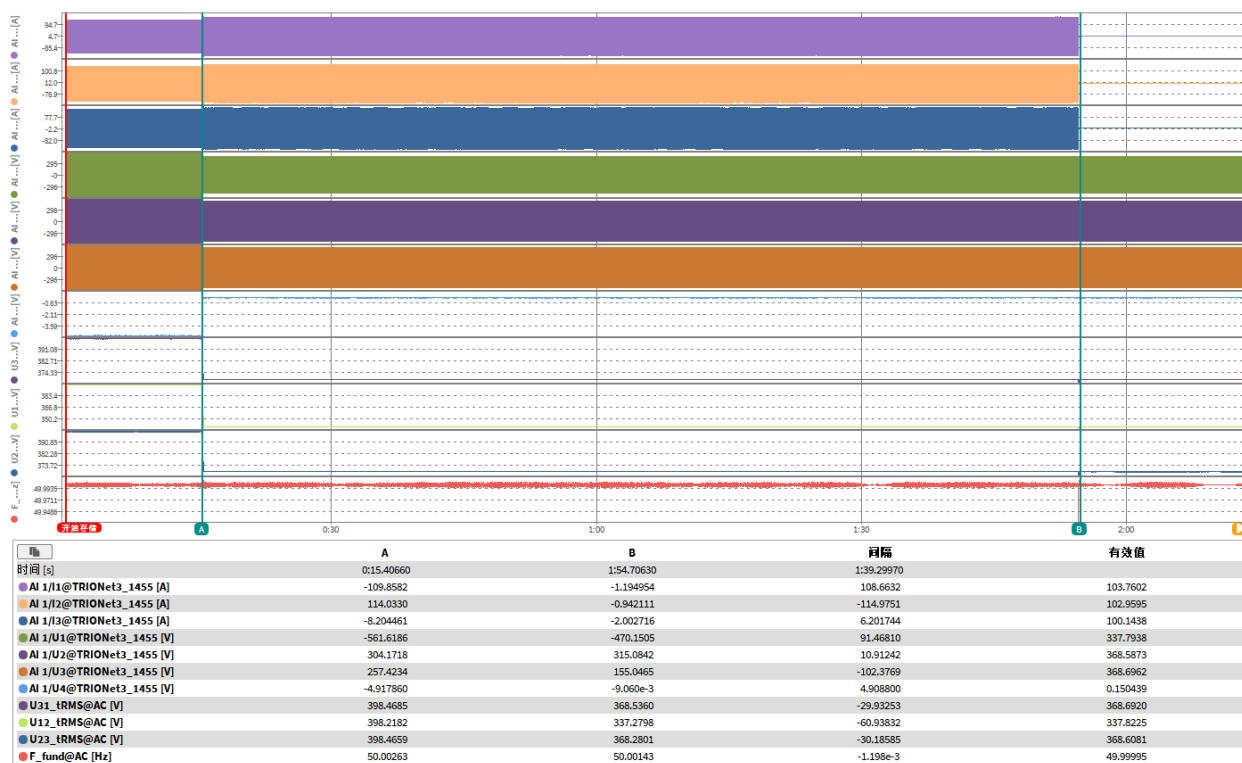
Over-voltage – Stage 2



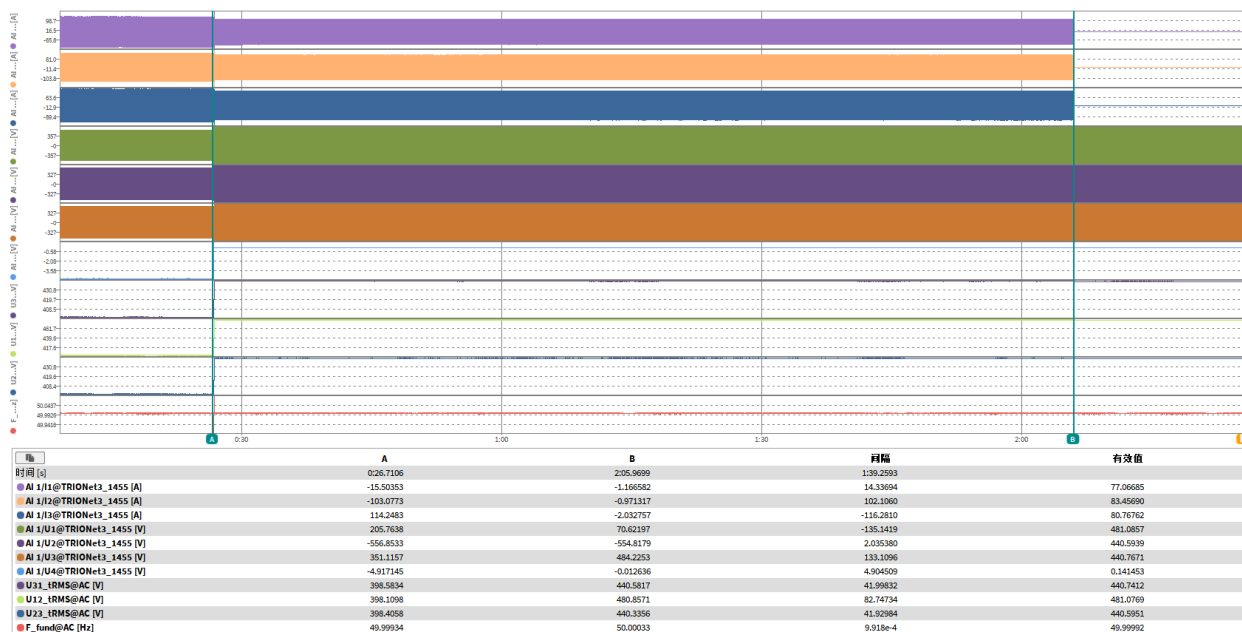
5.8 Interface protection 5.8.5.1 Voltage protections (Three-phase type, L1-L2)				P
Test conditions	Output power: $\geq 50\%P_n$, Frequency: $50 \pm 0.2\text{Hz}$			
Stage 1	Under-voltage		Over-voltage	
Parameter	Voltage	Time	Voltage	Time
Limit	$0.85U_n \pm 1\% U_n$	$99\text{s} \leq t \leq 100\text{s}$	$1.2U_n \pm 1\% U_n$	$99\text{s} \leq t \leq 100\text{s}$
Trip value [V]	340.5		480.6	
	340.9		480.6	
	340.6		480.4	
Trip time [s]	$U_n \text{ To } U_{\text{th-low}} - 5\%U_n$	99.054	$U_n \text{ To } U_{\text{th-high}} + 5\%U_n$	99.259
		99.146		99.112
		99.300		99.169
Stage 2	Under-voltage		Over-voltage	
Parameter	Voltage	Time	Voltage	Time
Limit	$0.5U_n \pm 1\% U_n$	$4\text{s} \leq t \leq 5\text{s}$	$1.3U_n \pm 1\% U_n$	$4\text{s} \leq t \leq 5\text{s}$
Trip value [V]	201.3		520.6	
	200.6		521.0	
	200.3		520.7	
Trip time [s]	$U_n \text{ To } U_{\text{th-low}} - 5\%U_n$	4.586	$U_n \text{ To } U_{\text{th-high}} + 5\%U_n$	4.581
		4.574		4.575
		4.574		4.560

Scope pictures of the disconnection time

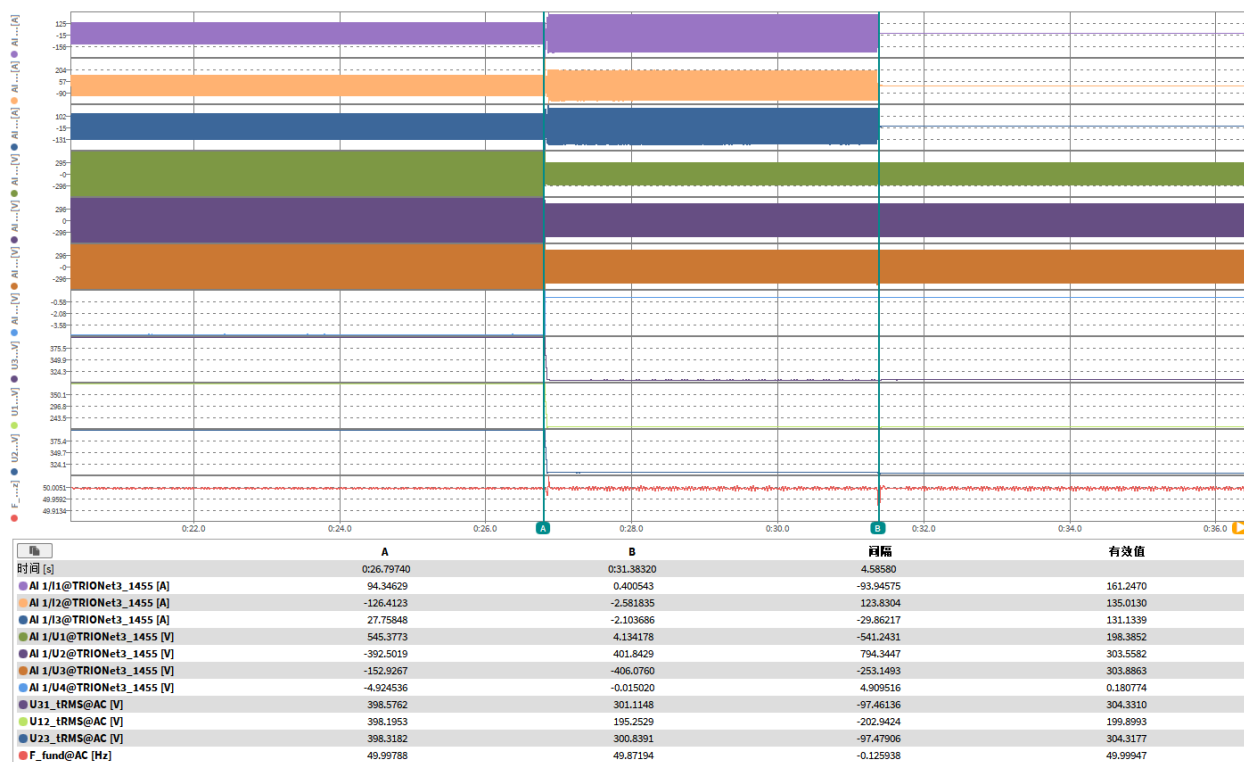
Under -voltage – Stage 1



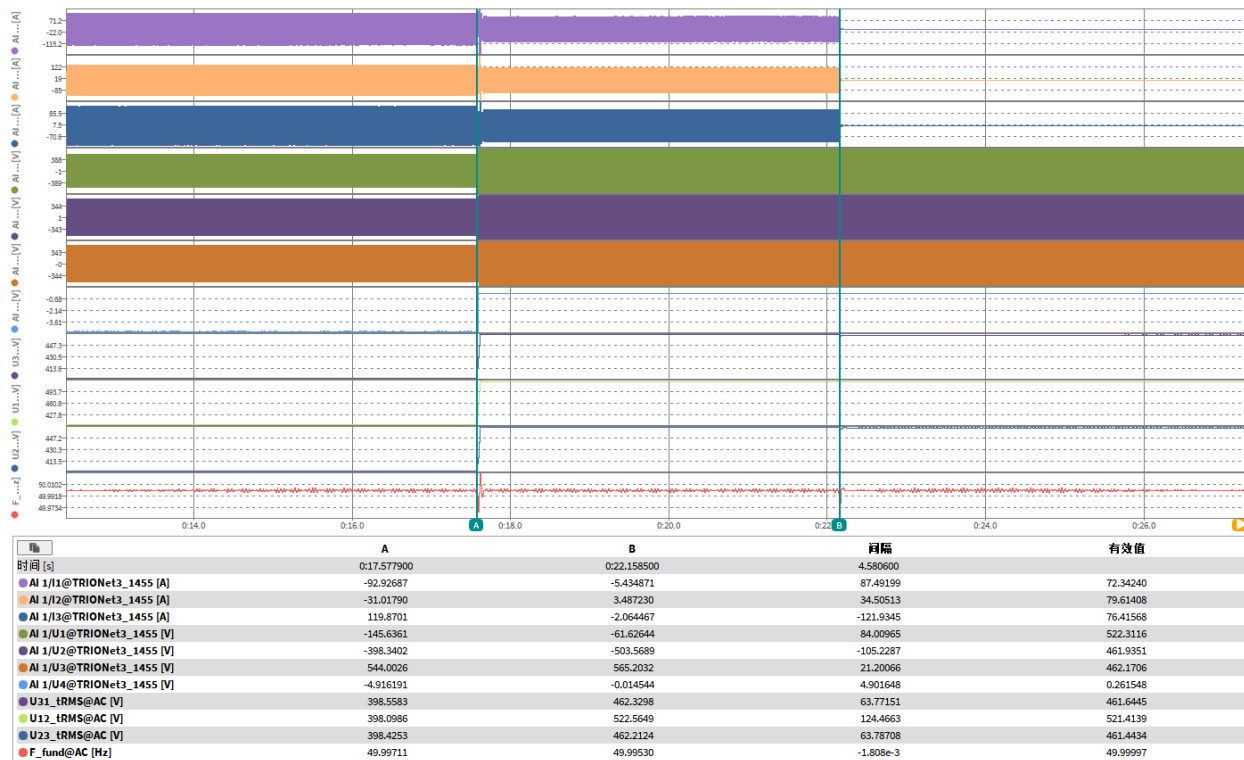
Over-voltage – Stage 1



Under -voltage – Stage 2



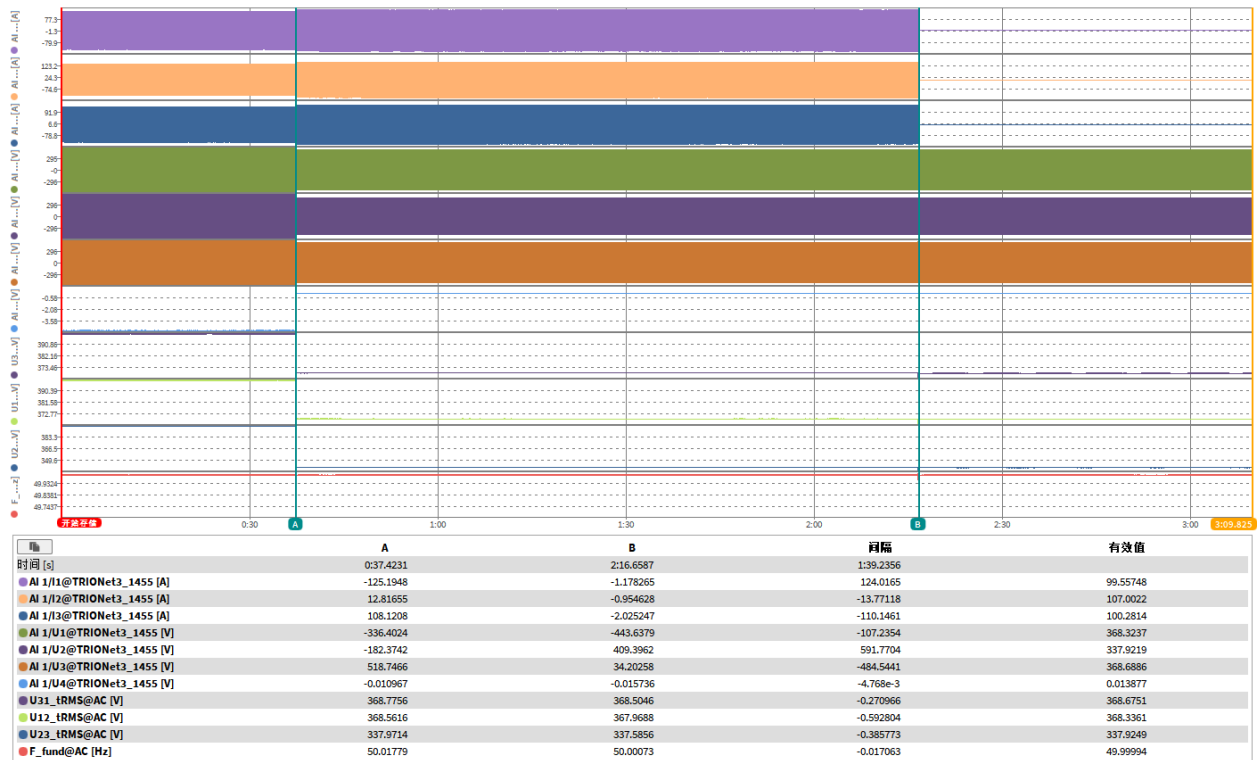
Over-voltage – Stage 2



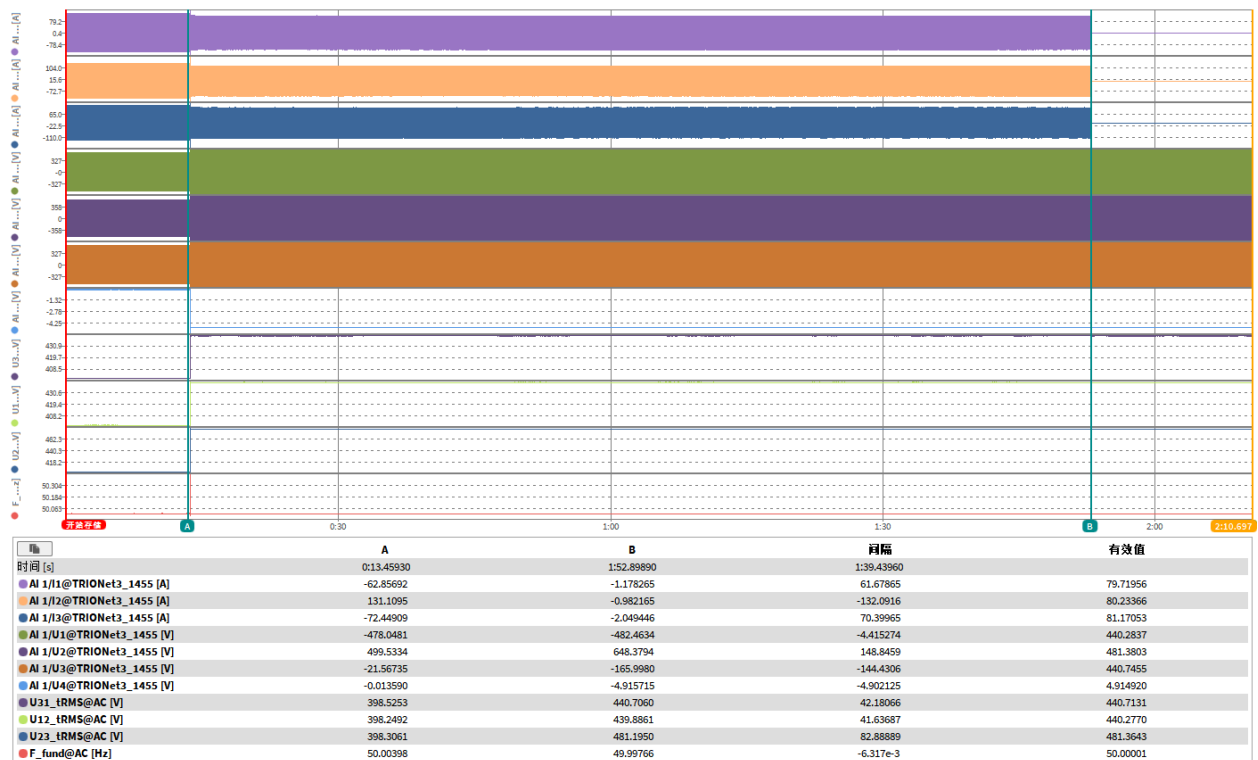
5.8 Interface protection 5.8.5.1 Voltage protections (Three-phase type, L2-L3)				P
Test conditions	Output power: $\geq 50\%P_n$, Frequency: $50 \pm 0.2\text{Hz}$			
Stage 1	Under-voltage		Over-voltage	
Parameter	Voltage	Time	Voltage	Time
Limit	$0.85U_n \pm 1\% U_n$	$99\text{s} \leq t \leq 100\text{s}$	$1.2U_n \pm 1\% U_n$	$99\text{s} \leq t \leq 100\text{s}$
Trip value [V]	341.2		481.1	
	341.1		481.2	
	340.8		480.6	
Trip time [s]	$U_n \text{ To } U_{\text{th-low}} - 5\%U_n$	99.233	$U_n \text{ To } U_{\text{th-high}} + 5\%U_n$	99.440
		99.236		99.334
		99.129		99.368
Stage 2	Under-voltage		Over-voltage	
Parameter	Voltage	Time	Voltage	Time
Limit	$0.5U_n \pm 1\% U_n$	$4\text{s} \leq t \leq 5\text{s}$	$1.3U_n \pm 1\% U_n$	$4\text{s} \leq t \leq 5\text{s}$
Trip value [V]	201.1		521.6	
	200.9		521.3	
	200.9		521.4	
Trip time [s]	$U_n \text{ To } U_{\text{th-low}} - 5\%U_n$	4.554	$U_n \text{ To } U_{\text{th-high}} + 5\%U_n$	4.551
		4.567		4.543
		4.567		4.568

Scope pictures of the disconnection time

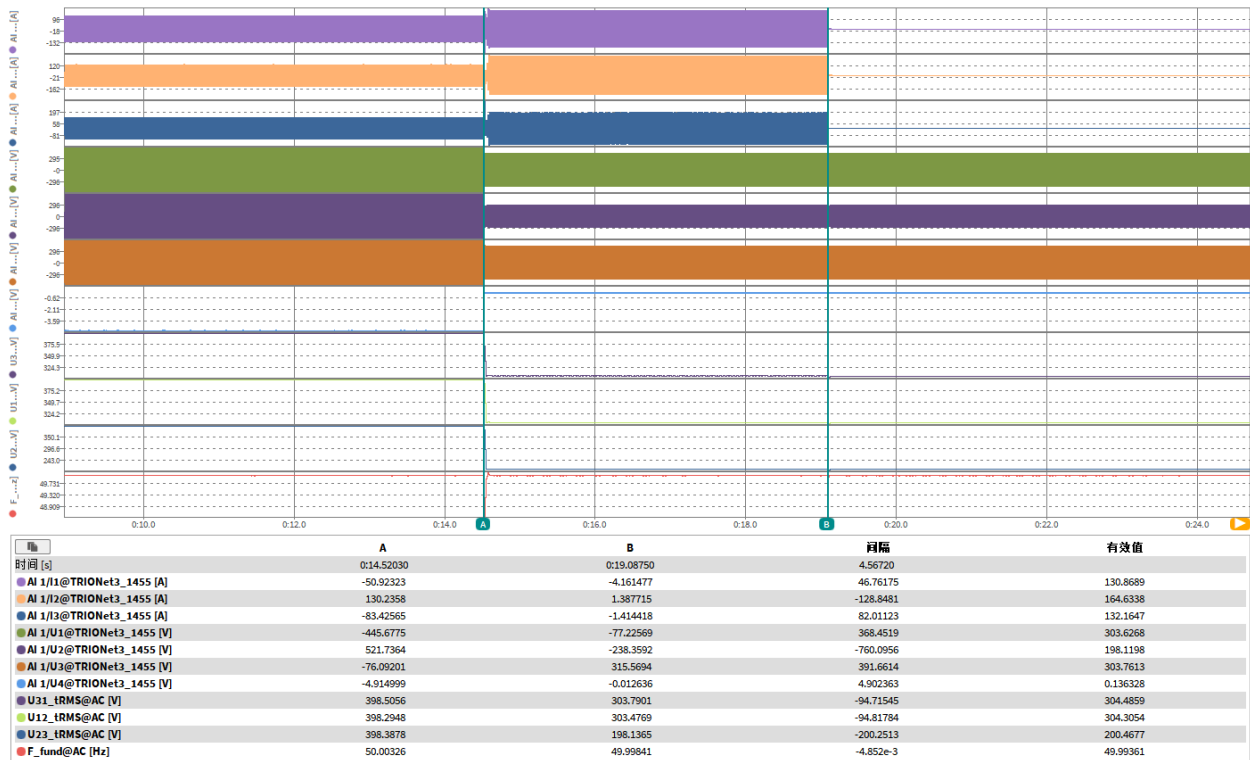
Under -voltage – Stage 1



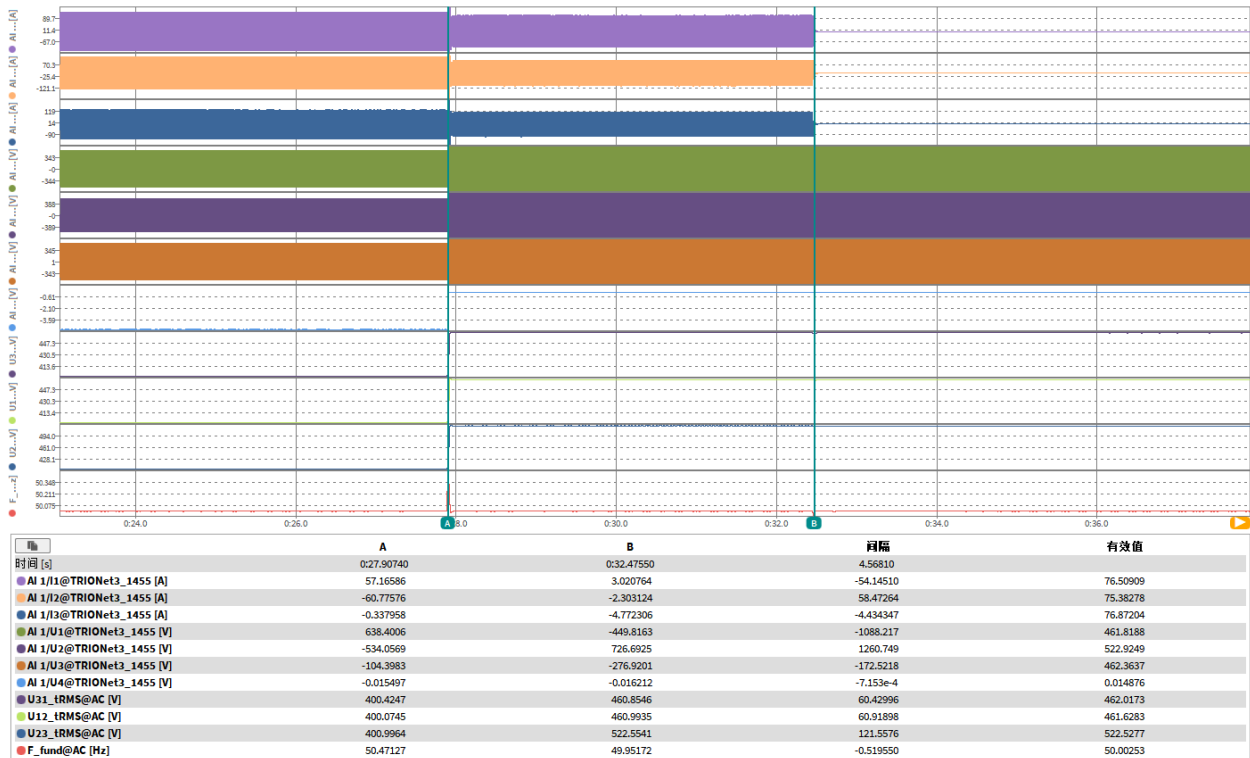
Over-voltage – Stage 1



Under -voltage – Stage 2



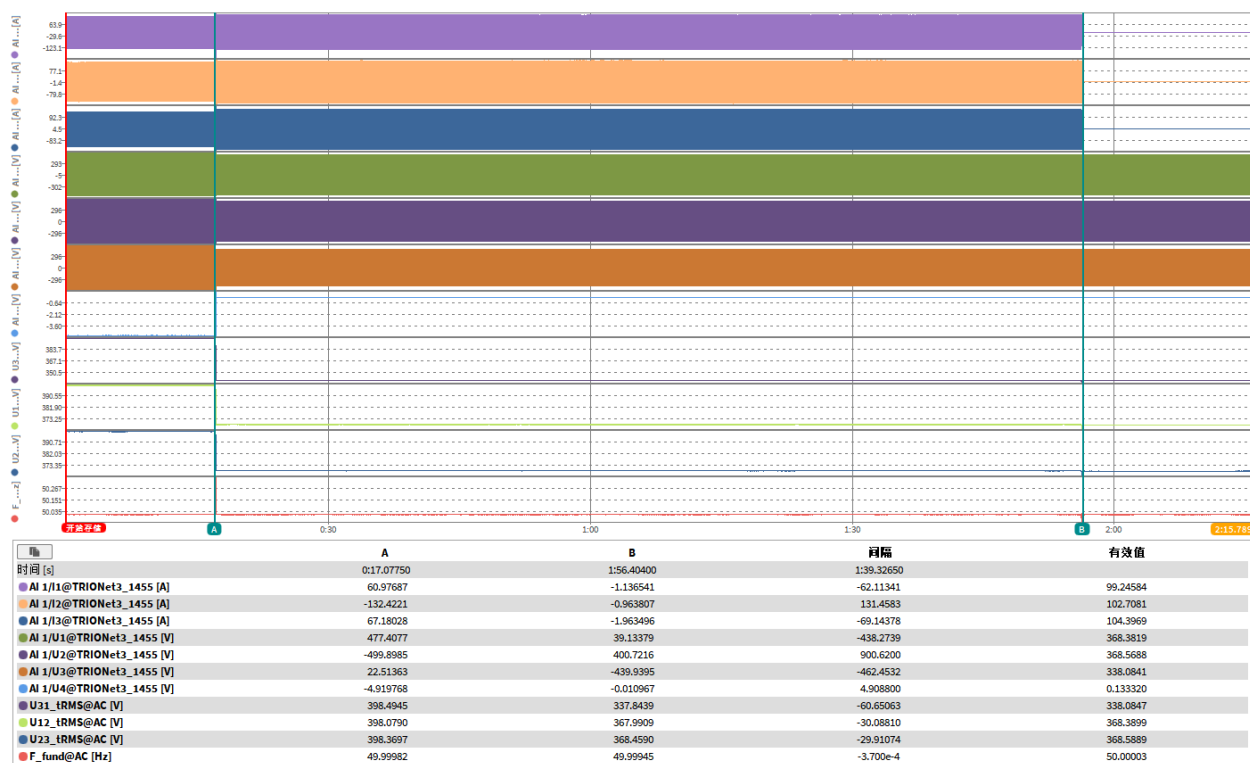
Over-voltage – Stage 2



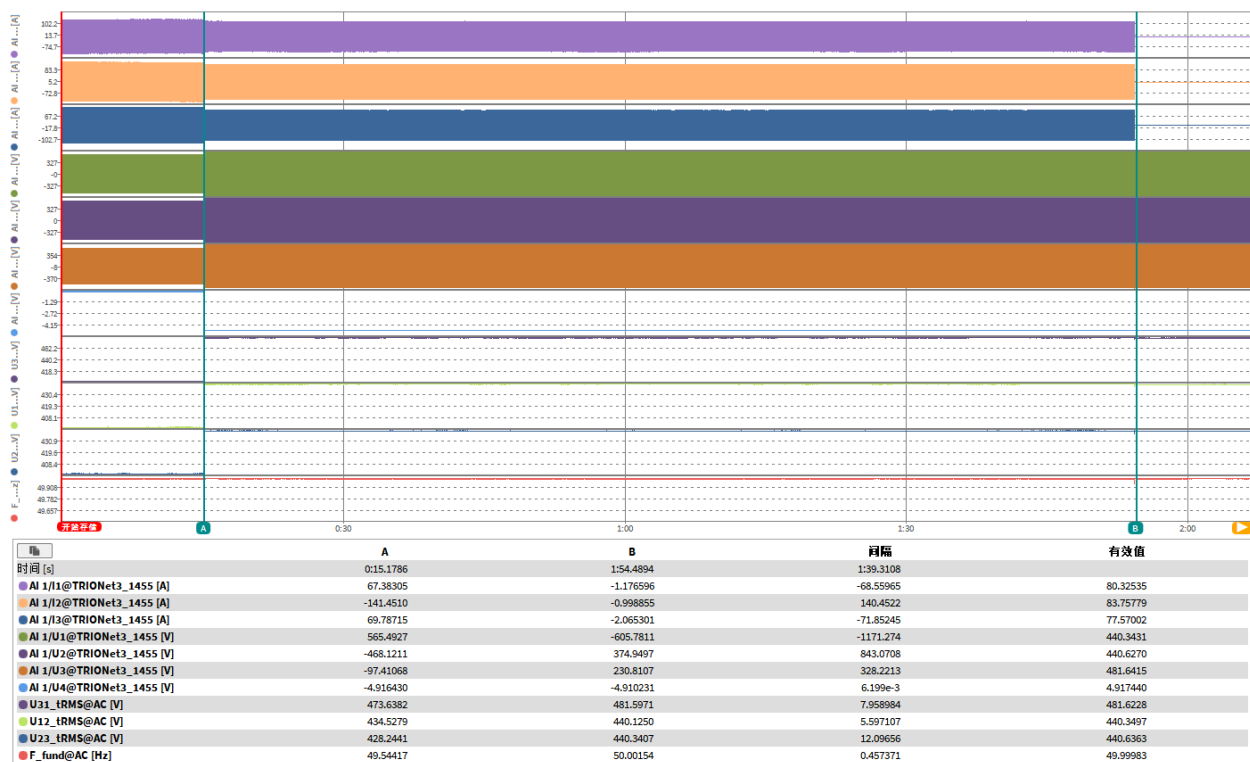
5.8 Interface protection 5.8.5.1 Voltage protections (Three-phase type, L3-L1)				P
Test conditions	Output power: $\geq 50\%P_n$, Frequency: $50 \pm 0.2\text{Hz}$			
Stage 1	Under-voltage		Over-voltage	
Parameter	Voltage	Time	Voltage	Time
Limit	$0.85U_n \pm 1\% U_n$	$99\text{s} \leq t \leq 100\text{s}$	$1.2U_n \pm 1\% U_n$	$99\text{s} \leq t \leq 100\text{s}$
Trip value [V]	340.3		479.8	
	340.1		479.7	
	339.9		480.0	
Trip time [s]	$U_n \text{ To } U_{th-low} - 5\%U_n$	99.135	$U_n \text{ To } U_{th-high} + 5\%U_n$	99.234
		99.327		99.311
		99.094		99.109
Stage 2	Under-voltage		Over-voltage	
Parameter	Voltage	Time	Voltage	Time
Limit	$0.5U_n \pm 1\% U_n$	$4\text{s} \leq t \leq 5\text{s}$	$1.3U_n \pm 1\% U_n$	$4\text{s} \leq t \leq 5\text{s}$
Trip value [V]	200.1		520.3	
	200.8		520.1	
	200.2		520.4	
Trip time [s]	$U_n \text{ To } U_{th-low} - 5\%U_n$	4.556	$U_n \text{ To } U_{th-high} + 5\%U_n$	4.566
		4.572		4.558
		4.569		4.570

Scope pictures of the disconnection time

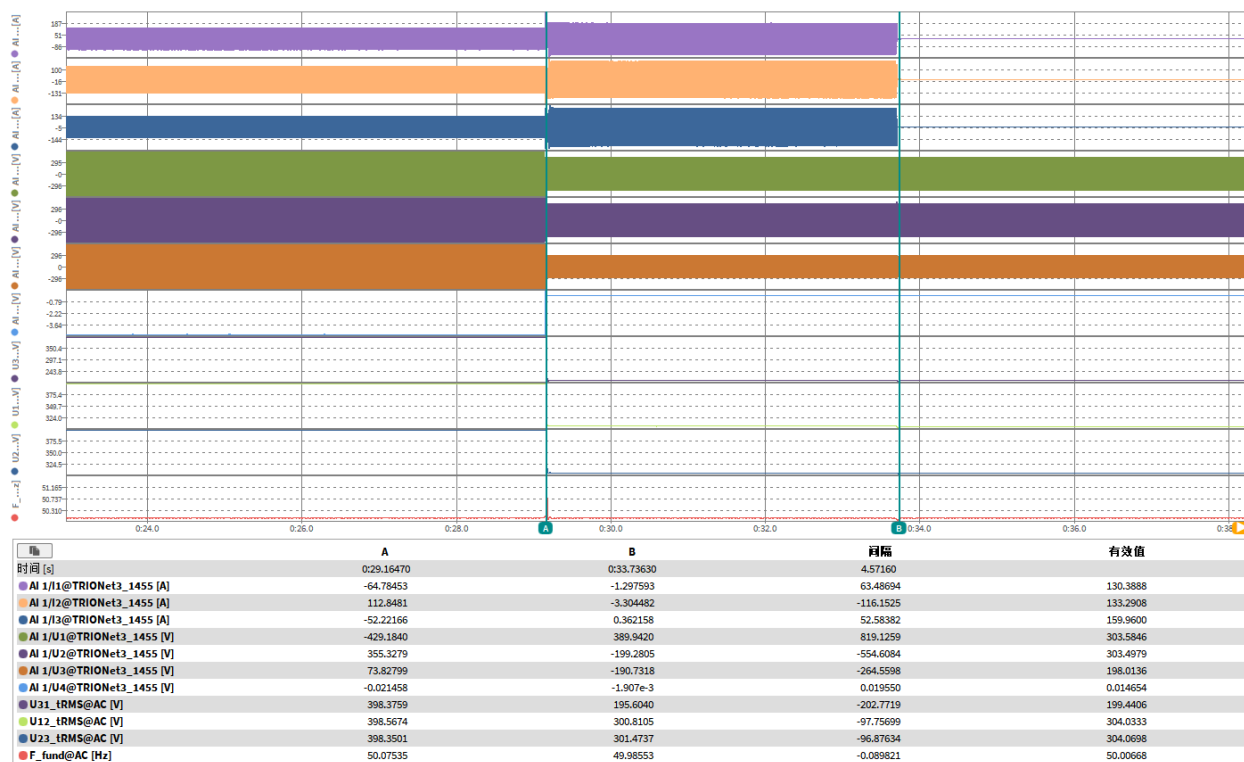
Under -voltage – Stage 1



Over-voltage – Stage 1



Under -voltage – Stage 2



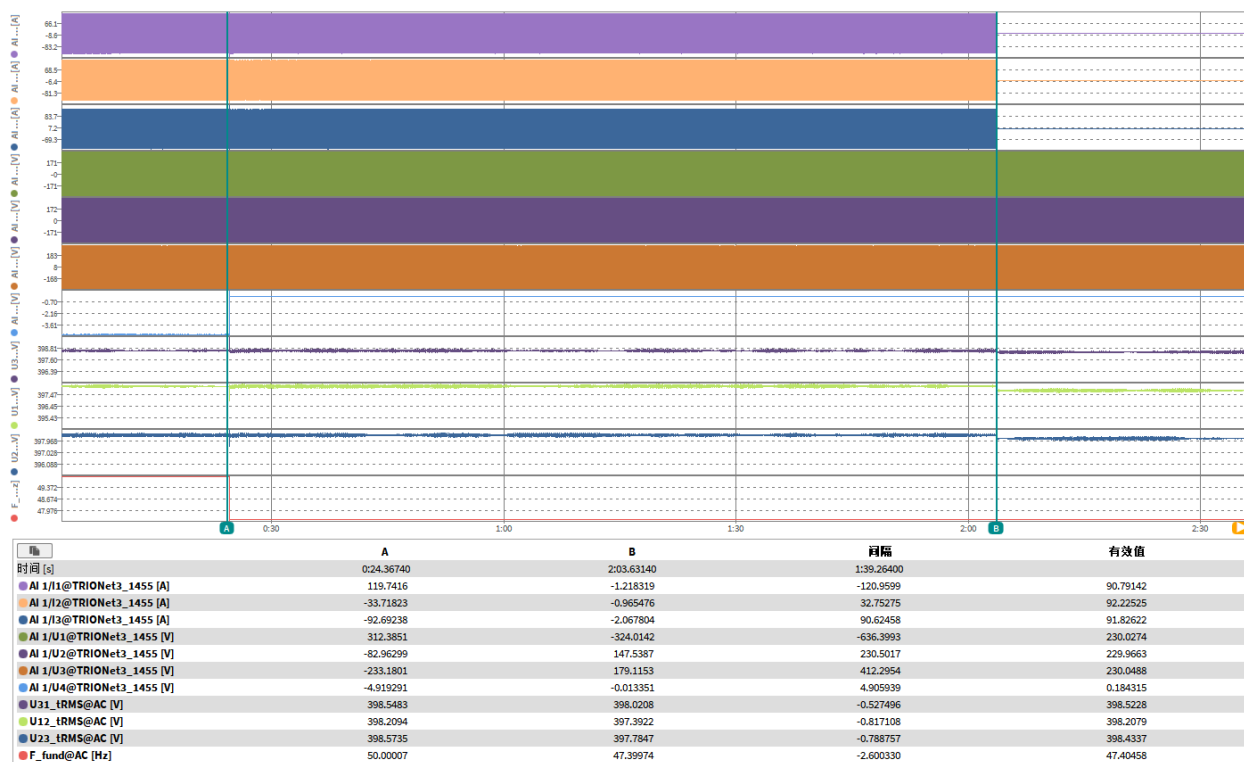
Over-voltage – Stage 2



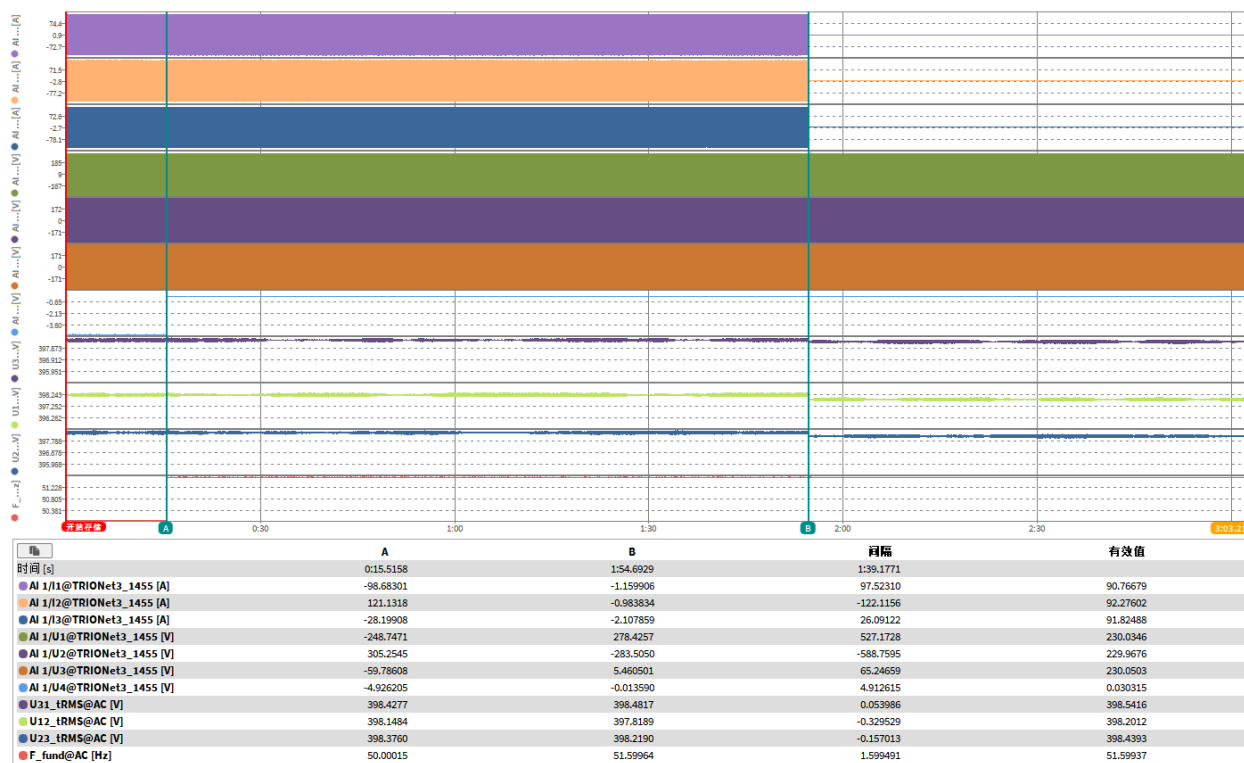
5.8 Interface protection				P
5.8.5.3 Frequency protections				
Test conditions	Output power: $\geq 50\%P_n$ $U_n = 1.0U_n$			
Stage 1	Under-frequency		Over-frequency	
Parameter	Frequency	Time	Frequency	Time
Limit	47.50 Hz ± 0.05 Hz	99s $\leq t \leq 100$ s	51.50 Hz ± 0.05 Hz	99s $\leq t \leq 100$ s
Trip value [Hz]	47.51		51.50	
	47.50		51.50	
	47.50		51.50	
Trip time [s]	50.0 Hz To 47.4 Hz	99.084	50.0 Hz To 51.6 Hz	99.086
		99.048		99.146
		99.264		99.177
Stage 2	Under-frequency		Over-frequency	
Parameter	Frequency	Time	Frequency	Time
Limit	47.00 Hz ± 0.05 Hz	4s $\leq t \leq 5$ s	52.00 Hz ± 0.05 Hz	4s $\leq t \leq 5$ s
Trip value [Hz]	47.01		52.01	
	47.00		52.00	
	47.00		52.00	
Trip time [s]	50.0 Hz To 46.9 Hz	4.623	50.0 Hz To 52.1 Hz	4.564
		4.599		4.580
		4.604		4.558

Scope pictures of the disconnection time

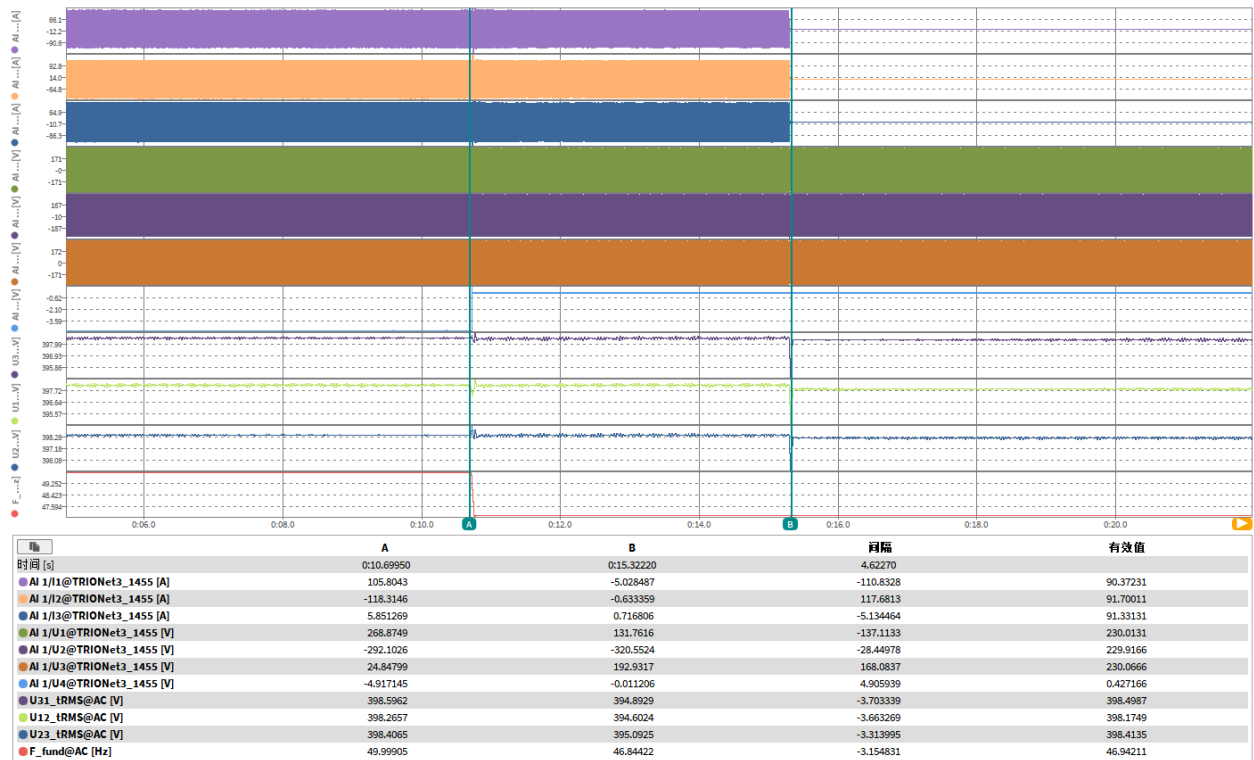
Under-frequency stage1



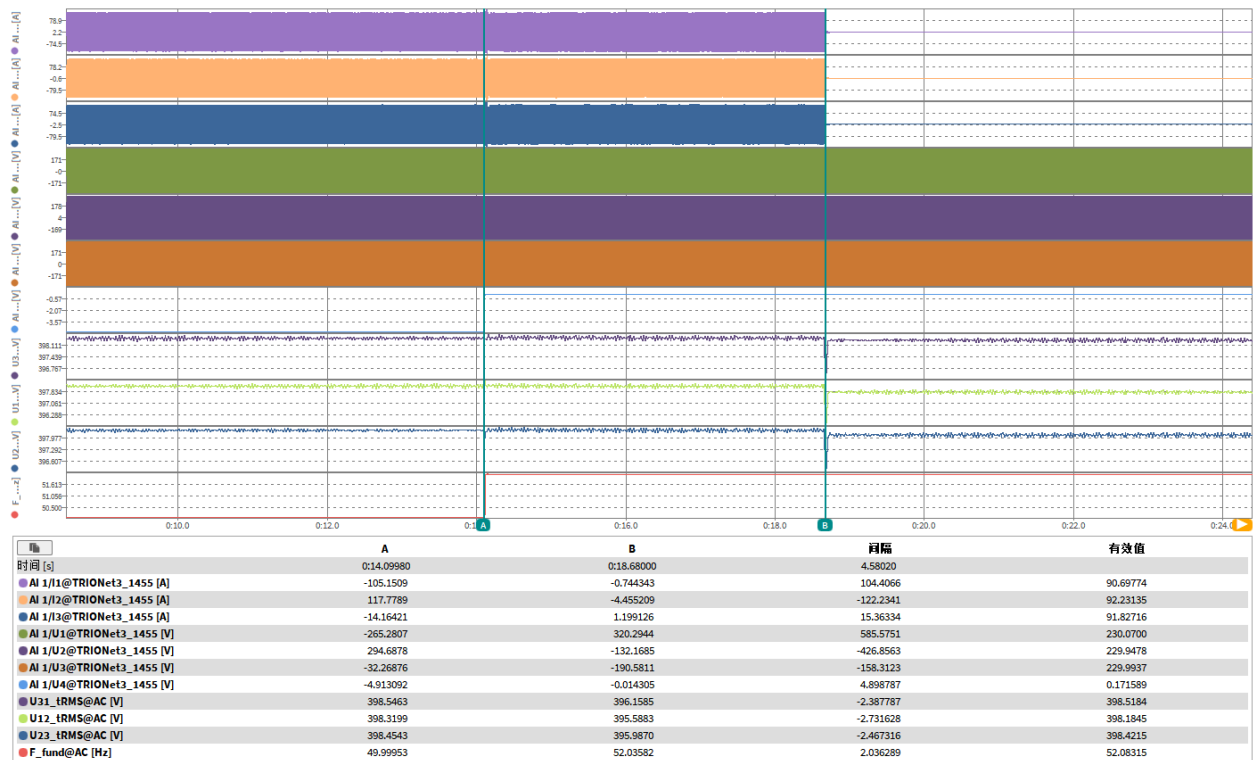
Over-frequency stage1



Under-frequency stage2



Over-frequency stage2



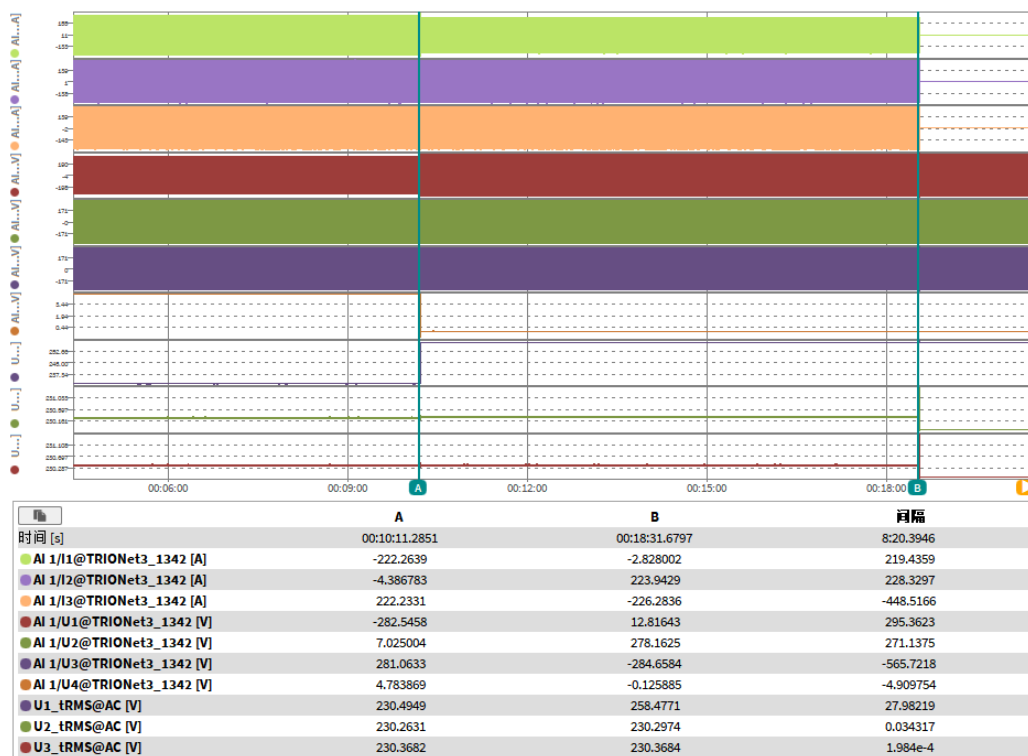
5.8 Interface protection							P
5.8.3.2.4 Reset time and reset ratio							
Test conditions	Output power: $\geq 50\%P_n$, Frequency: $50 \pm 0.2\text{Hz}$						
Stage 1	Under-voltage			Over-voltage			
Parameter	Voltage /Time			Voltage /Time			
Setting value	$0.85U_n / 99000\text{ms}$			$1.2U_n / 99000\text{ms}$			
1.00 $U_n \rightarrow 0.82U_n$ (98940ms) $\rightarrow 0.87U_n$ (99200ms) $\rightarrow 1.00U_n$	Phase	Reset time	Result	1.00 $U_n \rightarrow 1.23U_n$ (98940ms) $\rightarrow 1.18U_n$ (99200ms) $\rightarrow 1.00U_n$	Phase	Reset time	Result
	L1-N, L2-N, L3-N	$\leq 50\text{ms}$	No trip		L1-N, L2-N, L3-N	$\leq 50\text{ms}$	No trip
	L1-N	$\leq 50\text{ms}$	No trip		L1-N	$\leq 50\text{ms}$	No trip
	L2-N	$\leq 50\text{ms}$	No trip		L2-N	$\leq 50\text{ms}$	No trip
	L3-N	$\leq 50\text{ms}$	No trip		L3-N	$\leq 50\text{ms}$	No trip
	L1-L2	$\leq 50\text{ms}$	No trip		L1-L2	$\leq 50\text{ms}$	No trip
	L2-L3	$\leq 50\text{ms}$	No trip		L2-L3	$\leq 50\text{ms}$	No trip
	L3-L1	$\leq 50\text{ms}$	No trip		L3-L1	$\leq 50\text{ms}$	No trip
Stage 2	Under-voltage			Over-voltage			
Parameter	Voltage /Time			Voltage /Time			
Setting value	$0.5U_n / 4000\text{ms}$			$1.3U_n / 4000\text{ms}$			
1.00 $U_n \rightarrow 0.47U_n$ (3940ms) $\rightarrow 0.52U_n$ (4200ms) $\rightarrow 1.00U_n$	Phase	Reset time	Result	1.00 $U_n \rightarrow 1.33U_n$ (3940ms) $\rightarrow 1.28U_n$ (4200ms) $\rightarrow 1.00U_n$	Phase	Reset time	Result
	L1-N, L2-N, L3-N	$\leq 50\text{ms}$	No trip		L1-N, L2-N, L3-N	$\leq 50\text{ms}$	No trip
	L1-N	$\leq 50\text{ms}$	No trip		L1-N	$\leq 50\text{ms}$	No trip
	L2-N	$\leq 50\text{ms}$	No trip		L2-N	$\leq 50\text{ms}$	No trip
	L3-N	$\leq 50\text{ms}$	No trip		L3-N	$\leq 50\text{ms}$	No trip
	L1-L2	$\leq 50\text{ms}$	No trip		L1-L2	$\leq 50\text{ms}$	No trip
	L2-L3	$\leq 50\text{ms}$	No trip		L2-L3	$\leq 50\text{ms}$	No trip
	L3-L1	$\leq 50\text{ms}$	No trip		L3-L1	$\leq 50\text{ms}$	No trip
Stage 1	Under-frequency			Over-frequency			
Parameter	Frequency /Time			Frequency /Time			
Setting value	$47.5\text{Hz} / 99000\text{ms}$			$51.5\text{Hz} / 99000\text{ms}$			

50Hz→47.2Hz(98940ms)→47.7Hz(99200ms)→50Hz	Reset time	Result	50Hz→51.8Hz(98940ms)→51.3Hz(99200ms)→50Hz	Reset time	Result
	≤50ms	No trip		≤50ms	No trip
Stage 2	Under-frequency		Over-frequency		
Parameter	Frequency /Time		Frequency /Time		
Setting value	47.0Hz / 4000ms		52.0Hz / 4000ms		
50Hz→46.7Hz(3940ms)→47.2Hz(4200ms)→50Hz	Reset time	Result	50Hz→52.3Hz(3940ms)→51.8Hz(4200ms)→50Hz	Reset time	Result
	≤50ms	No trip		≤50ms	No trip

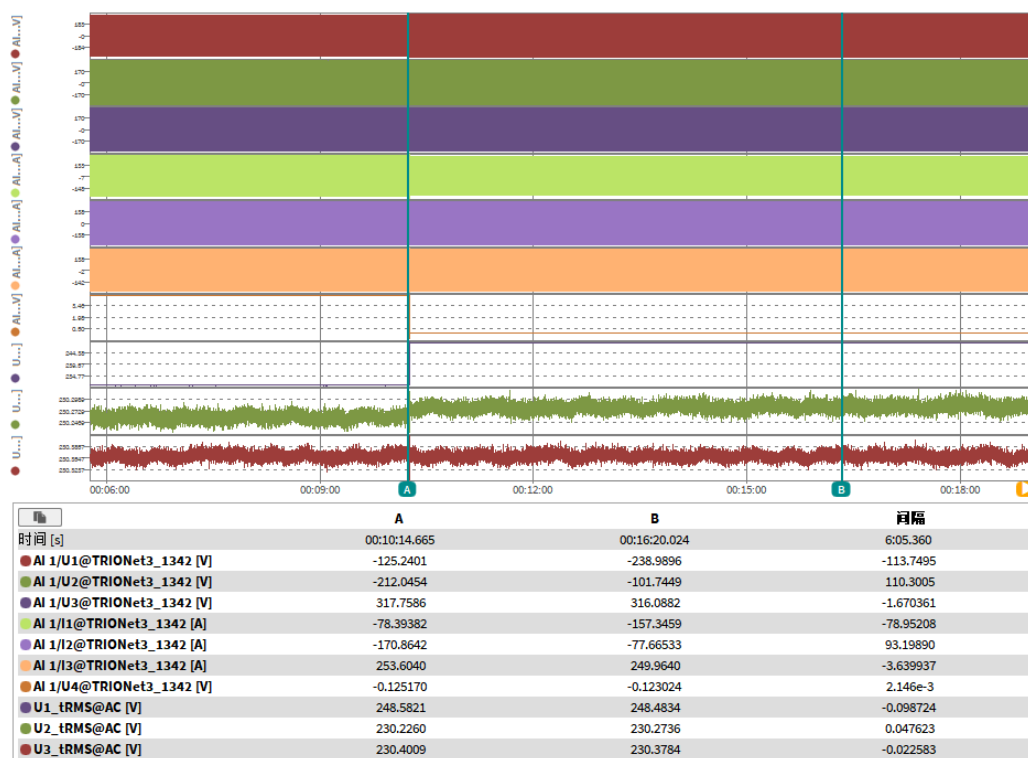
5.8.5.2 Overvoltage 10 min mean protection			P
Setting values of the protection:	Trip value Setting [V]	253	
	Setting T _{disconnection trip value} [s]	600	
	Setting T _{disconnection} [ms]	200	
Test:			
		Disconnection time [s]	Limit [s]
a)	The voltage is set to 100% U _n and held for 600 s. Thereafter the voltage is set to 112% U _n . Disconnection must take place within 600 s.		
	Phase L1-N	500.395	450s-550s
	Phase L2-N	499.916	
	Phase L3-N	498.864	
b)	The voltage is set to U _n for 600 s and then to 108% U _n for 600 s. No disconnection should take place.		
	Phase L1-N	No disconnection	Disconnection should not take place.
	Phase L2-N	No disconnection	
	Phase L3-N	No disconnection	
c)	The voltage is set to 106 % U _n and held for 600 s. Thereafter the voltage is set to 114 % U _n . The disconnection should last for half the period as in Point a)*		
	Phase L1-N	300.020	The disconnection time should be about 50 % of the value measured in a). *(225s-375s)
	Phase L2-N	299.389	
	Phase L3-N	300.337	
Test:			
a) This test serves as proof of the measurement accuracy and the maximum set time.			
b) This test serves as proof of the measurement accuracy			
c) This test serves as proof of the correct formation of the 1-minute running mean value.			
Assessment criterion:			
The permitted tolerance between setting value and trip value of the voltage may not exceed ± 1 % of U _N .			
Limit values:			
Rise-in voltage protection 1.1 U _N after a max. 600 s, the switch off after 200 ms.			
Note:			
If only one integrated protection is used for the power generation systems, the value of the rise-in voltage protection of 1.1 U _N may not be changed.			
*If the setting value is set to 600 s, then the disconnection time can be in the range between 225 s and 375 s.			

L1-N

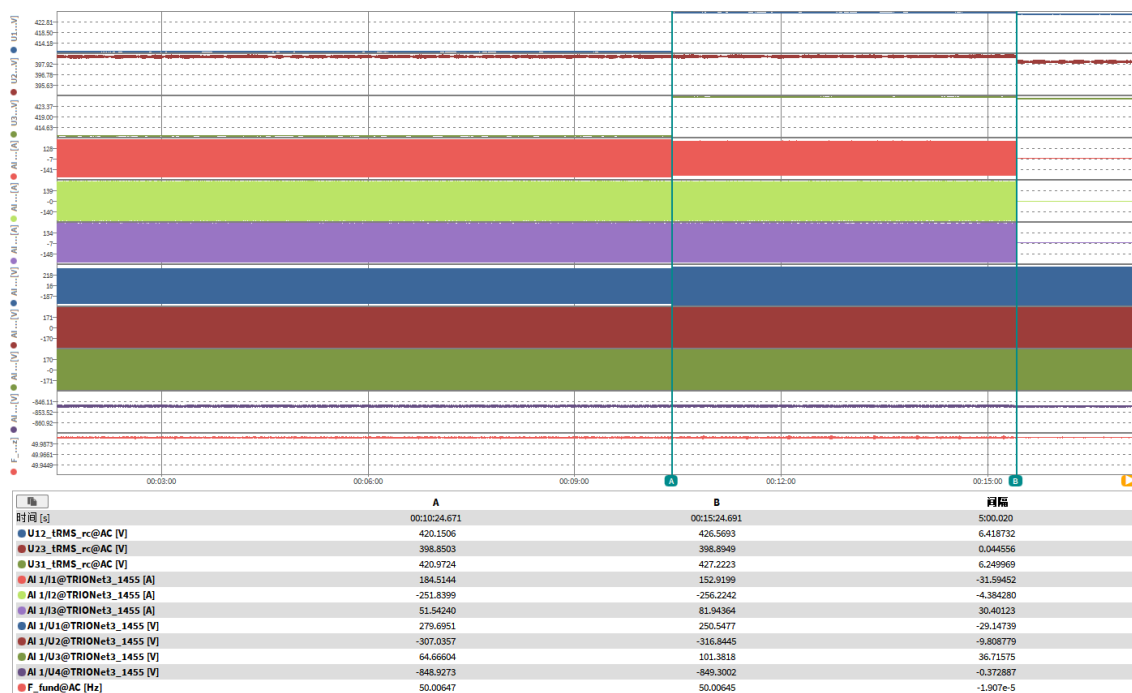
Voltage: a) 112%Un



Voltage: b) 108%Un

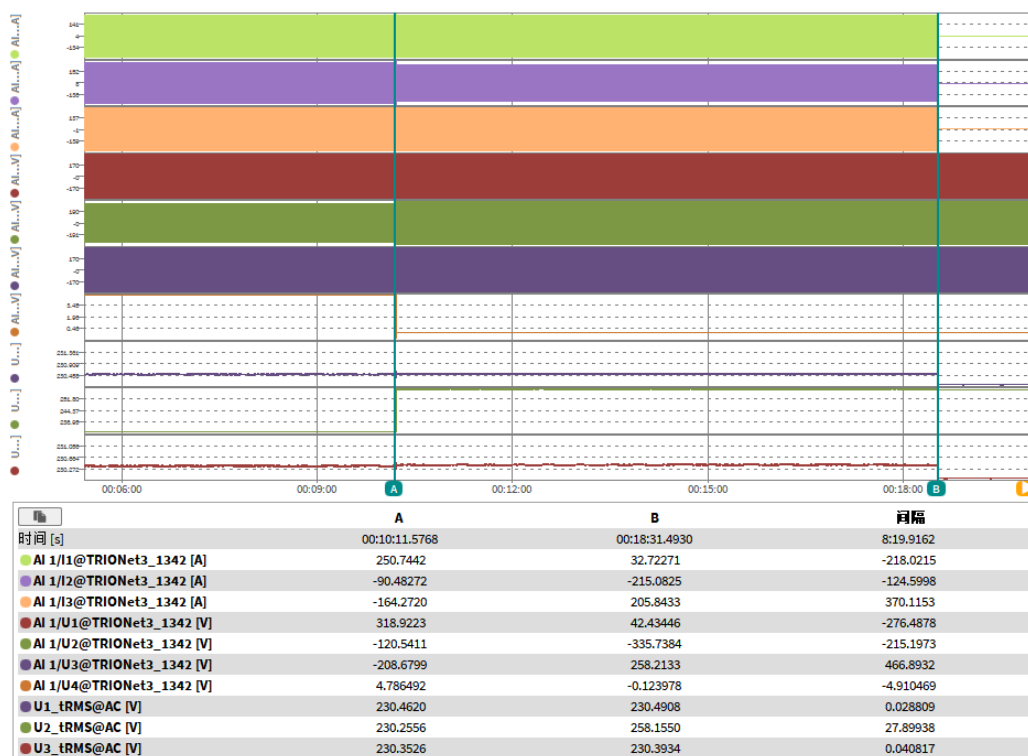


Voltage: c) 114%Un

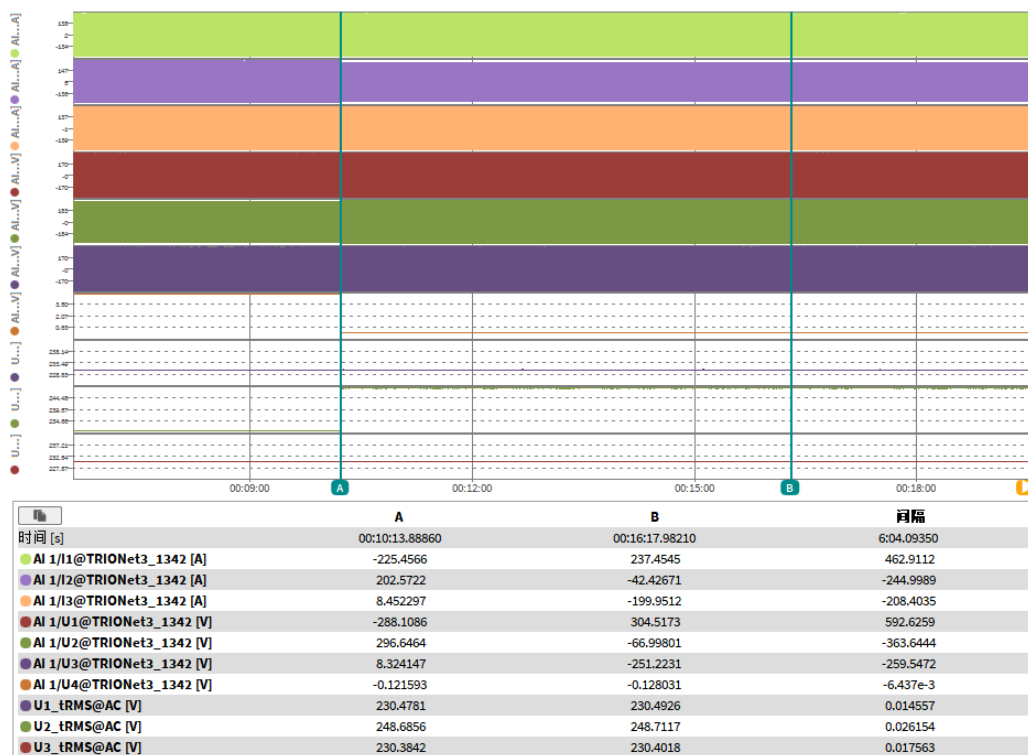


L2-N

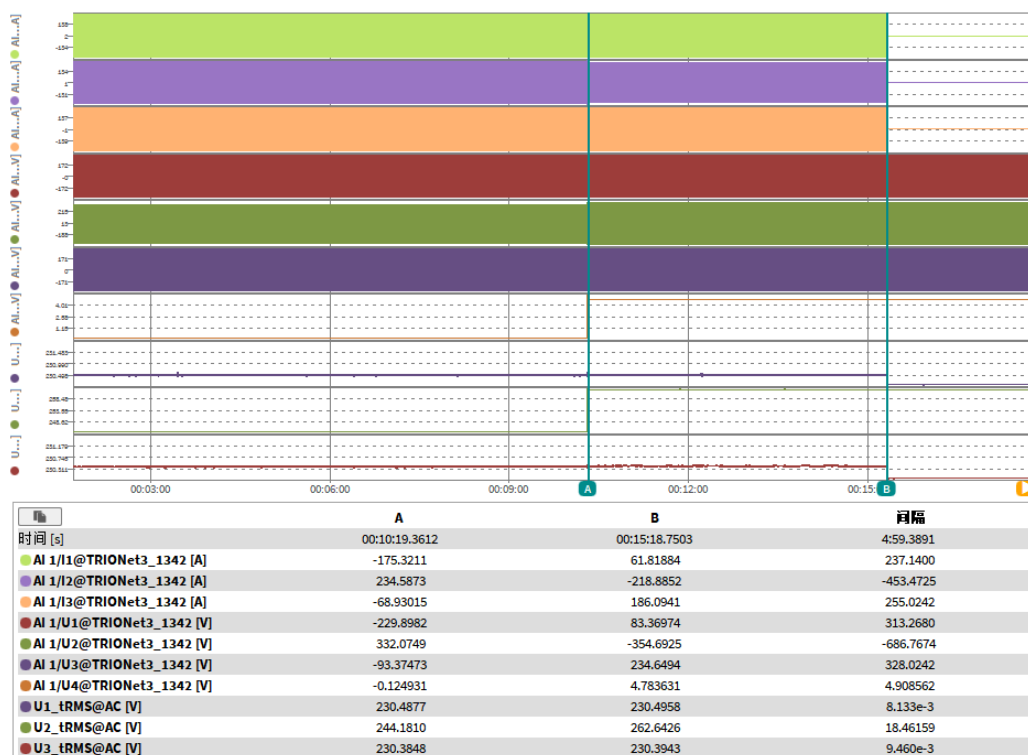
Voltage: a) 112%Un



Voltage: b) 108%Un

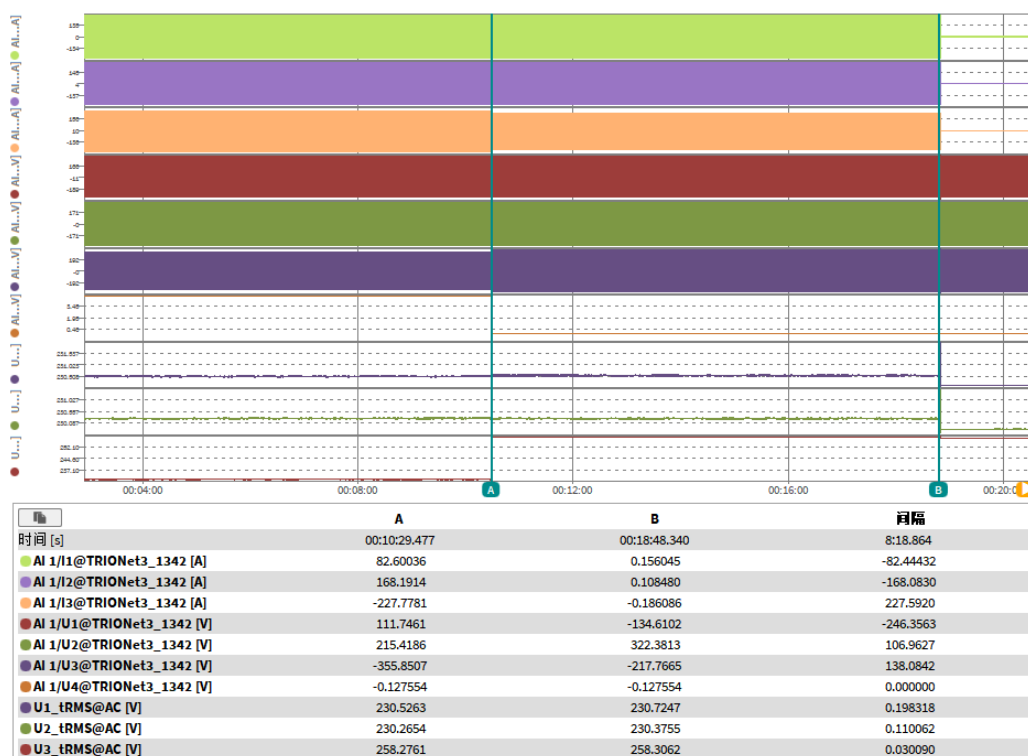


Voltage: c) 114%Un

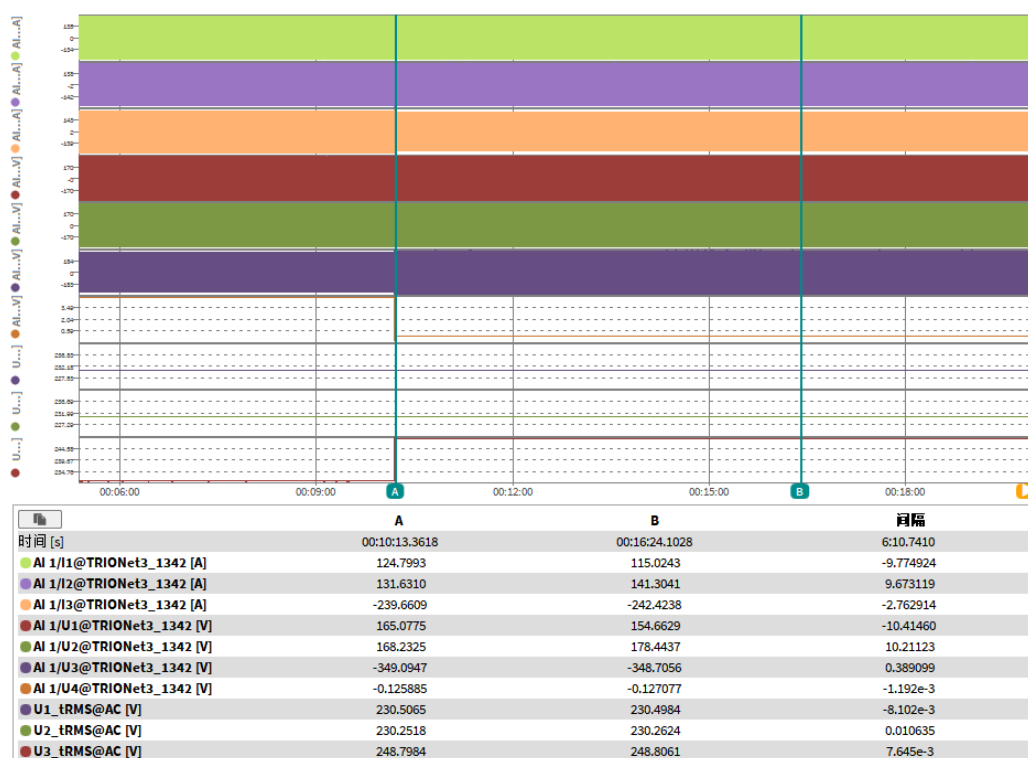


L3-N

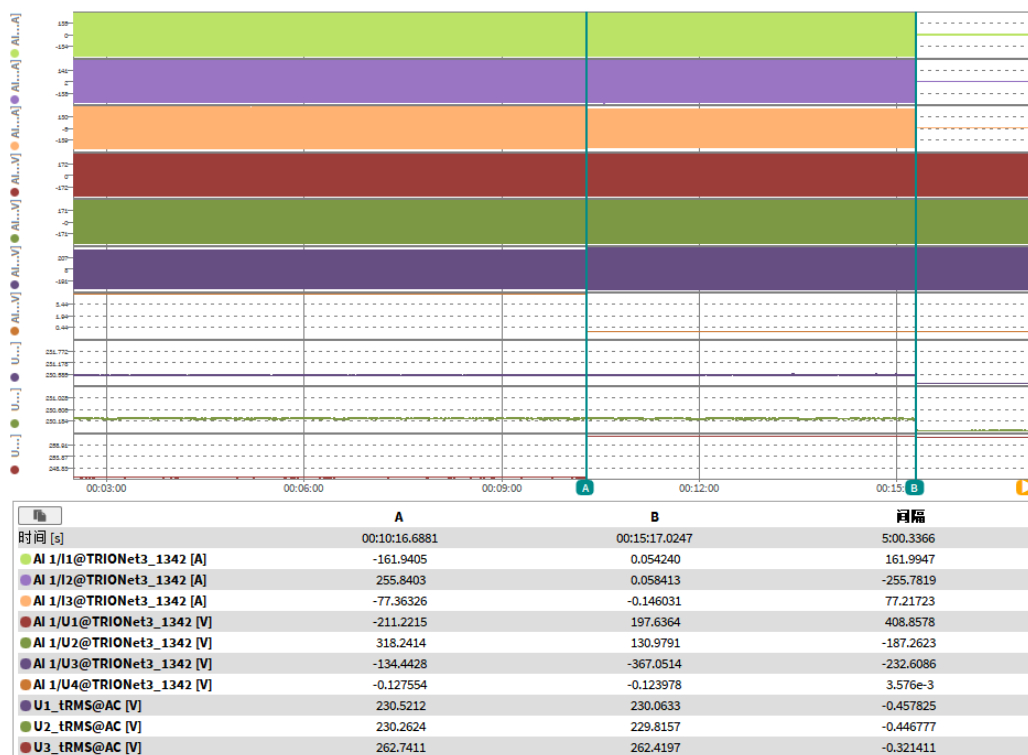
Voltage: a) 112%Un



Voltage: b) 108%Un



Voltage: c) 114%Un

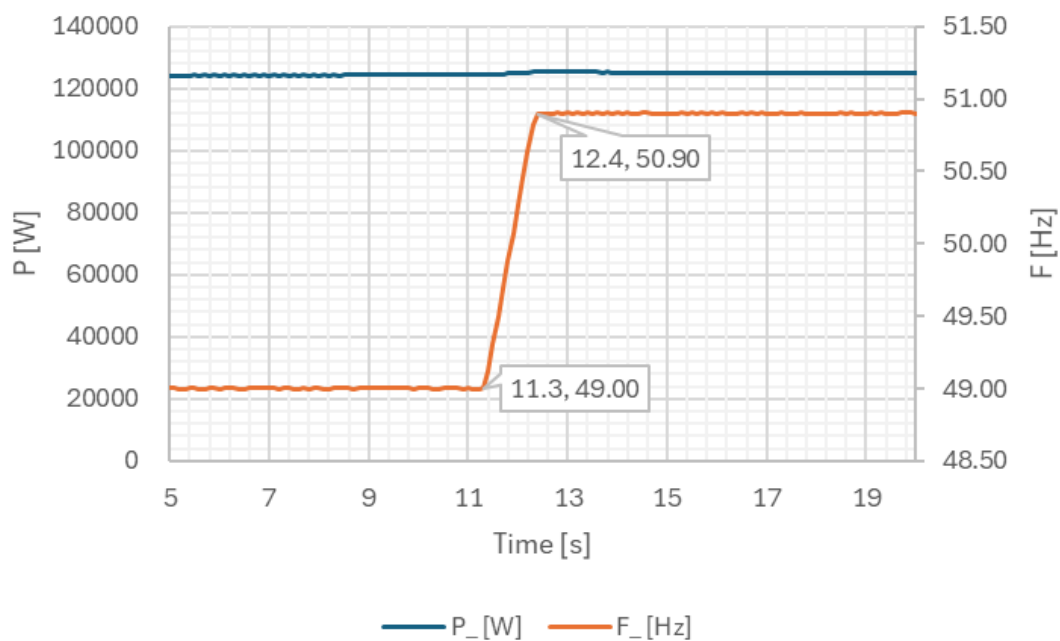


5.8.3.4 ROCOF protect				P		
5.8.3.4.1 Accuracy of the ROCOF setting values and operate time: verification method						
Setting values	Frequency change df/dt [Hz/s]			2.0Hz/s		
	Disconnecting time [s]			< 1s		
Test:						
P _{Emax} in %	100% ±5%					
Test condition	Start Frequency	Change	End Frequency	Connection		
Test 1: (*)	49.0 Hz	1.9Hz/s	50.9 Hz	Always connected		
Test 2: (*)	49.0 Hz	2.1Hz/s	51.1 Hz	Disconnected at 0.9s		
Test 3: (*)	51.0 Hz	1.9Hz/s	49.1 Hz	Always connected		
Test 4: (*)	51.0 Hz	2.1Hz/s	48.9 Hz	Disconnected at 0.9s		
Limit	disconnected time <1s					
Note:						
Table 30 — ROCOF protection test items, accuracy of the setting values and operate time measurements (1/2)						
Test item	Protection function	ROCOF direction	Test signal start frequency (Hz)	ROCOF of the test (Hz/s)	Test signal end frequency (Hz)	Step remain times
RVAL POS 1	ROCOF	Increase	$f_n - ROCOF \times 0,5 s$	$ROCOF = G_S + 2 \times A_r$	$f_n + ROCOF \times 0,5 s$	1 s
RVAL POS 2	ROCOF	Increase	$f_n - ROCOF \times 0,5 s$	$ROCOF = G_S - 2 \times A_r$	$f_n - ROCOF \times 0,5 s$	1 s
RVAL NEG 1	ROCOF	Decrease	$f_n + ROCOF \times 0,5 s$	$ROCOF = G_S + 2 \times A_r$	$f_n + ROCOF \times 0,5 s$	1 s
RVAI NEG 2	ROCOF	Decrease	$f_n + ROCOF \times 0,5 s$	$ROCOF = G_S - 2 \times A_r$	$f_n - ROCOF \times 0,5 s$	1 s

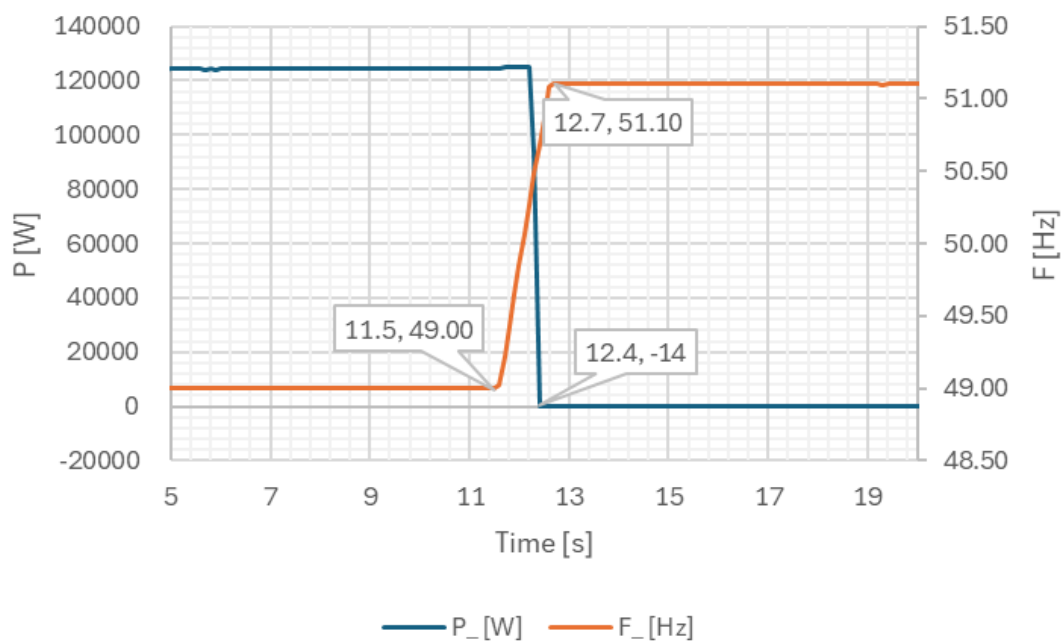
For each test the signal shall be applied and the ROCOF shall be increased or decreased, as appropriate, according to the criteria below (statements below apply for a positive ROCOF):

- to start the test, a signal which is at frequency $f = f_n \pm ROCOF_{test} \times (\Delta t_2/2)$, where Δt_2 is defined in the next step, is applied for Δt_1 , equal to at least 1 s and longer than the time delay setting plus 200 ms;
- then a ROCOF equal to the activation setting value minus twice its required accuracy, or if no requirement is made, twice the manufacturer stated accuracy, in direction of the setting value, is applied to the signal and shall be maintained constant for $\Delta t_2 = T_{op} - (T_{Res} + T_{ov})$ where T_{op} is the operate time, T_{Res} is the maximum required reset time and T_{ov} is the overshoot time, a tolerance margin equal to 10 ms
- if the reset value is:
within the band between the setting value and the setting values minus twice its required accuracy, then the signal is suddenly adjusted to the setting values minus twice its required accuracy (case shown in the Figure 42) for at least Δt_1
- below the setting value minus twice its accuracy, the signal is suddenly adjusted the reset value minus twice its accuracy for at least Δt_1 .

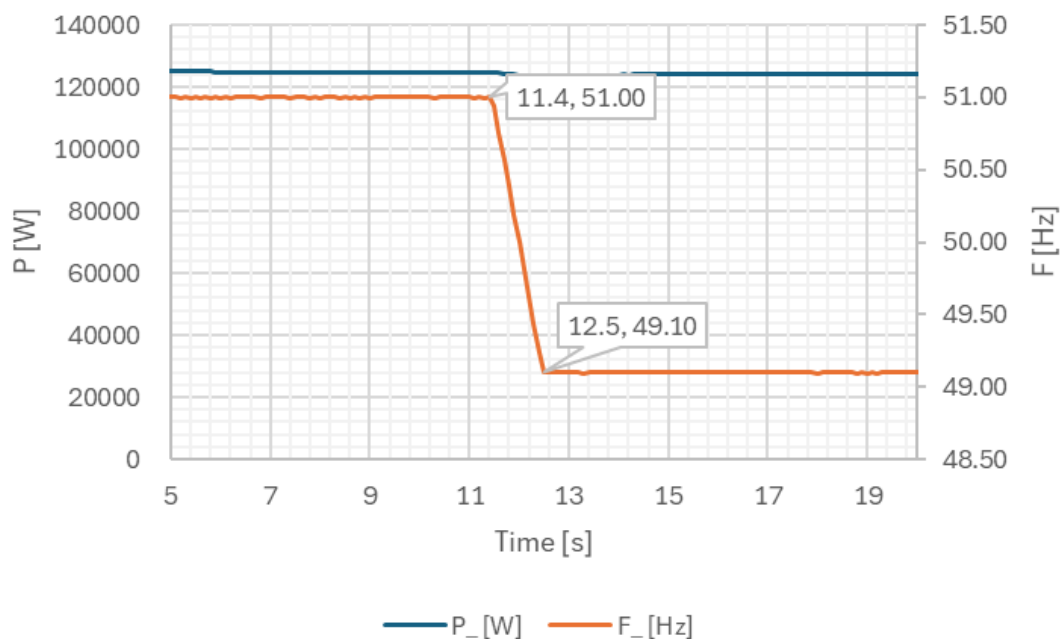
Test 1



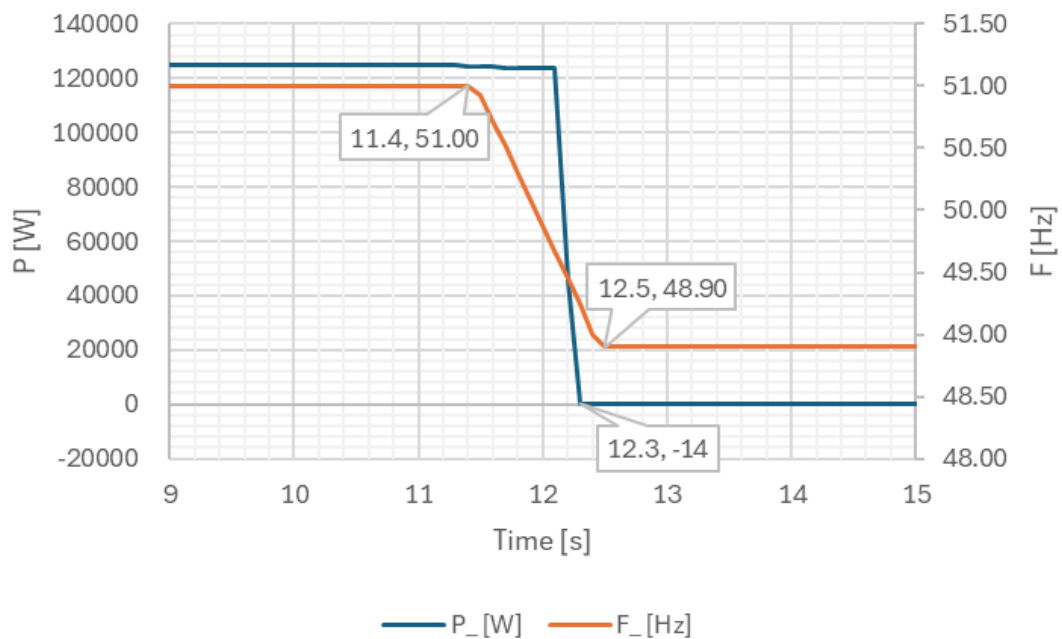
Test 2



Test 3



Test 4



5.8.3.4 ROCOF protect					P
5.8.3.4.2 Reset time and reset ratio					
Test conditions	Output power: >=50%Pn, Frequency: 50+/-0.2Hz				
	NOP POS		NOP NEG		
Parameter	ROCOF /Time		ROCOF /Time		
Setting value	2.0 Hz/s / 500ms		2.0 Hz/s / 500ms		
0Hz/s (49.56Hz hold at least 1s)→2.1 Hz/s (440ms)→1.9Hz/s(1000ms)	Reset time	Result	0Hz/s (50.44Hz hold at least 1s)→2.1 Hz/s (440ms)→-1.9Hz/s(1000ms)	Reset time	Result
	≤50ms	No trip		≤50ms	No trip
	≤50ms	No trip		≤50ms	No trip
	≤50ms	No trip		≤50ms	No trip

Assessment criterion:

Table 31 — ROCOF protection test items, reset time and reset ratio requirements (2/2) (under consideration)

Test-item		NOP POS	NOP NEG
Protection function		ROCOF	ROCOF
ROCOF direction		Increase ($G_S > 0$)	Decrease ($G_S < 0$)
Step 0	Test signal frequency (Hz)	$f_n - ROCOF_1 \left(\frac{T_{Op} - T_{Res} - 10\text{ ms}}{2} \right)$	$f_n + ROCOF_1 \left(\frac{T_{Op} - T_{Res} - 10\text{ ms}}{2} \right)$
	Remain time	1 s	1 s
Step 1	ROCOF (Hz/s)	$ROCOF_1 = G_S + 2 \times A_r$	$ROCOF_1 = G_S - 2 \times A_r$
	Remain time	$T_{Op} - T_{Res} - 10\text{ ms}$	$T_{Op} - T_{Res} - 10\text{ ms}$
	Test signal end frequency (Hz)	$f_{1-2} = f_n + ROC_1 \left(\frac{T_{Op} - T_{Res} - 10\text{ ms}}{2} \right)$	$f_{1-2} = f_n - ROCOF_1 \left(\frac{T_{Op} - T_{Res} - 10\text{ ms}}{2} \right)$
Step 2	ROCOF (Hz/s)	$ROCOF_2 = \min(G_S - 2 \times A_c, Res - 2 \times A_r)$	$ROCOF_2 = \max(G_S + 2 \times A_r, Res + 2 \times A_r)$
	Remain time	1 s	1 s
	Test signal end frequency (Hz)	$f_{1-2} + ROCOF_2 \times 1\text{ s}$	$f_{1-2} - ROCOF_2 \times 1\text{ s}$

5.8.6 Islanding detection

Test circuit and parameters

Parameter	Symbol	Units
EUT DC Input		
DC voltage	V_{DC}	V
DC Current	I_{DC}	A
DC Power	P_{DC}	W
EUT AC output		
AC voltage	V_{EUT}	V
AC current	I_{EUT}	A
Real power	P_{EUT}	W
Reactive power	Q_{EUT}	Var
Test Load		
Resistive load current	I_R	A
Inductive load current	I_L	A
Capacitive load current	I_C	A
AC (utility) power source		
Utility real power	P_{AC}	W
Utility reactive power	Q_{AC}	Var
Utility current	I_{AC}	A

Block diagram test circuit IEC 62116:2014

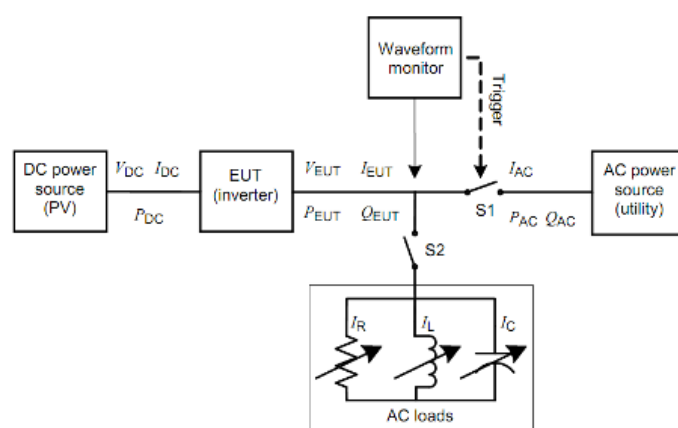


Figure 1 – Test circuit for islanding detection function in a power conditioner (inverter)

Load imbalance (real, reactive load) for test condition A (EUT output = 100%)									P
Test conditions			Frequency: 50+/-0.1Hz $U_N=230\pm2.3V_{ac}$ Distortion factor of chokes < 2% Quality = 1						
Disconnection limit			2s (IEC 62116)						
No.	$P_{EUT}^{1)}$ [% of EUT rating]	Reactive load [% of Q_L in 6.1.d] ¹⁾	$P_{AC}^{2) 5)}$ (% of nominal)	$Q_{AC}^{3) 5)}$ (% of nominal)	Run on Time [ms]	P_{EUT} Pre-phase [kW]	Q_f	V_{DC} [V]	Remarks ⁴⁾
1.	100	100	0	0	1850	125	1.000	740	Test A at BL
2.	100	100	0	-5	1192	125	0.975	740	Test A at IB
3.	100	100	0	+5	1166	125	1.025	740	Test A at IB
4.	100	100	-5	-5	1112	125	1.026	740	Test A at IB
5.	100	100	-5	0	1110	125	1.053	740	Test A at IB
6.	100	100	-5	+5	1174	125	1.079	740	Test A at IB
7.	100	100	+5	-5	1208	125	0.928	740	Test A at IB
8.	100	100	+5	0	964	125	0.952	740	Test A at IB
9.	100	100	+5	+5	1230	125	0.976	740	Test A at IB
10.	100	100	0	-10	950	125	0.949	740	Test A at IB
11.	100	100	0	+10	1160	125	1.049	740	Test A at IB
12.	100	100	-5	-10	1136	125	0.999	740	Test A at IB
13.	100	100	-5	+10	1170	125	1.104	740	Test A at IB
14.	100	100	+5	-10	1186	125	0.904	740	Test A at IB
15.	100	100	+5	+10	1180	125	0.999	740	Test A at IB
16.	100	100	-10	-10	830	125	1.054	740	Test A at IB
17.	100	100	-10	-5	1376	125	1.083	740	Test A at IB
18.	100	100	-10	0	1750	125	1.111	740	Test A at IB
19.	100	100	-10	+5	834	125	1.139	740	Test A at IB
20.	100	100	-10	+10	700	125	1.165	740	Test A at IB
21.	100	100	+10	-10	1172	125	0.862	740	Test A at IB
22.	100	100	+10	-5	988	125	0.886	740	Test A at IB

23.	100	100	+10	0	938	125	0.909	740	Test A at IB
24.	100	100	+10	+5	1380	125	0.932	740	Test A at IB
25.	100	100	+10	+10	1100	125	0.953	740	Test A at IB
Parameter at 0% per phase		L= 4.04 mH			R= 1.27 Ω		C= 2507.13 μ F		

Note:

RLC is adjusted to min. +/-1% of the inverter rated output power

¹⁾ P_{EUT} : EUT output power

²⁾ P_{AC} : Real power flow at S1 in Figure 1. Positive means power from EUT to utility. Nominal is the 0 % test condition value.

³⁾ Q_{AC} : Reactive power flow at S1 in Figure 1. Positive means power from EUT to utility. Nominal is the 0 % test condition value.

⁴⁾ Fundamental of I_{AC} when RLC is adjusted

⁵⁾ BL: Balance condition, IB: Imbalance condition.

Condition A:

EUT output power P_{EUT} = Maximum ⁶⁾

EUT input voltage ⁶⁾ = >75% of rated input voltage range

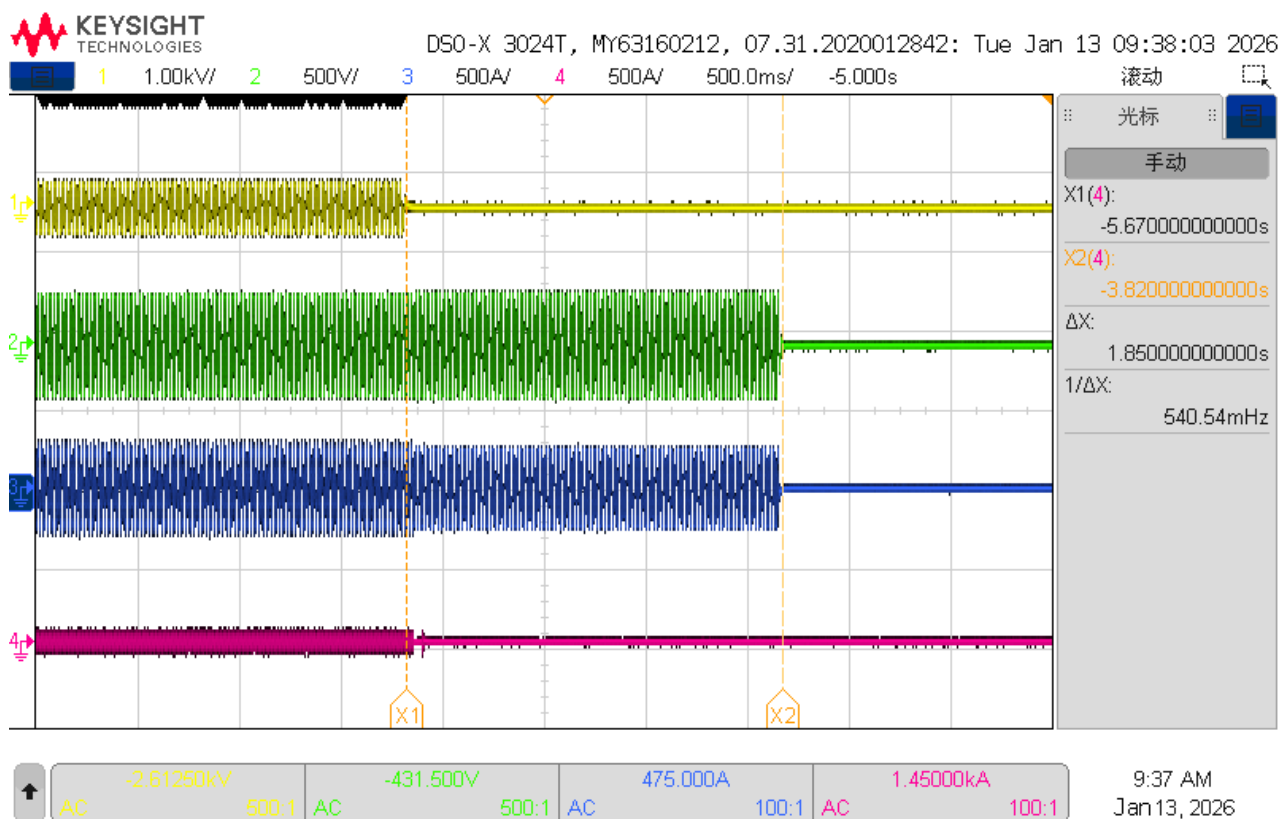
⁶⁾ Maximum EUT output power condition should be achieved using the maximum allowable input power.

Actual output power may exceed nominal rated output.

⁷⁾ Based on EUT rated input operating range. For example, If range is between X volts and Y volts, 75 % of range = $X + 0.75 \times (Y - X)$. Y shall not exceed $0.8 \times$ EUT maximum system voltage (i.e., maximum allowable array open circuit voltage). In any case, the EUT should not be operated outside of its allowable input voltage range.

Scope pictures of the disconnection time

Disconnection at No. 1

**Note:**

Yellow: Feedback current flows into the grid signal

Green: EUT output voltage

Blue: EUT output current

Pink: Grid input current

Load imbalance (reactive load) for test condition B (EUT output = 50 % – 66 %)									P
Test conditions			Frequency: 50+/-0.1Hz U _N =230±2.3Vac Distortion factor of chokes < 2% Quality = 1						
Disconnection limit			2s (IEC 62116)						
No.	P _{EUT} ¹⁾ [% of EUT rating]	Reactive load [% of Q _L in 6.1.d] ¹⁾	P _{AC} ^{2) 5)} (% of nominal)	Q _{AC} ^{3) 5)} (% of nominal)	Run on Time [ms]	P _{EUT} Pre- phase [kW]	Q _f	V _{DC} [V]	Remarks ⁴⁾
1.	66	66	0	-5	1038	82.5	0.975	720	Test B at IB
2.	66	66	0	-4	1240	82.5	0.980	720	Test B at IB
3.	66	66	0	-3	1052	82.5	0.985	720	Test B at IB
4.	66	66	0	-2	1470	82.5	0.990	720	Test B at IB
5.	66	66	0	-1	1462	82.5	0.995	720	Test B at IB
6.	66	66	0	0	1790	82.5	1.000	720	Test B at BL
7.	66	66	0	1	1528	82.5	1.005	720	Test B at IB
8.	66	66	0	2	1418	82.5	1.010	720	Test B at IB
9.	66	66	0	3	1320	82.5	1.015	720	Test B at IB
10.	66	66	0	4	1200	82.5	1.020	720	Test B at IB
11.	66	66	0	5	982	82.5	1.025	720	Test B at IB
Parameter at 0% per phase			L= 6.12 mH		R= 1.92 Ω		C= 1654.73 μF		
Note: RLC is adjusted to min. +/-1% of the inverter rated output power ¹⁾ P _{EUT} : EUT output power ²⁾ P _{AC} : Real power flow at S1 in Figure 1. Positive means power from EUT to utility. Nominal is the 0 % test condition value. ³⁾ Q _{AC} : Reactive power flow at S1 in Figure 1. Positive means power from EUT to utility. Nominal is the 0 % test condition value. ⁴⁾ Fundamental of I _{AC} when RLC is adjusted ⁵⁾ BL: Balance condition, IB: Imbalance condition. Condition B: EUT output power P _{EUT} = 50 % – 66 % ⁶⁾ of maximum									

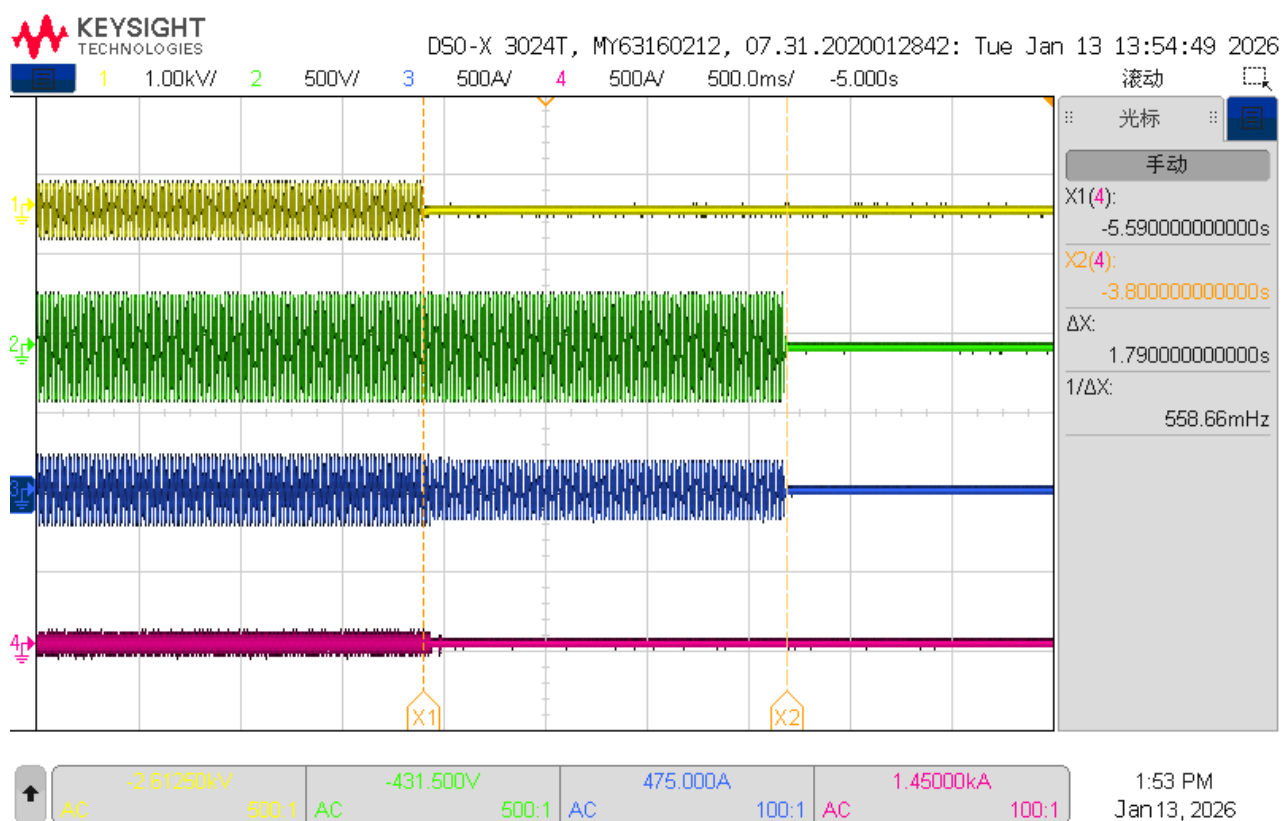
EUT input voltage ⁷⁾ = 50 % of rated input voltage range

⁶⁾ Or minimum allowable EUT output level if greater than 66 %.

⁷⁾ Based on EUT rated input operating range. For example, If range is between X volts and Y volts, 20 % of range = $X + 0.5 \times (Y - X)$. Y shall not exceed $0.8 \times$ EUT maximum system voltage (i.e., maximum allowable array open circuit voltage). In any case, the EUT should not be operated outside of its allowable input voltage range.

Scope pictures of the disconnection time

Disconnection at No. 6



Note:

Yellow: Feedback current flows into the grid signal

Green: EUT output voltage

Blue: EUT output current

Pink: Grid input current

Load imbalance (reactive load) for test condition C (EUT output = 25 % – 33 %)									P
Test conditions			Frequency: 50+/-0.1Hz U _N =230±2.3Vac Distortion factor of chokes < 2% Quality = 1						
Disconnection limit			2s (IEC 62116)						
No.	P _{EUT} ¹⁾ [% of EUT rating]	Reactive load [% of Q _L in 6.1.d] ¹⁾	P _{AC} ^{2) 5)} (% of nominal)	Q _{AC} ^{3) 5)} (% of nominal)	Run on Time [ms]	P _{EUT} Pre-phase [kW]	Q _f	V _{DC} [V]	Remarks ⁴⁾
1.	33	33	0	-5	940	41.25	0.975	696	Test C at IB
2.	33	33	0	-4	1540	41.25	0.980	696	Test C at IB
3.	33	33	0	-3	1250	41.25	0.985	696	Test C at IB
4.	33	33	0	-2	1150	41.25	0.990	696	Test C at IB
5.	33	33	0	-1	1480	41.25	0.995	696	Test C at IB
6.	33	33	0	0	1580	41.25	1.000	696	Test C at BL
7.	33	33	0	1	1320	41.25	1.005	696	Test C at IB
8.	33	33	0	2	1128	41.25	1.010	696	Test C at IB
9.	33	33	0	3	1228	41.25	1.015	696	Test C at IB
10.	33	33	0	4	1360	41.25	1.020	696	Test C at IB
11.	33	33	0	5	916	41.25	1.025	696	Test C at IB
Parameter at 0% per phase L= 6.12 mH R= 1.92 Ω C= 1654.73 μF									
Note: RLC is adjusted to min. +/-1% of the inverter rated output power 1) P _{EUT} : EUT output power 2) P _{AC} : Real power flow at S1 in Figure 1. Positive means power from EUT to utility. Nominal is the 0 % test condition value. 3) Q _{AC} : Reactive power flow at S1 in Figure 1. Positive means power from EUT to utility. Nominal is the 0 % test condition value. 4) Fundamental of I _{AC} when RLC is adjusted 5) BL: Balance condition, IB: Imbalance condition. Condition C: EUT output power P _{EUT} = 25 % – 33 % ⁶⁾ of maximum									

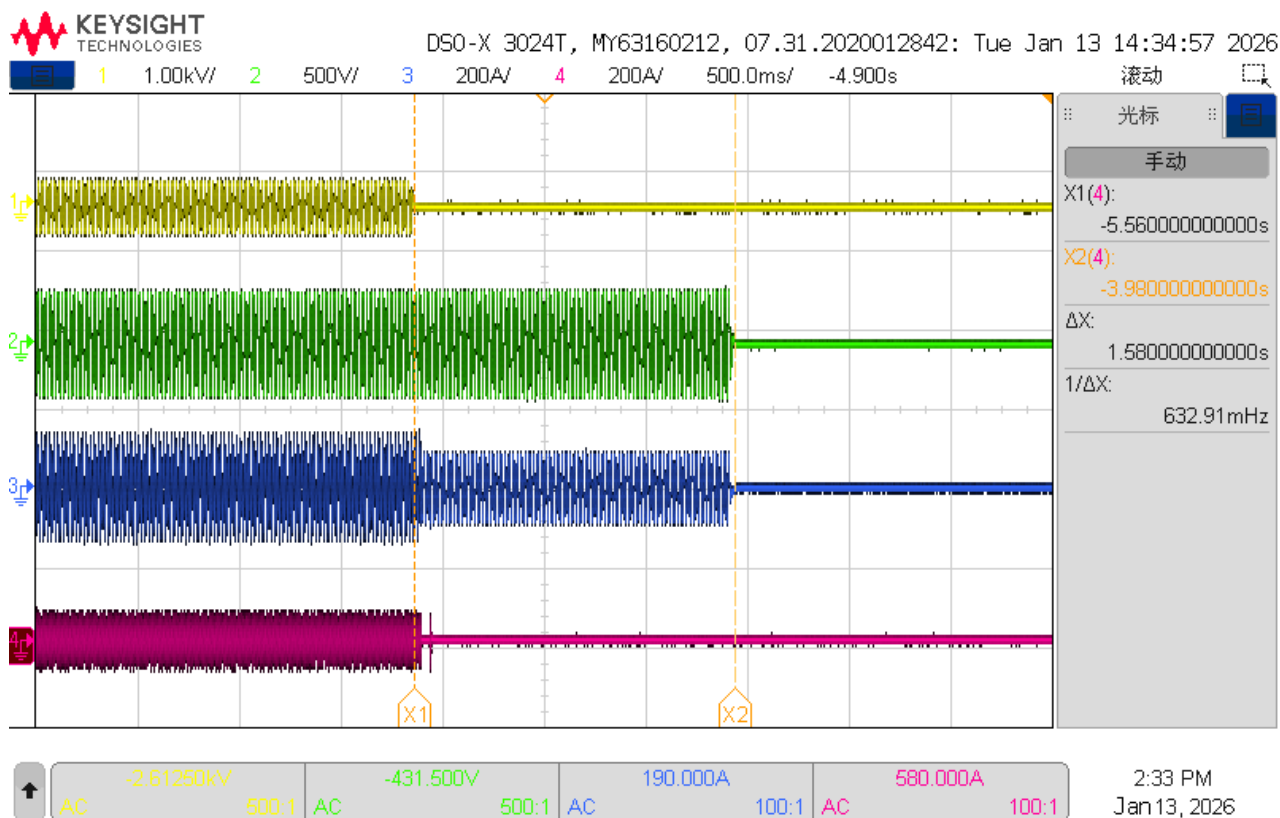
EUT input voltage ⁷⁾ = <20 % of rated input voltage range

⁶⁾ Or minimum allowable EUT output level if greater than 33 %.

⁷⁾ Based on EUT rated input operating range. For example, If range is between X volts and Y volts, 20 % of range = $X + 0.2 \times (Y - X)$. Y shall not exceed $0.8 \times$ EUT maximum system voltage (i.e., maximum allowable array open circuit voltage). In any case, the EUT should not be operated outside of its allowable input voltage range.

Scope pictures of the disconnection time

Disconnection at No. 6



Note:

Yellow: Feedback current flows into the grid signal

Green: EUT output voltage

Blue: EUT output current

Pink: Grid input current

EN 50549-10:2022		
Clause	Test items	Remark
5.9	Connection and starting	
5.9.3	Automatic reconnection after tripping	P
5.9.4	Starting to generate electrical power	P
5.9.7	Synchronisation	P

5.9 Connection and starting			P
5.9.3 Automatic reconnection after tripping			
5.9.4 Starting to generate electrical power			
Setting value	Min. voltage for connected to grid:	207.0V	
	Max. voltage for connected to grid:	253.0V	
	Min. Frequency for connected to grid:	49.50Hz	
	Max. Frequency for connected to grid:	50.10Hz	
	Observation time (≥60s):	60s	
Test:			
	Voltage conditons		
a) Start up for voltage range	<90% Un for twice of observation time	>110% Un for twice of observation time	
Connection:	No connection	No connection	
Limit	No connection allowed		
b) In voltage range at start-up	≥90% Un within twice setting observation time	≤110% Un within twice setting observation time	
Reconnection time [s]	114.4	107.8	
Limit:	Connected after setting observation time (≥60s)		
Gradient:	The maximum occurring active power gradient after connection respectively start generating electrical power is less than the configured maximum active power per minute Max gradient: disable. For recorded gradient see diagram below.		
c) In voltage range after voltage failure	≥90% Un within twice setting observation time	≤110% Un within twice setting observation time	
Reconnection time [s]	115.8	114.6	
Limit:	Reconnection after setting observation time (≥60s)		
Gradient:	For adjustable micro generators the maximum occurring active power gradient after connection respectively start generating electrical power is less than the configured maximum active power per minute Max gradient: 10%PEmax/min. For non or partly adjustable generators the connection after trip of the interface protection is delayed by a randomised value between 1 min and 10 min. For recorded gradient see diagram below.		
	Frequency conditions		
d) Start up for frequency range	<49.50 Hz for twice of setting observation time	>50.10 Hz for twice of setting observation time	
Connection:	No connection	No connection	
Limit	No connection allowed		
e) In frequency range at start-up	≥49.50 Hz within twice setting observation time	≤50.10 Hz within twice setting observation time	
Reconnection time [s]	105.2	107.2	
Limit:	Connected after setting delay time(≥60s)		

Gradient:	The maximum occurring active power gradient after connection respectively start generating electrical power is less than the configured maximum active power per minute Max gradient: disable. For recorded gradient see diagram below.	
f) In frequency range after frequency failure	≥ 49.50 Hz within twice setting observation time	≤ 50.10 Hz within twice setting observation time
Reconnection time [s]	108.8	105.8
Limit:	Reconnection after setting observation time (≥ 60 s)	
Gradient:	For adjustable micro generators the maximum occurring active power gradient after connection respectively start generating electrical power is less than the configured maximum active power per minute Max gradient: 10%P _n /min. For non or partly adjustable generators the connection after trip of the interface protection is delayed by a randomised value between 1 min and 10 min. For recorded gradient see diagram below.	

Test:

Test condition a), b), d) and e): Refer table 4

Test condition c) and f): Refer table 3

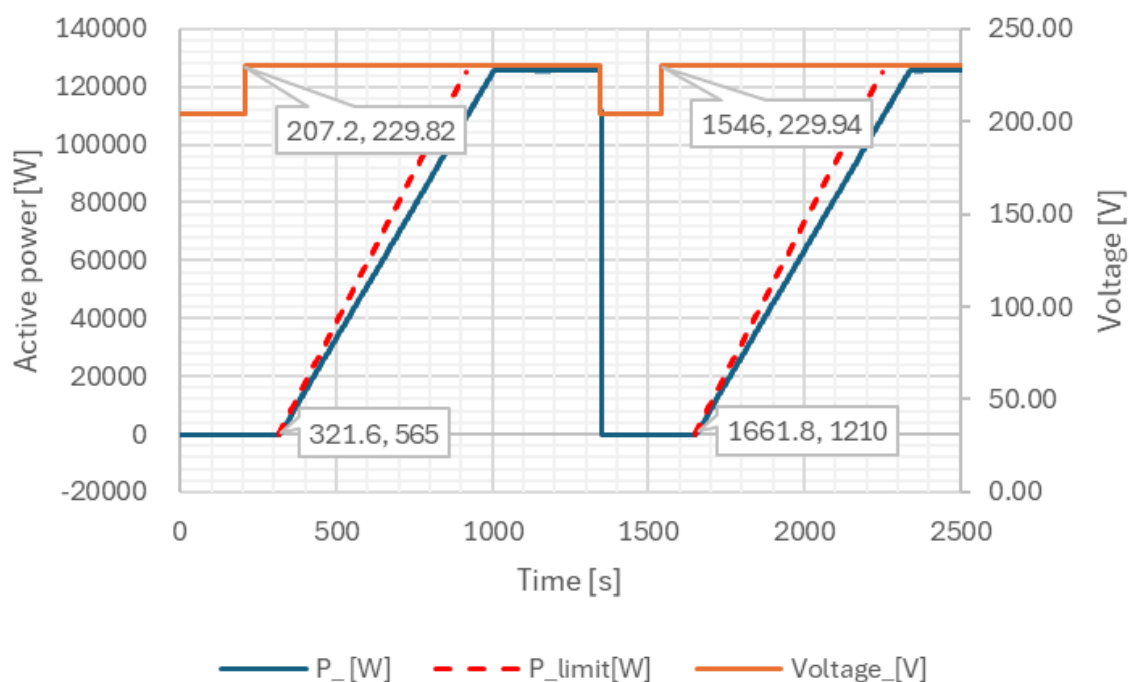
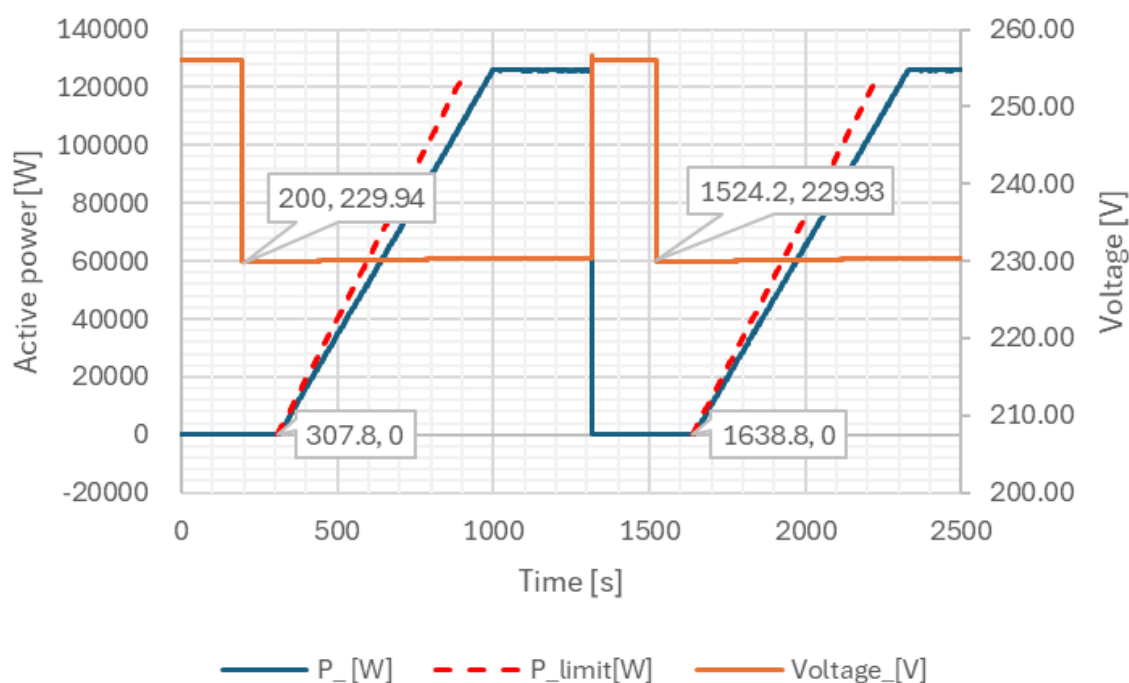
In order to avoid continuous starting and disengaging operations of the interface protection relay, the disengaging value of frequency and voltage functions shall be above 2 % deviating from the operate value.

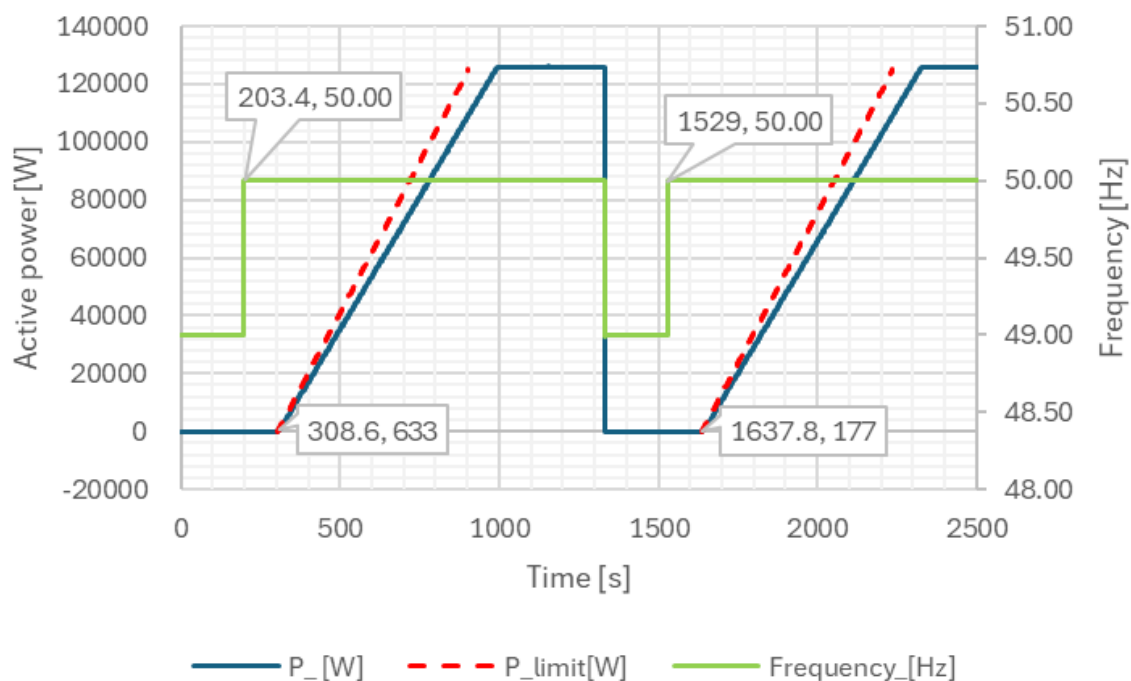
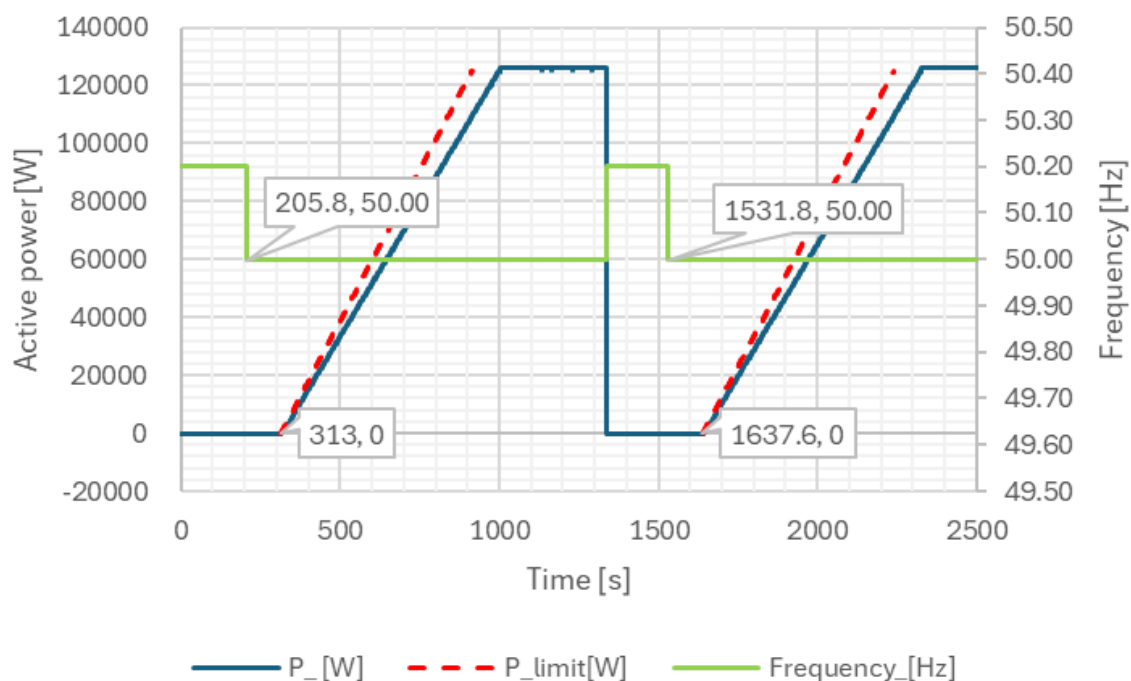
Table 3 – Automatic reconnection after tripping

Parameter	Range	Default setting
Low frequency	47.0-50.0Hz	49.5Hz
High frequency	50.0-52.0Hz	50.2Hz
Low voltage	50%-100%U _n	85%U _n ^a / 90%U _c ^b
High voltage	100%-120%U _n	110%U _n ^a / 110%U _c ^b
Observation	10s-600s	60s
Active power	6%-3000%/minute	10%/minute
a: in case of EN 50549-1:2019		
b: in case of EN 50549-2:2019		

Table 4 – Starting to generate electric power

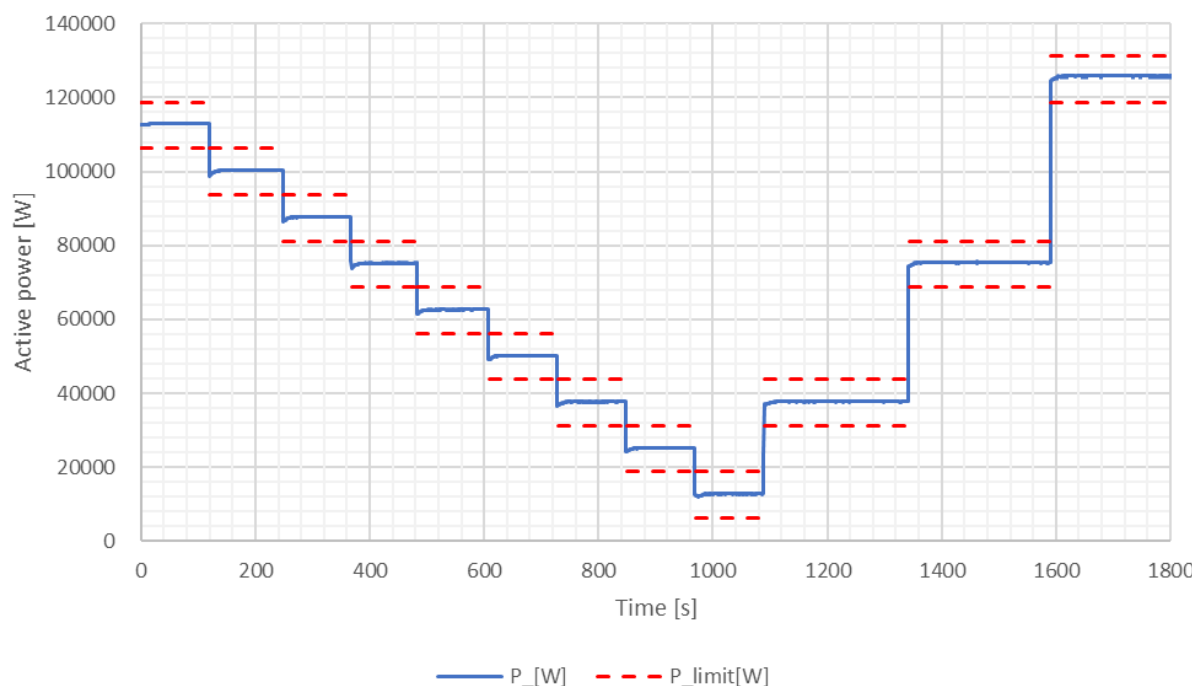
Parameter	Range	Default setting
Low frequency	47.0-50.0Hz	49.5Hz
High frequency	50.0-52.0Hz	50.1Hz
Low voltage	50%-100%U _n	85%U _n ^a / 90%U _c ^b
High voltage	100%-120%U _n	110%U _n ^a / 110%U _c ^b
Observation	10s-600s	60s
Active power	6%-3000%/minute	Disable
a: in case of EN 50549-1:2019		
b: in case of EN 50549-2:2019		

Graph of the gradual power supply: Test 1) for $\geq 90\% U_n$ Graph of the gradual power supply: Test 2) for $\leq 110\% U_n$ 

Graph of the gradual power supply: Test 3) for $\geq 49.50\text{Hz}$ Graph of the gradual power supply: Test 4) for $\leq 50.10\text{Hz}$ 

5.10 Active power reduction on set point						P
5.10.3.2 Verification of the accuracy						
Test:						
Active power step [% of P _n]	Requested set point value		Actual value		Accuracy Actual value-set point	
	W	P/P _n [%]	W	P/P _n [%]	W	P/P _n [%]
90	112500	90.0	113003	90.40	503	0.40
80	100000	80.0	100322	80.26	322	0.26
70	87500	70.0	87677	70.14	177	0.14
60	75000	60.0	75156	60.12	156	0.12
50	62500	50.0	62602	50.08	102	0.08
40	50000	40.0	50111	40.09	111	0.09
30	37500	30.0	37681	30.14	181	0.14
20	25000	20.0	25190	20.15	190	0.15
10	12500	10.0	12752	10.20	252	0.20
Limit ΔP _{E60} /P _{Setpoint} :	± 5 % of P _{E_{max}}					
30	37500	30.0	37751	30.20	251	0.20
60	75000	60.0	75330	60.26	330	0.26
100	125000	100.0	125833	100.67	833	0.67
Limit ΔP _{E60} /P _{Setpoint} :	+ 5 % of P _{E_{max}}					

Graph of active power on set point



— P_[W] - - - P_limit[W]

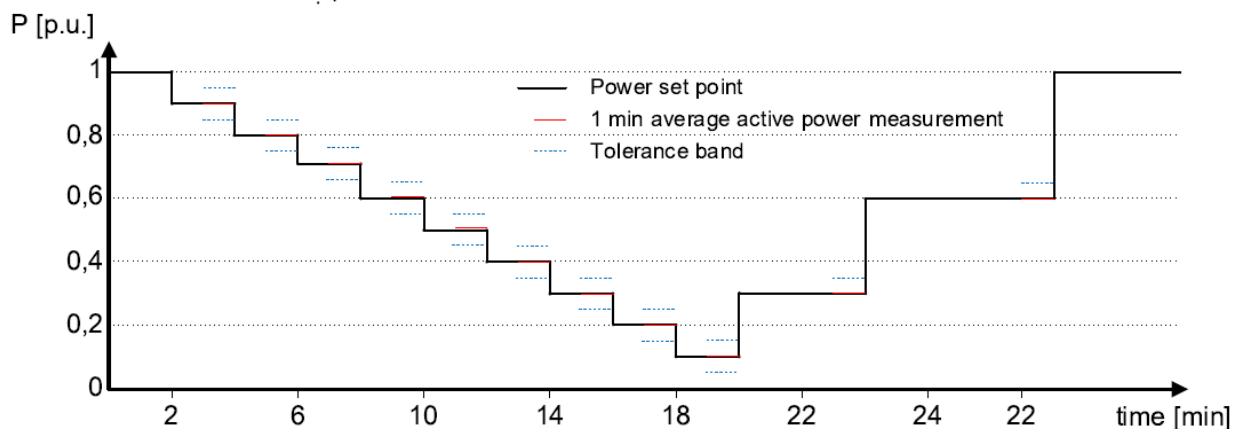
Assessment criterion:

a) for adjustable PGUs:

- no network disconnection
- the active power value does not exceed the setpoint by more than 5% $P_{E_{max}}$
- the setting time determined this way is ≤ 1 min

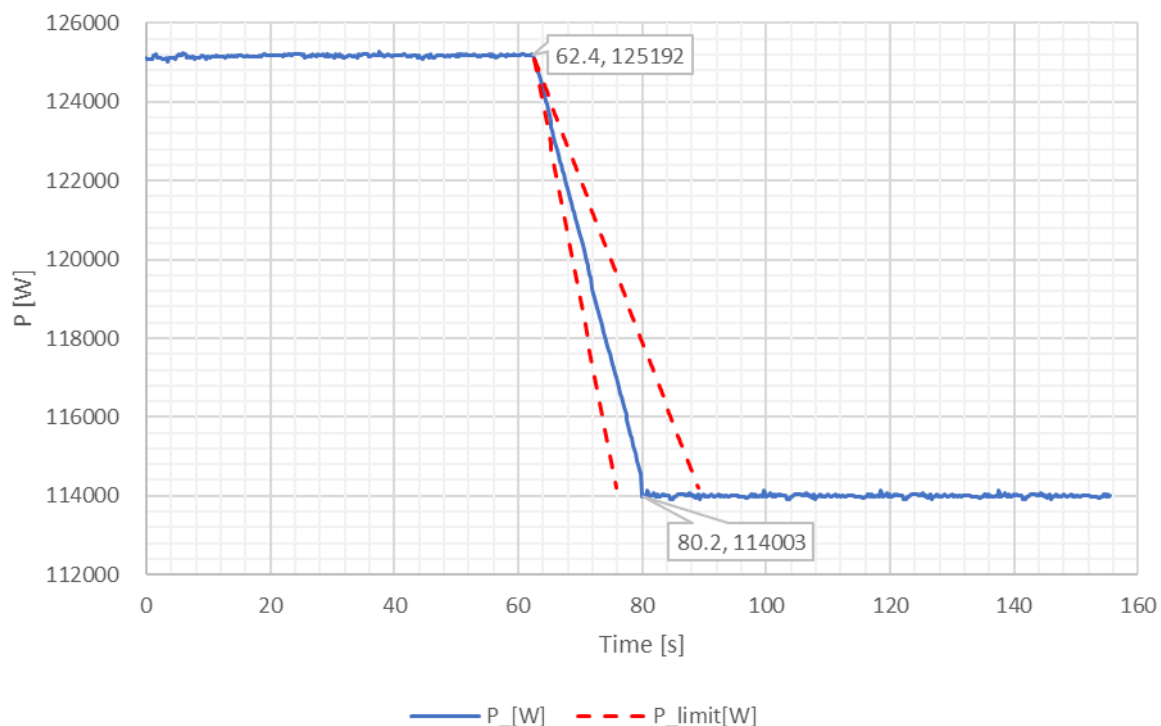
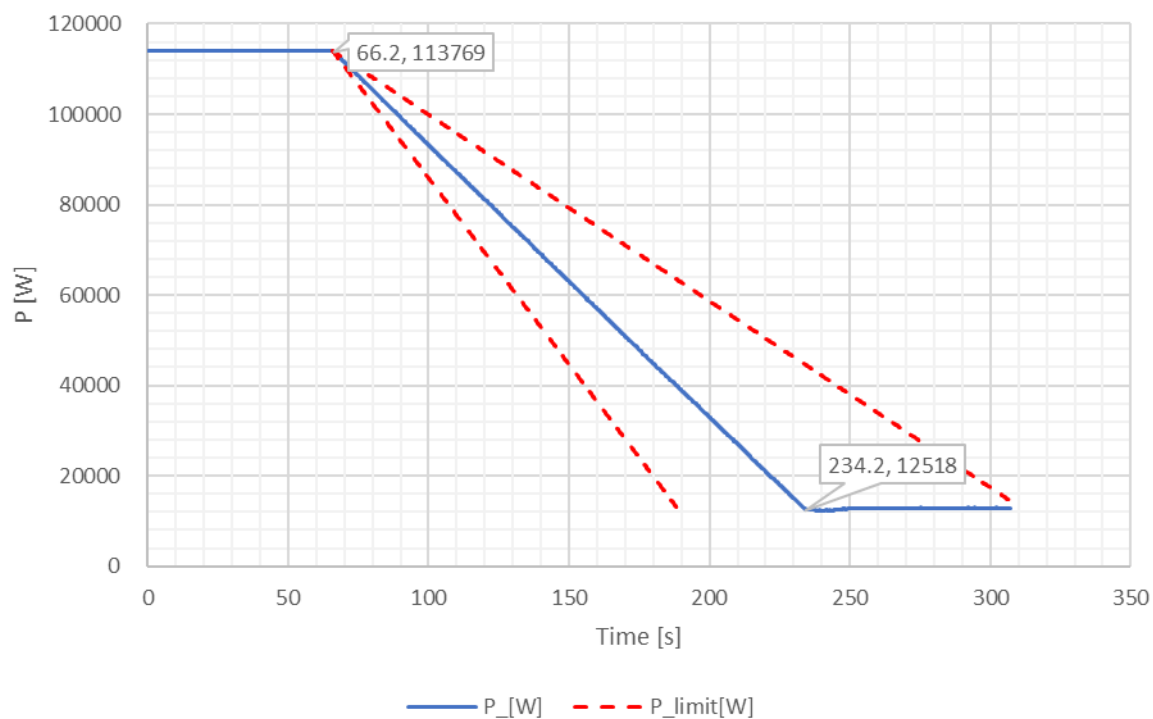
b) For all other PGUs:

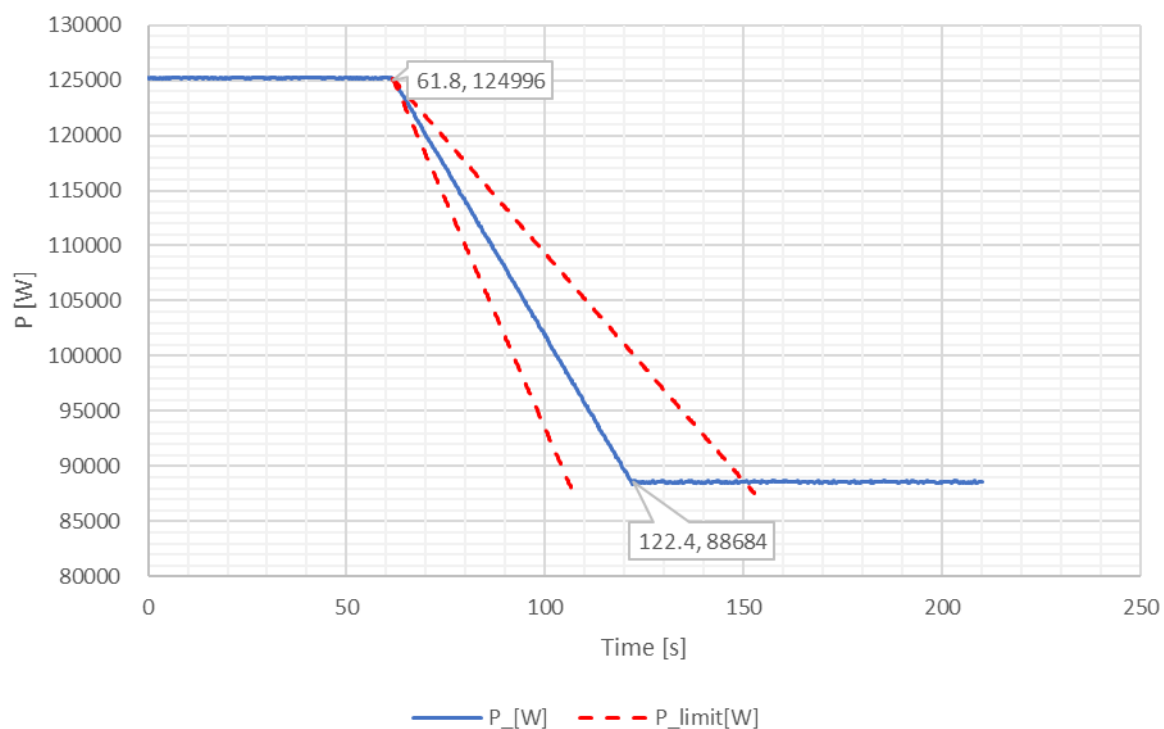
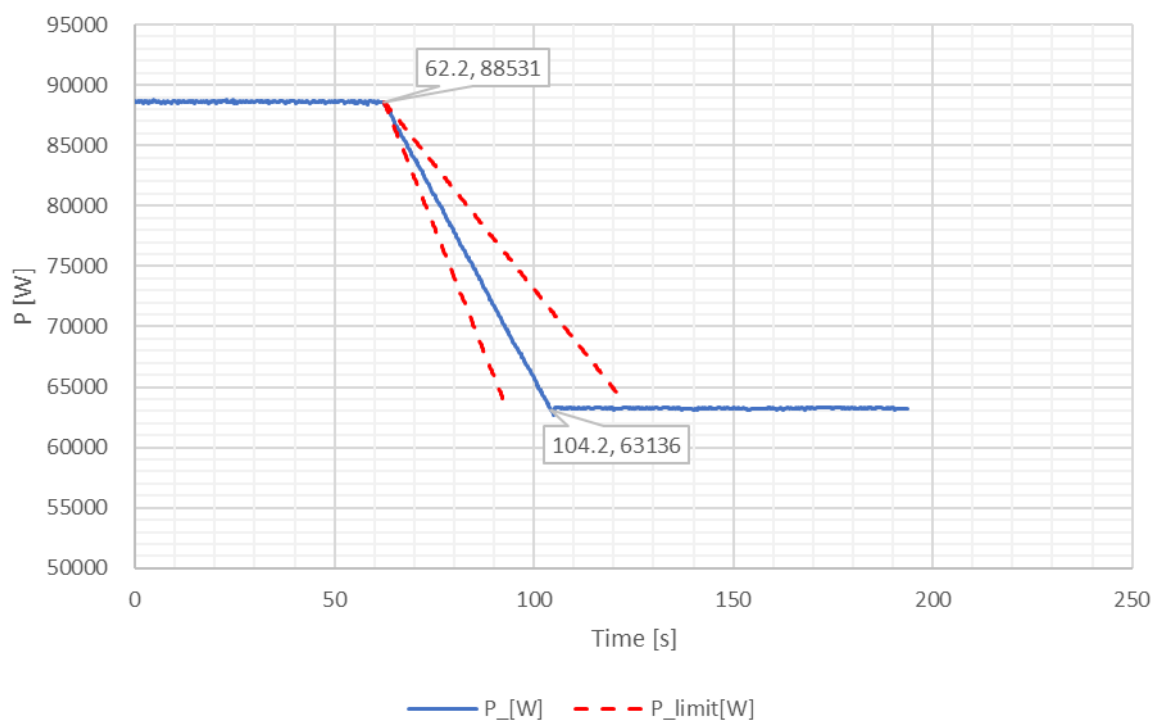
- the active power value does not exceed the setpoint by more than 5% $P_{E_{max}}$ or
- the setpoint is fallen below within 5 minutes or the PGU has switched off

**Figure 46 — Test results graph (example)**

EN 50549-10:2022		
Clause	Test items	Remark
5.10	Active power reduction on set point	
5.10.3.2	Verification of the accuracy	P
5.10.3.3	Test procedure for dynamic behaviour	P
5.11	Remote information exchange	P

5.10 Active power reduction on set point				P
5.10.3.3 Verification procedure for dynamic behaviour				
Test:1				
1-min mean value	100% of P _{E_{max}}	90% of P _{E_{max}}	10% of P _{E_{max}}	
P _{Setpoint} [W]:	125000	112500	12500	
P _{E60} [W]:	125165	113996	12844	
ΔP _{E60} /P _{Setpoint} [%]:	100.13	91.20	10.28	
Limit ΔP _{E60} /P _{Setpoint} :	± 5%of P _{E_{max}}			
Condition	100% of P _{E_{max}} to 90% of P _{E_{max}}		90% of P _{E_{max}} to 10% of P _{E_{max}}	
Measurements ΔP/Δt [%P _{rE} /s]	0.502		0.482	
Limit ΔP/Δt [%P _{rE} /s]	0.33-0.66			
Test:2				
1-min mean value	100% of P _{E_{max}}	70% of P _{E_{max}}	50% of P _{E_{max}}	
P _{Setpoint} [W]:	125000	87500	62500	
P _{E60} [W]:	125192	88584	63235	
ΔP _{E60} /P _{Setpoint} [%]:	100.15	70.87	50.59	
Limit ΔP _{E60} /P _{Setpoint} :	± 5%of P _{E_{max}}			
Condition	100% of P _{E_{max}} to 70% of P _{E_{max}}		90% of P _{E_{max}} to 50% of P _{E_{max}}	
Measurements ΔP/Δt [%P _{rE} /s]	0.483		0.484	
Limit ΔP/Δt [%P _{rE} /s]	0.33-0.66			
Test method:				
The test shall be carried out starting from an initial condition with the generating unit running at 0.9 p.u. active power and applying a step of the active power set point from 0.9 p.u. down to 0.1 p.u. of nominal power or minimum regulating power, whatever is the higher.				
If this is not compatible with the specific test conditions, active power set point shall be stepped from 0.7 p.u. down to 0.5 p.u. of nominal power.				
The step response behaviour is measured starting from the instance the set point change is transmitted to the EUT. For the step response time and the settling time a tolerance band of ± 5 % P _{E_{max}} , to the reactive power set point is considered.				

100% of P_{Emax} to 90% of P_{Emax} 90% of P_{Emax} to 10% of P_{Emax} 

100% of P_{Emax} to 70% of P_{Emax} 70% of P_{Emax} to 50% of P_{Emax} 

EN 50549-10:2022		
Clause	Test items	Remark
5.12	Requirements regarding single fault tolerance of interface protection system and interface switch	
5.12.2	Single fault tolerance of the interface protection system	P
5.12.3	Single fault tolerance of the interface switch	P

5.12 Requirements regarding single fault tolerance of interface protection system and interface switch							P
			test voltage (V)	See below			—
			Ambient temperature (°C)	25°C, if not stated otherwise			
No	Component No.	fault	test voltage (V)	test time	fuse No.	fuse current (A)	result
M0, sampling board							
1.	DC sampling circuit, R104	O-C	800Vdc 400Vac	10min	-	-	The fault applied during operation, after applied the fault, the unit protected and software alarm code: 7 (battery low voltage). No hazard, can resettable.
2.	DC sampling circuit, R104	S-C	800Vdc 400Vac	10min	-	-	The fault applied during operation, after applied the fault, the unit protected and software alarm code: 9 (battery over voltage). No hazard, can resettable.
3.	Bus sampling circuit, R156	S-C	800Vdc 400Vac	10min	-	-	The fault applied during operation, after applied the fault, the unit protected and software alarm code: 4 & 6 (Bus over voltage and Bus unbalance). No hazard, can resettable.
4.	AC sampling circuit, R691	S-C	800Vdc 400Vac	10min	-	-	The fault applied during operation, after applied the fault, the unit operated normally. No hazard.
U1, Chip board							
5.	Power supply of U2 Chip, J1, Pin 37 & 45	S-C	800Vdc 400Vac	10min	-	-	The fault applied during operation, after applied the fault, the unit shut down. No hazard, can resettable.
6.	Power supply of U3 Chip, J1, Pin 37 & 45	S-C	800Vdc 400Vac	10min	-	-	The fault applied during operation, after applied the fault, the unit shut down. No hazard, can resettable.

M5, BUS balance Board							
7.	Current Sensor, T1, Pin 1 and 2	S-C	800Vdc 400Vac	10min	-	-	The fault applied during operation, after applied the fault, the unit operated normally. No hazard.
M1, convert power board							
8.	Driver circuit of IGBT, R319	S-C	800Vdc 400Vac	10min	-	-	The fault applied during operation, after applied the fault, the unit operated normally. No hazard.
9.	Bus Capacitor, C27	S-C	800Vdc 400Vac	10min	-	-	The fault applied during operation, after applied the fault, the capacitor C27 broken, and unit shut down. No hazard, damaged and cannot resettable
10.	Power supply $\pm 15V$ of Current Sensor, J28	S-C	800Vdc 400Vac	10min	-	-	The fault applied during operation, after applied the fault, the unit shut down. No hazard, can resettable.
M2, AC/DC Power board							
11.	AC Relay RLY1, signal pin	S-C	800Vdc 400Vac	10min	-	-	The fault applied before operation, after applied the fault, the software alarm code: 5 (Bus under voltage) and cannot connect to grid. No hazard, can resettable.
12.	AC Relay RLY2, signal pin	S-C	800Vdc 400Vac	10min	-	-	The fault applied before operation, after applied the fault, the software alarm code: 5 (Bus under voltage) and cannot connect to grid. No hazard, can resettable.
13.	AC Relay RLY3, signal pin	S-C	800Vdc 400Vac	10min	-	-	The fault applied before operation, after applied the fault, the software alarm code: 5 (Bus under voltage) and cannot connect to grid. No hazard, can resettable.

14.	AC Relay RLY5, signal pin	S-C	800Vdc 400Vac	10min	-	-	The fault applied before operation, after applied the fault, the software alarm code: 5 (Bus under voltage) and cannot connect to grid. No hazard, can resettable.
15.	AC Relay RLY7, signal pin	S-C	800Vdc 400Vac	10min	-	-	The fault applied before operation, after applied the fault, the software alarm code: 5 (Bus under voltage) and cannot connect to grid. No hazard, can resettable.
M4, auxiliary power board							
16.	T1, pin 11 and 14, 24 power source	S-C	800Vdc 400Vac	10min	-	-	The fault applied during operation, after applied the fault, the unit operated normally. No hazard.
17.	T2, pin 9 and 10, driver circuit for Mosfet	S-C	800Vdc 400Vac	10min	-	-	The fault applied during operation, after applied the fault, the unit shut down. No hazard, can resettable.
18.	T4, pin 13 and 14, 15 power source	S-C	800Vdc 400Vac	10min	-	-	The fault applied during operation, after applied the fault, the unit shut down, no software alarm, LED alarm flash. No hazard, can resettable.
19.	AC Relay RLY16, signal pin	S-C	800Vdc 400Vac	10min	-	-	The fault applied during operation, after applied the fault, the unit protected and software alarm code: 8 (AC relay fault) and cannot connect to grid. No hazard, can resettable.
20.	DC contactor RLY17, signal pin	S-C	800Vdc 400Vac	10min	-	-	The fault applied during operation, after applied the fault, the unit protected and software alarm code: 6 (Positive and negative bus unbalanced fault). No hazard, can resettable.
21.	DC contactor on enclosure, signal pin	S-C	800Vdc 400Vac	10min	-	-	The fault applied during operation, after applied the fault, the unit protected and software alarm code: 6 (Positive and negative bus unbalanced fault). No hazard, can resettable.
Cooling							
22.	Fan	Blocked	800Vdc 400Vac	30min	-	-	The fault applied during operation, after applied the fault, the unit operated for 1minute, then software alarm code: 1

							(Fan fault of hardware). No hazard, can resettable.
23.	Fan driver circuit, M1 board, J24, pin 1 and 2	S-C	800Vdc 400Vac	10min	-	-	The fault applied during operation, after applied the fault, the unit operated for 1minute, then software alarm code: 1 (Fan fault of hardware). No hazard, can resettable.
General input and output							
24.	AC terminal (3 phase)	S-C	800Vdc 400Vac	10min	-	-	The fault applied during operation, after applied the fault, the unit protected and software alarm code: 0 & 1 (Phase A over voltage, Phase B over voltage). No hazard, can resettable.
25.	AC terminal – Phase A & Phase B	MIS	800Vdc 400Vac	10min	-	-	The fault applied during operation, after applied the fault, the unit protected and software alarm code: 8 (Grid phase wrong). No hazard, can resettable.
26.	AC terminal – Phase C	LP	800Vdc 400Vac	10min	-	-	The fault applied during operation, after applied the fault, the unit protected and software alarm code: 1 & 2 (Phase B over voltage, Phase C overvoltage). No hazard, can resettable.
27.	DC terminal	S-C	800Vdc 400Vac	10min	-	-	The fault applied before operation, after applied the fault, the software alarm code: 0 (pre-charge over time for Capacitor) and cannot connect to grid. No hazard, can resettable.
28.	DC terminal	RE	800Vdc 400Vac	10min	-	-	The fault applied before operation, after applied the fault, the software alarm code: 7 & 15 (Battery under voltage, Battery reverse) and cannot connect to grid. No hazard, can resettable.
29.	120% AC output	OL	800Vdc 400Vac	10min	-	-	The fault applied during operation, after applied the fault, the unit keep operated at 110%Pn. No hazard, can resettable.
30.	PE conductor	MIS S	800Vdc 400Vac	10min	-	-	Fault applied before operation, the unit can start up normally. No damage, no hazard.
Legend							

S-C	short-circuited	O-C	open-circuited
OL	Over-load.	MIS	Miss wiring
RE	Reverse polarity	LP	Loss of phase
Note(s): The electric strength test performed after fault condition test and see appended table 7.5.2 for detailed test conditions.			

5.12 Requirements regarding single fault tolerance of interface protection system and interface switch							P
No.	component No.	fault	test voltage (V)	test time	fuse No.	fuse current (A)	result
a.	Error of an AD converter or measuring card for voltage measurement		DC:850V AC:230V	10s	--	--	The unit disconnect within 10 s, no damage, no fire, no hazard
b.	malfunction or freezing of a microprocessor or PLC		DC:850V AC:230V	10s	--	--	The unit disconnect within 10 s, no damage, no fire, no hazard
c.	Merging or jamming of the contacts of the switching output		DC:850V AC:230V	10s	--	--	The unit disconnect within 10 s, no damage, no fire, no hazard
d.	overvoltage of the supply voltage		DC:850V AC:230V	10s	--	--	The unit disconnect within 10 s, no damage, no fire, no hazard
e.	Cable break in connecting cables between the measuring input and the control output to the coupling switch		DC:850V AC:230V	10s	--	--	The unit disconnect within 10 s, no damage, no fire, no hazard
f.	Failure of the supply voltage (auxiliary voltage)		DC:850V AC:230V	10s	--	--	The unit disconnect within 10 s, no damage, no fire, no hazard

Pictures of the unit



Figure 1. Front view
(for model TAA-105KW-01, TAA-125KW-01)

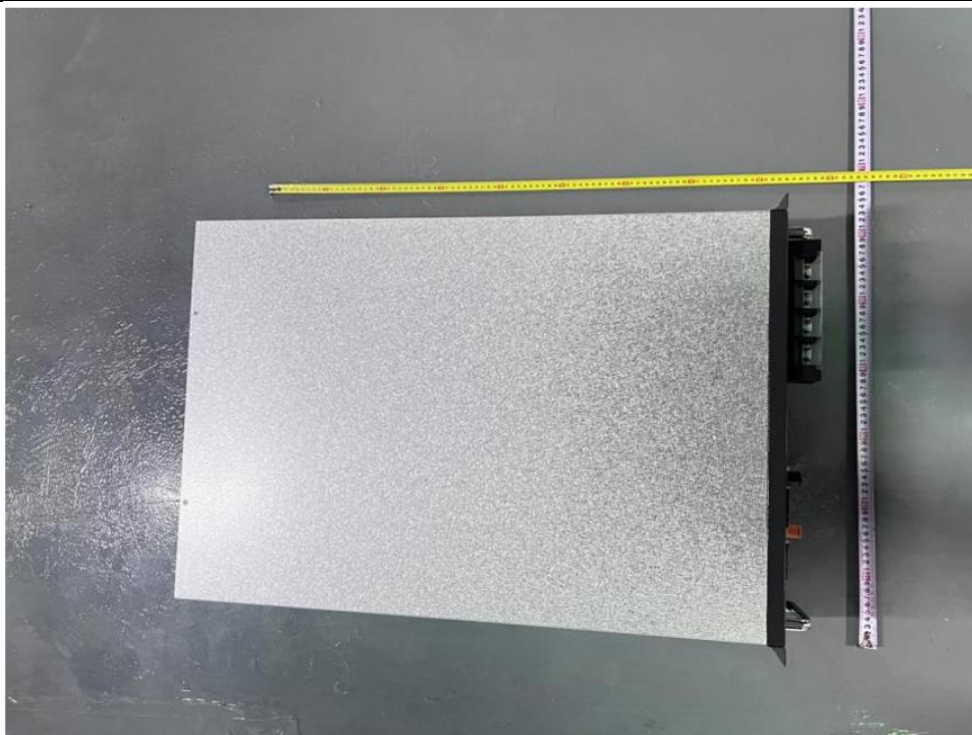


Figure 2. Top view
(for model TAA-105KW-01, TAA-125KW-01)

Pictures of the unit

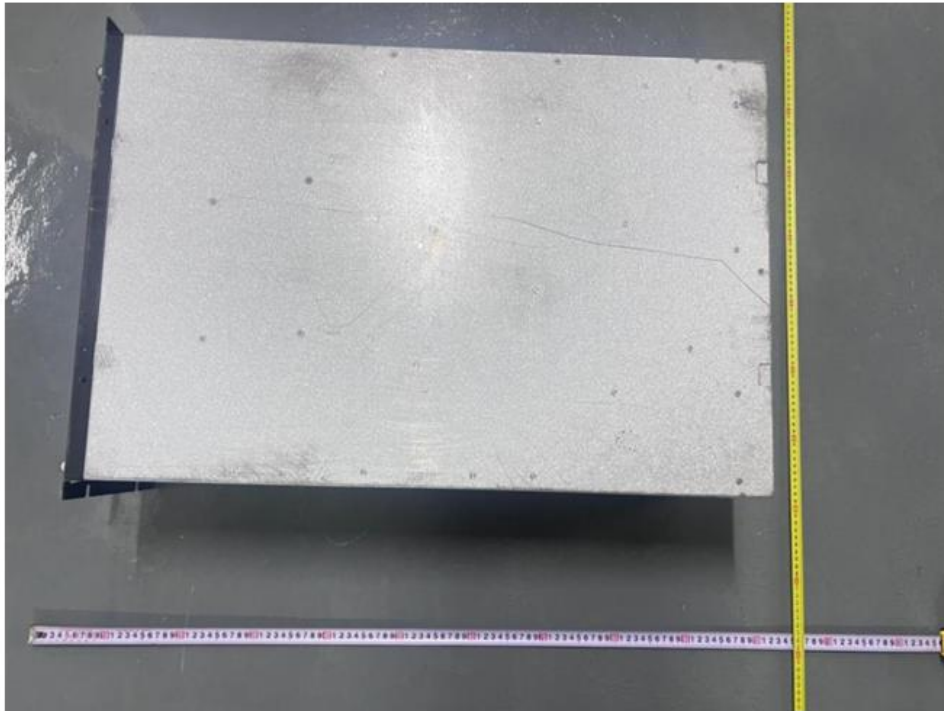


Figure 3. Bottom view
(for model TAA-105KW-01, TAA-125KW-01)

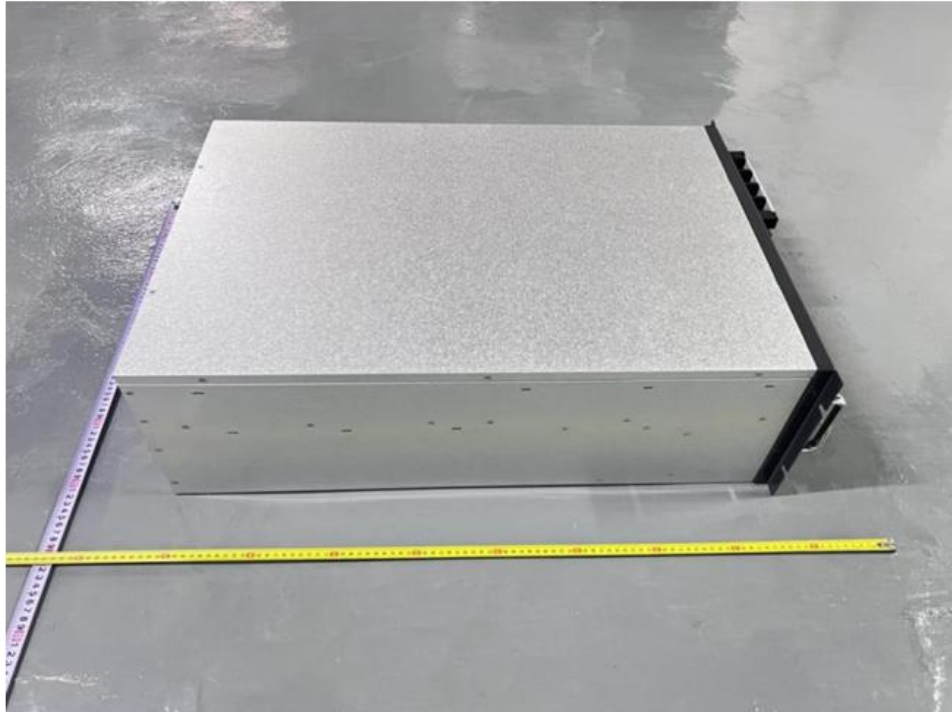


Figure 4. Right view
(for model TAA-105KW-01, TAA-125KW-01)

Pictures of the unit

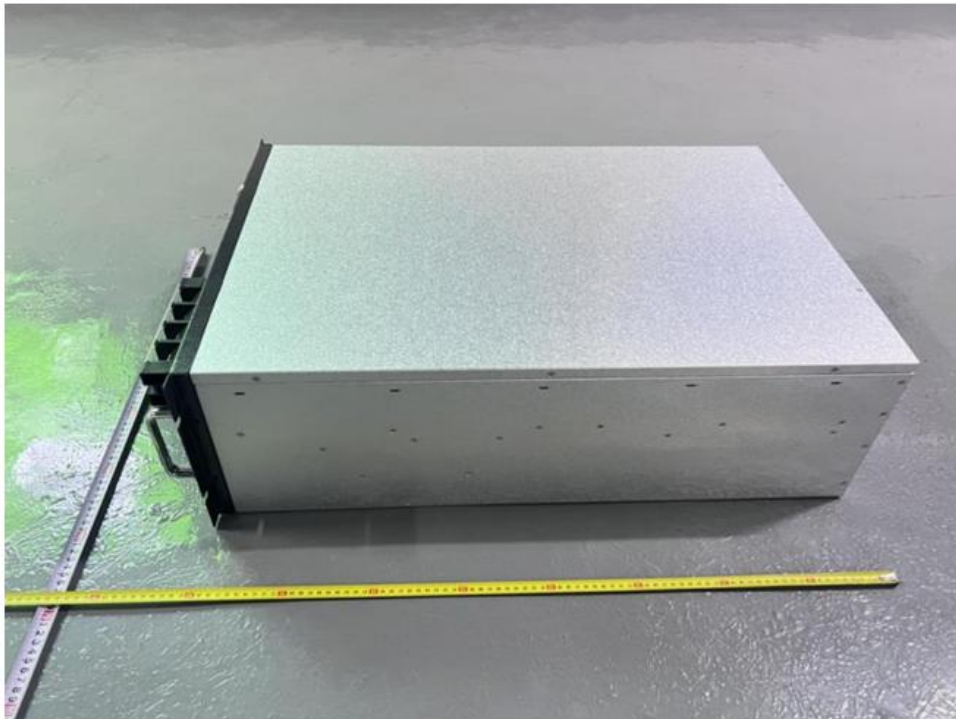


Figure 5. Left view
(for model TAA-105KW-01, TAA-125KW-01)

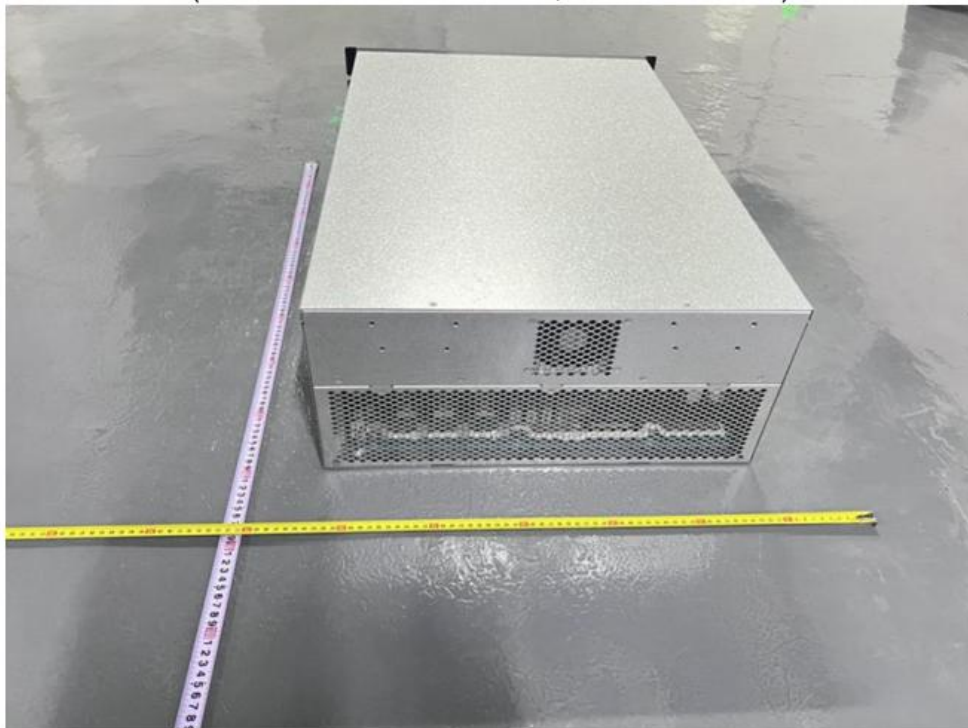


Figure 6. Back view
(for model TAA-105KW-01, TAA-125KW-01)

Pictures of the unit



Figure 7. Front view
(for model TAA-105KW-11, TAA-125KW-11)

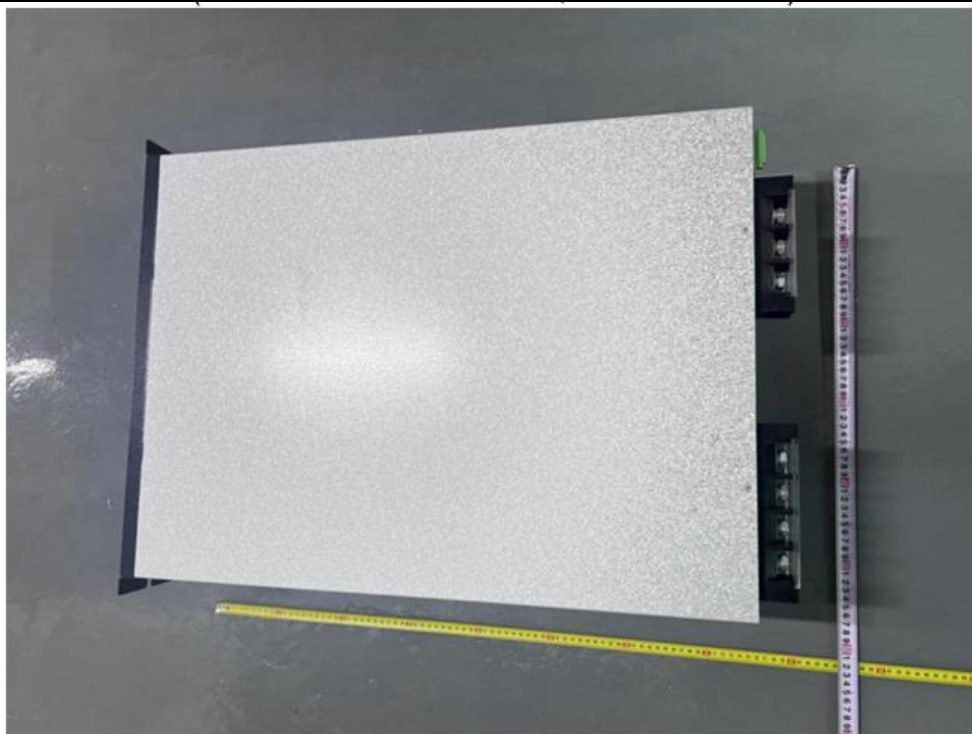


Figure 8. Top view
(for model TAA-105KW-11, TAA-125KW-11)

Pictures of the unit

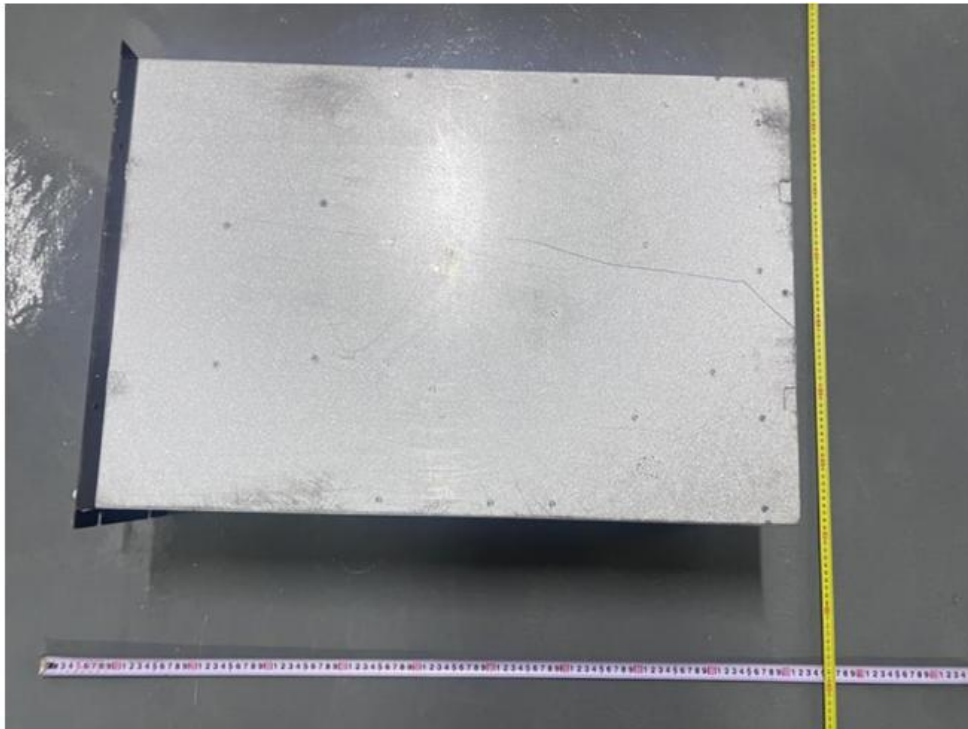


Figure 9. Bottom view
(for model TAA-105KW-11, TAA-125KW-11)

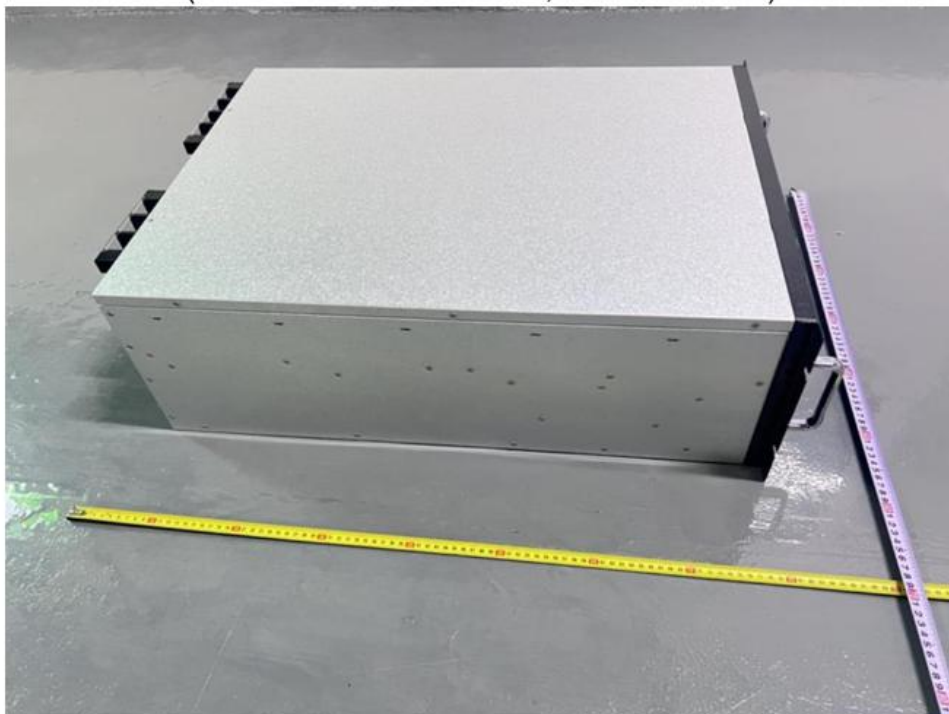


Figure 10. Right view
(for model TAA-105KW-11, TAA-125KW-11)

Pictures of the unit

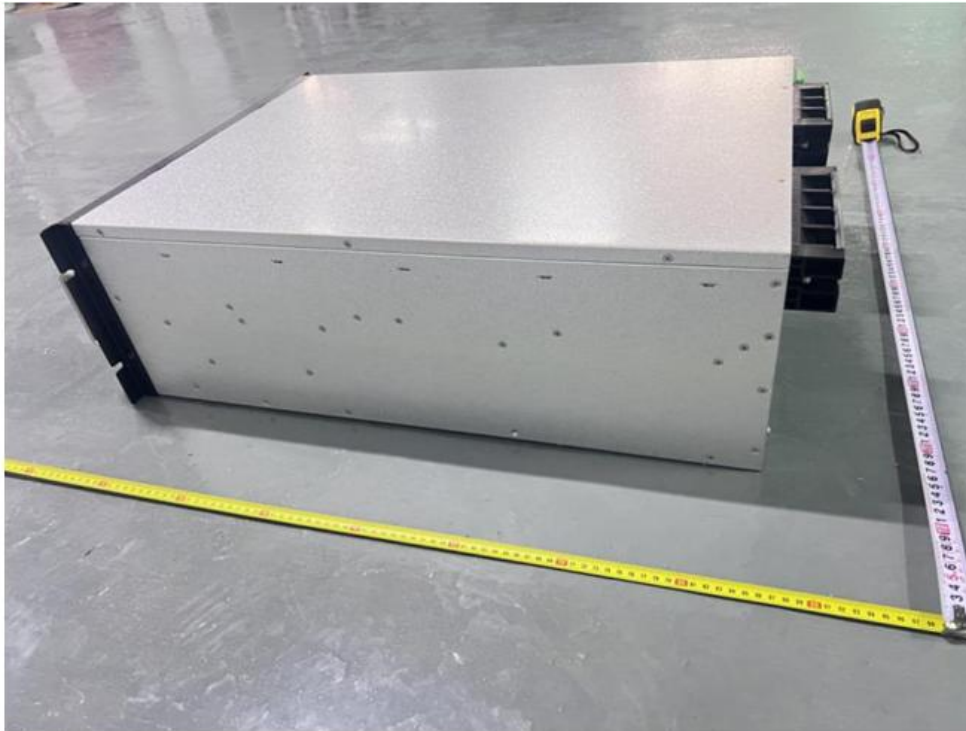


Figure 11. Left view
(for model TAA-105KW-11, TAA-125KW-11)

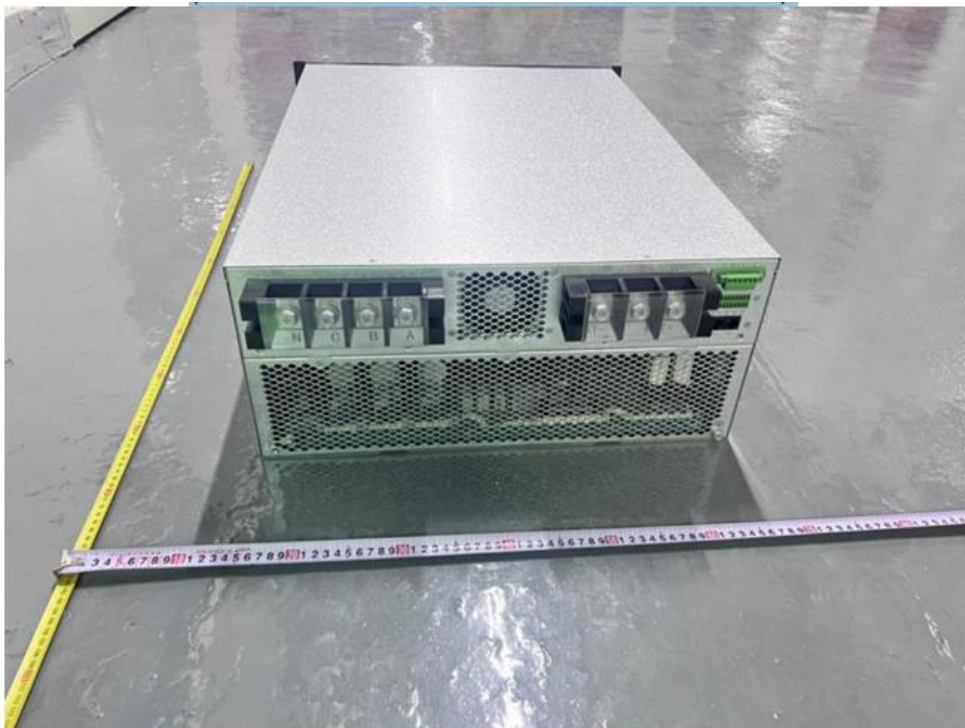


Figure 12. Back view
(for model TAA-105KW-11, TAA-125KW-11)

Pictures of the unit

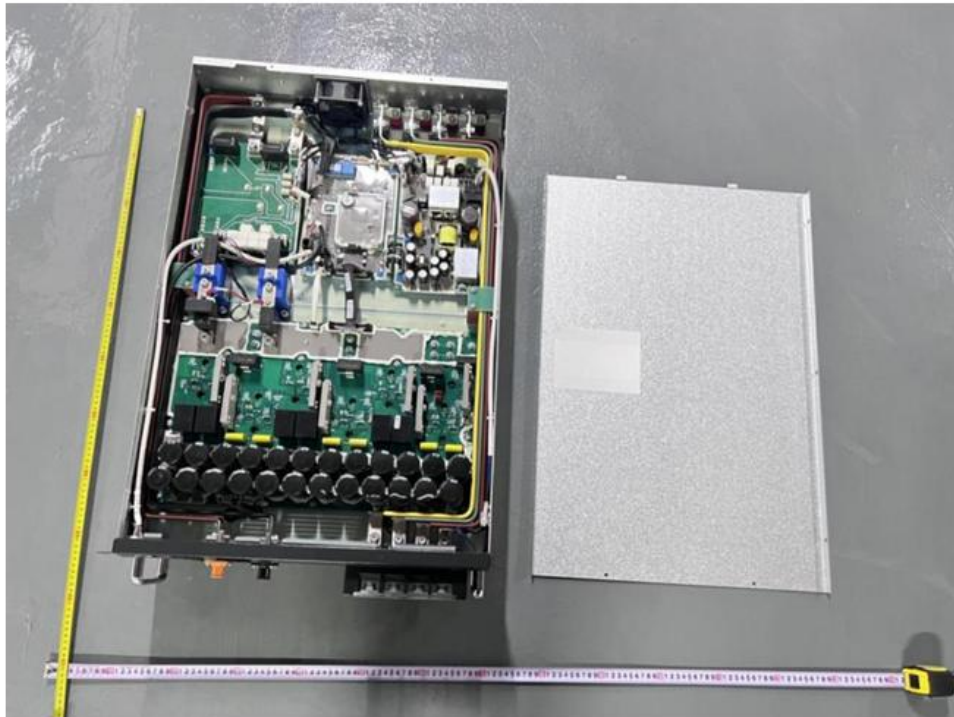


Figure 13. Opening view 1
(for model TAA-105KW-01, TAA-125KW-01)

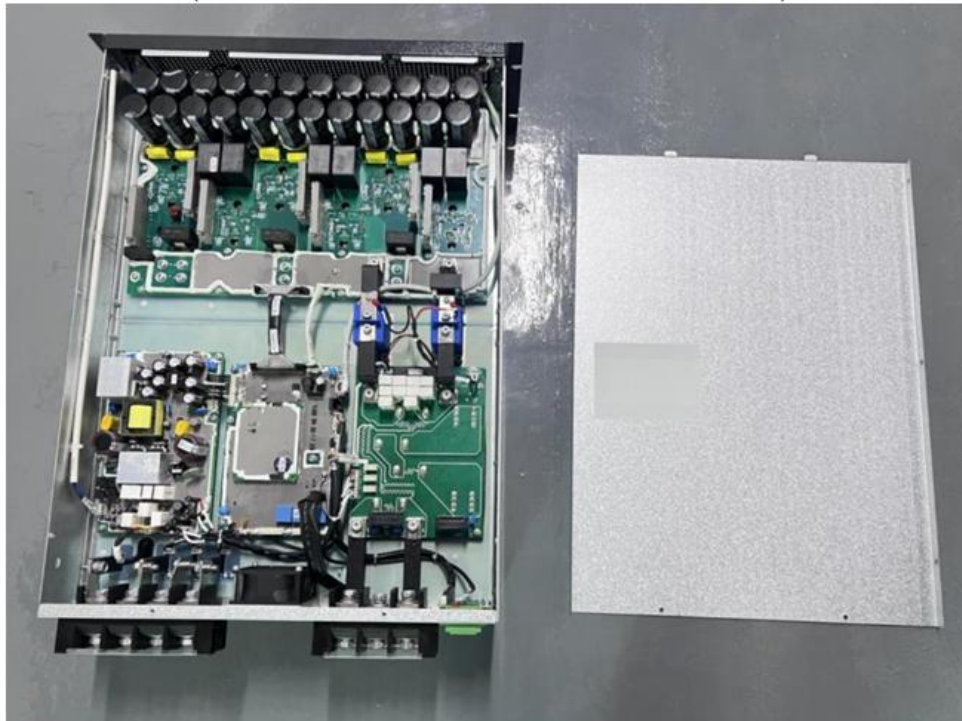


Figure 14. Opening view 2
(for model TAA-105KW-11, TAA-125KW-11)

Test Equipment list				
No	Test Equipment	Equipment model	Equipment No.	Calibration due date
1	AC source	WPLA-330200KVA	BZ-DGD-L204	2026/09/01
2	DC source	WPLA-150KW	BZ-DGD-L013	2026/07/29
3	RLC load	ACLT-38160H	BZ-DGD-L003	2026/09/01
4	Power analyzer	DEWE2-PA7	BZ-DGD-L119	2026/07/31
5	Current sensor	CT6863-05	BZ-DGD-L026-1	2027/01/11
6	Current sensor	CT6863-05	BZ-DGD-L026-2	2027/01/11
7	Current sensor	CT6863-05	BZ-DGD-L026-3	2027/01/11
8	Current sensor	CT6863-05	BZ-DGD-L026-4	2027/01/11
9	Oscilloscope	MS04054B	BZ-DGD-L064	2027/01/12

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